

Warning, configure PB0 and PB1 as open drain output with external pullup

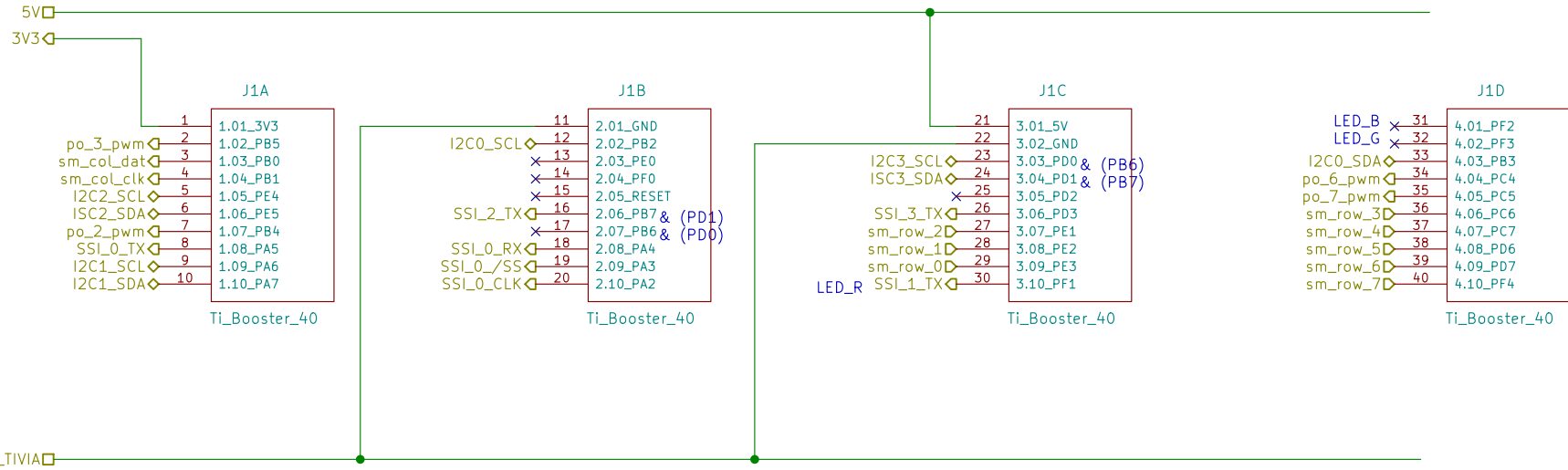


Table 2-3. J1 Connector<sup>(1)</sup>

J1 Pin	GPIO	Analog Function GPIO AMSEL	On-board Function	Tiva C Series MCU Pin	GPIOCTL Register Setting												
					1	2	3	4	5	6	7	8	9	10	11	12	13
1.01	PB5	AN12	5V	SSOPIN													
1.02	PB0	USBDIO	40	USBDIO													
1.03	PB1	USBDIO	40	USBDIO													
1.04	PB1	USBDIO	40	USBDIO													
1.05	PE4	AN8	58	USBDIO													
1.06	PE5	AN8	60	USBDIO													
1.07	PB4	AN10	58	SSOPIN													
1.08	PA5	AN10	22	SSOPIN													
1.09	PA6	AN10	22	SSOPIN													
1.10	PA7	AN10	22	SSOPIN													

<sup>(1)</sup> Shaded cells indicate configuration for compatibility with the MSP430 LaunchPad.

Table 2-5. J3 Connector<sup>(1)</sup>

J3 Pin	GPIO	Analog Function GPIO AMSEL	On-board Function	Tiva C Series MCU Pin	GPIOCTL Register Setting												
					1	2	3	4	5	6	7	8	9	10	11	12	13
3.01	PD0	AN1	61	SSOPIN													
3.02	PD1	AN1	61	SSOPIN													
3.03	PD2	AN1	61	SSOPIN													
3.04	PD3	AN1	61	SSOPIN													
3.05	PD4	AN1	61	SSOPIN													
3.06	PD5	AN1	61	SSOPIN													
3.07	PD6	AN1	61	SSOPIN													
3.08	PD7	AN1	61	SSOPIN													
3.09	PD8	AN1	61	SSOPIN													
3.10	PD9	AN1	61	SSOPIN													

<sup>(1)</sup> Shaded cells indicate configuration for compatibility with the MSP430 LaunchPad.

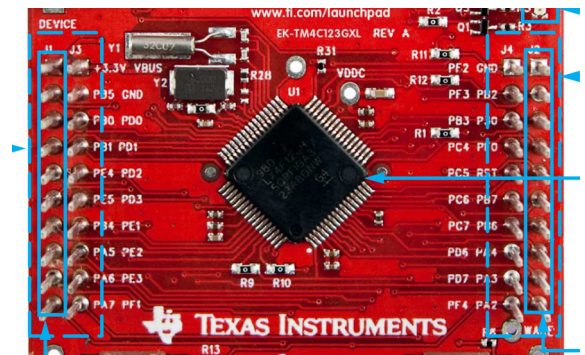
Table 2-4. J2 Connector<sup>(1)</sup>

J2 Pin	GPIO	Analog Function GPIO AMSEL	On-board Function	Tiva C Series MCU Pin	GPIOCTL Register Setting												
					1	2	3	4	5	6	7	8	9	10	11	12	13
2.01	PB2	AN3	47	SSOPIN													
2.02	PB3	AN3	9	USBDIO													
2.03	PB4	AN3	28	USBDIO													
2.04	PB5	AN3	28	USBDIO													
2.05	PB6	AN3	28	USBDIO													
2.06	PB7	AN3	28	USBDIO													
2.07	PB8	AN3	28	USBDIO													
2.08	PB9	AN3	28	USBDIO													
2.09	PB10	AN3	28	USBDIO													
2.10	PB11	AN3	28	USBDIO													

<sup>(1)</sup> Shaded cells indicate configuration for compatibility with the MSP430 LaunchPad.

Table 2-6. J4 Connector

J4 Pin	GPIO	Analog Function GPIO AMSEL	On-board Function	Tiva C Series MCU Pin	GPIOCTL Register Setting												
					1	2	3	4	5	6	7	8	9	10	11	12	13
4.01	PF2	Blue LED (R13)	30	SSOPIN													
4.02	PF3	Green LED (R13)	31	SSOPIN													
4.03	PF4	Red LED (R13)	32	SSOPIN													
4.04	PF5	Blue LED (R13)	33	SSOPIN													
4.05	PF6	Green LED (R13)	34	SSOPIN													
4.06	PF7	Red LED (R13)	35	SSOPIN													
4.07	PF8	Blue LED (R13)	36	SSOPIN													
4.08	PF9	Green LED (R13)	37	SSOPIN													
4.09	PF10	Red LED (R13)	38	SSOPIN													
4.10	PF11	Blue LED (R13)	39	SSOPIN													



\* Remove R10 on the launchpad evaluation board to avoid 2 GPIO pins connected in parallel on 2.06 and 3.04

po\_{0-3}\_pwm = MOPWM{0-3}

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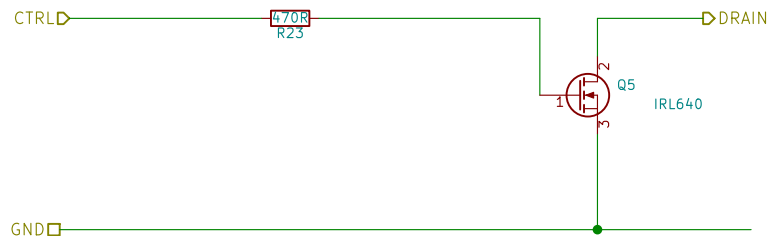
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KiCad E.D.A. eeschema 4.0.1-2.201512121406+619538ubuntu14.04.1-stable2/14



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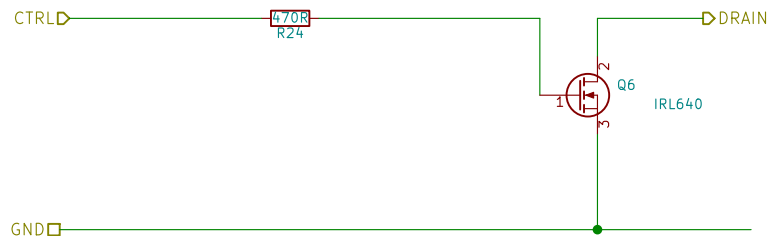
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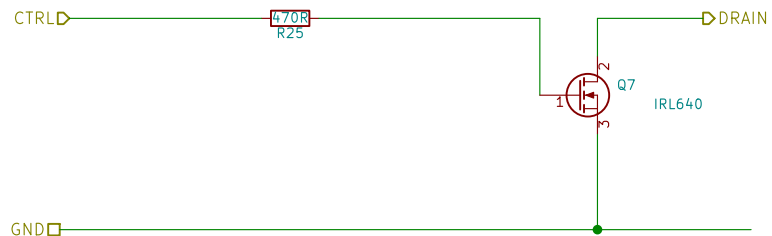
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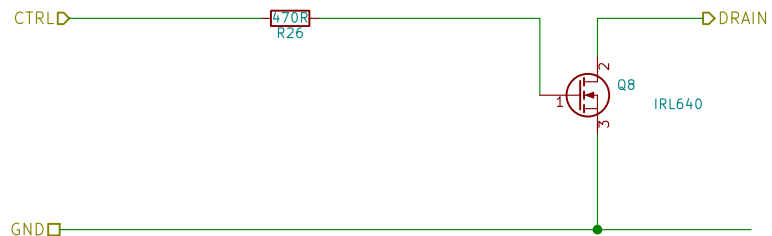
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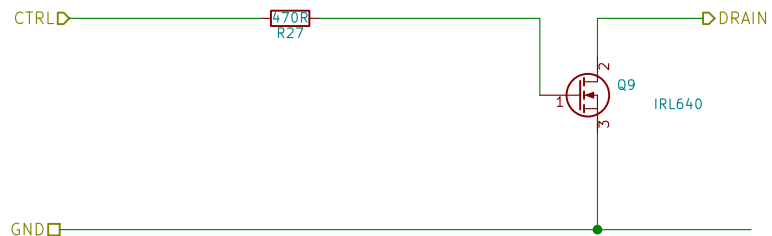
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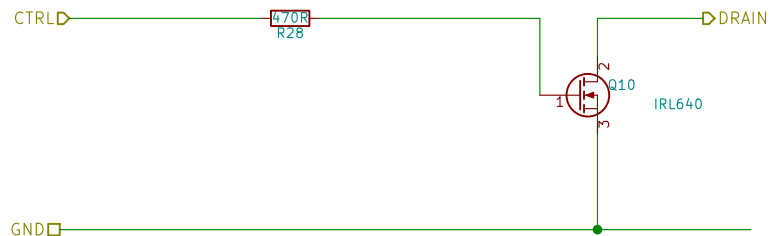
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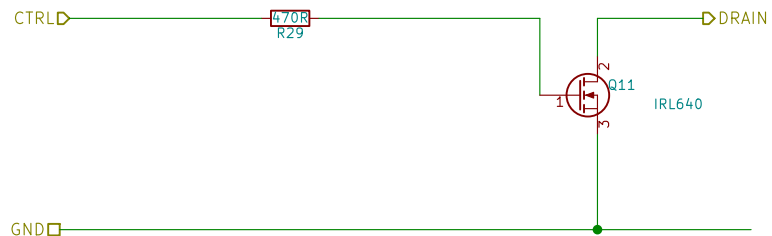
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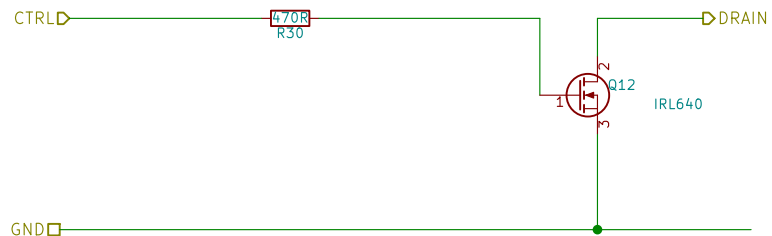
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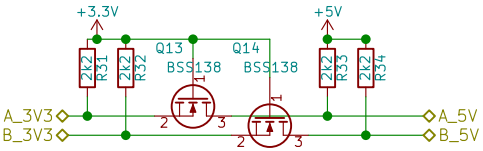
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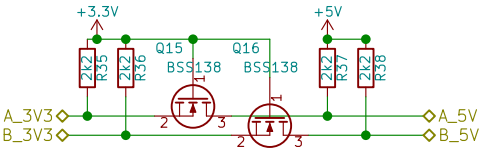
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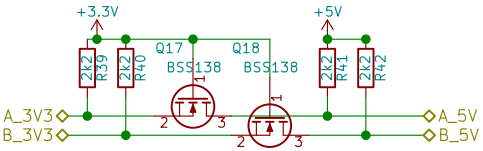
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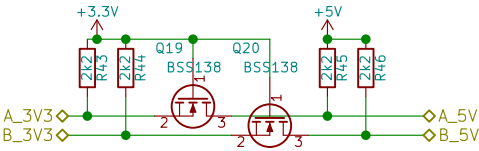
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