

Warning, configure PB0 and PB1 as open drain output with external pullup

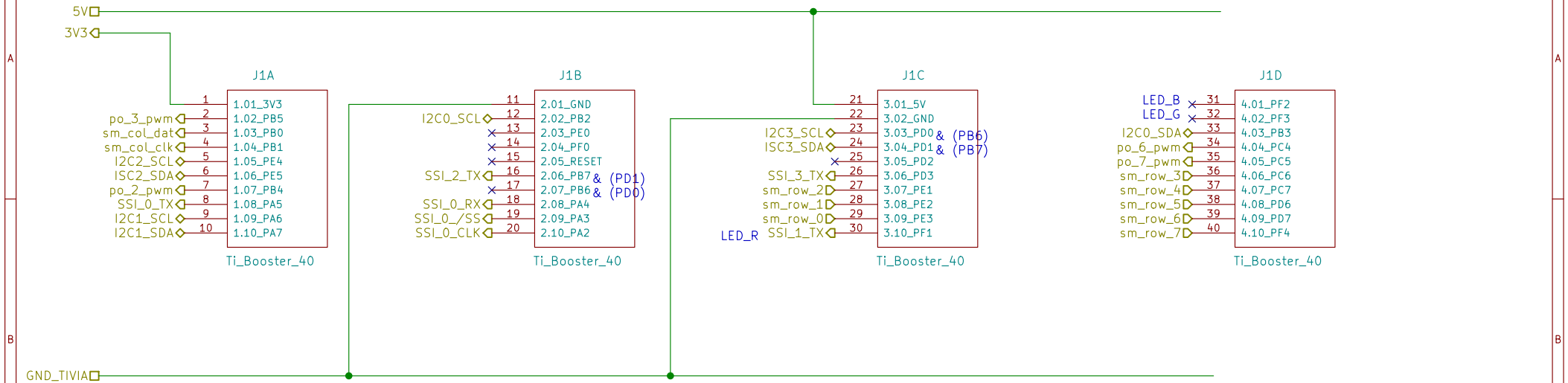


Table 2-3. J1 Connector⁽¹⁾

J1 Pin	GPIO	Analog Function GPIO AMSEL	On-board Function	Texas C Series MCU Pin	GPIOCTL Register Setting														
					1	2	3	4	5	6	7	8	9	14	15				
					3.3V														
1.01	PB0	AN01	57	SS0P0															
1.02	PB0	US0B0	40	US0B															
1.04	PB1	US0B0VUS	46	US0B															
1.05	PE4	AN05	58	US0A															
1.06	PE5	AN06	60	US0A															
1.07	PE4	AN07	58	SS0C0															
1.08	PA5	AN08	22	SS0C0															
1.09	PA6	AN09	23	SS0C0															
1.10	PA7	AN10	24	SS0C0															

⁽¹⁾ Shaded cells indicate configuration for compatibility with the MSP430 LaunchPad.

Table 2-5. J3 Connector⁽¹⁾

J3 Pin	GPIO	Analog Function GPIO AMSEL	On-board Function	Texas C Series MCU Pin	GPIOCTL Register Setting														
					1	2	3	4	5	6	7	8	9	14	15				
					GND														
3.01					GND														
3.02	PB0	AN1	51	SS0B	US0B0	MPWAVE	MPWAVE	–	WTC0P0	–	–	–	–	–	–	–	–	–	–
		Connected to MSP430 Comparator 1 (PB)	–	–	–	–	–	–	–	–	–	–	–	–	–	–	–	–	–
3.03	PE1	AN6	52	SS0B	US0B0	MPWAVE	MPWAVE	–	WTC0P1	–	–	–	–	–	–	–	–	–	–
		Connected to MSP430 Comparator 7 (PB)	–	–	–	–	–	–	–	–	–	–	–	–	–	–	–	–	–
3.04	PE1	AN6	52	SS0B	US0B0	MPWAVE	–	–	–	–	–	–	–	–	–	–	–	–	–
		Connected to MSP430 Comparator 7 (PB)	–	–	–	–	–	–	–	–	–	–	–	–	–	–	–	–	–
3.05	PE2	AN0	43	SS0B	US0B	–	MPWAVE	–	–	WTC0P0	–	–	–	–	–	–	–	–	–
3.06	PE3	AN4	54	SS0B	US0B	–	–	–	–	–	WTC0P1	US0B0P1	–	–	–	–	–	–	–
3.07	PE1	AN4	8	US0B	–	–	–	–	–	–	–	–	–	–	–	–	–	–	–
3.08	PE2	AN1	7	US0B	–	–	–	–	–	–	–	–	–	–	–	–	–	–	–
3.09	PE3	AN0	4	US0B	–	–	–	–	–	–	–	–	–	–	–	–	–	–	–
3.10	PE1	–	29	US0C5	US0B	–	–	–	–	–	–	–	–	–	–	–	CSD	TRD1	–

⁽¹⁾ Shaded cells indicate configuration for compatibility with the MSP430 LaunchPad.

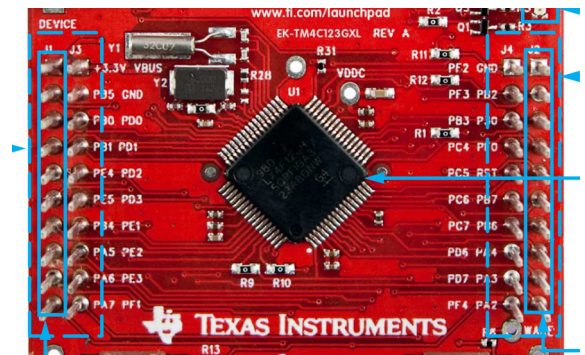
Table 2-4. J2 Connector⁽¹⁾

J2 Pin	GPIO	Analog Function GPIO AMSEL	On-board Function	Texas C Series MCU Pin	GPIOCTL Register Setting														
					1	2	3	4	5	6	7	8	9	14	15				
2.01					GND														
2.02	PB2	-	-	47	-	-	OCESSEL	-	-	-	-	-	-	-	-	TACCP0	-	-	-
2.03	PE0	AN0	-	7	UTR1	-	-	-	-	-	-	-	MPVWMA	PA0	TACCP0	W0	CD0	-	-
2.04			USER I/O (J1)																
2.06					RESET														
	PB1	-	-	6	-	-	SS0T0	-	-	-	-	-	MPVWMA	-	-	TACCP1	-	-	-
	PE1	AN6	Connected to A/D Input Compatibility (R10)	42	SS0B0	-	-	OCESSEL	-	-	-	-	MPVWMA	MPVWMA1	-	WTTCCP1	-	-	-
2.07					SS0D0														
	PB6	-	-	1	-	-	SS0D0	-	-	-	-	-	MPVWMA	-	-	TACCP0	-	-	-
	PE0	AN17	Connected to A/D Input Compatibility (R1)	61	SS0C0	SS0C0	OCESSEL	MPVWMA	MPVWMA	-	-	-	-	-	-	WTTCCP0	-	-	-
2.08	PA4	-	-	21	-	-	SS0C0	-	-	-	-	-	-	-	-	-	-	-	-
2.09	PA2	-	-	20	-	-	SS0B0	-	-	-	-	-	-	-	-	-	-	-	-
2.10	PA2	-	-	15	-	-	SS0C0	-	-	-	-	-	-	-	-	-	-	-	-

⁽¹⁾ Shaded cells indicate configuration for compatibility with the MSP430 LaunchPad.

Table 2-6. J4 Connector

J4 Pin	GPIO	Analog Function GPIO AMSEL	On-board Function	Texas C Series MCU Pin	GPIOCTL Register Setting														
					1	2	3	4	5	6	7	8	9	14	15				
4.01	PE2	Blue LED (R13)	30	SS0C0															
4.02	PE3	Green LED (R13)	31	SS0C0															
4.03	PE4	Red LED (R13)	32	SS0C0															
4.04	PE5	Blue LED (R13)	33	SS0C0															
4.05	PE6	Green LED (R13)	34	SS0C0															
4.06	PE7	Red LED (R13)	35	SS0C0															
4.07	PE8	Blue LED (R13)	36	SS0C0															
4.08	PE9	Green LED (R13)	37	SS0C0															
4.09	PE10	Red LED (R13)	38	SS0C0															
4.10	PE11	Blue LED (R13)	39	SS0C0															



* Remove R10 on the launchpad evaluation board to avoid 2 GPIO pins connected in parallel on 2.06 and 3.04

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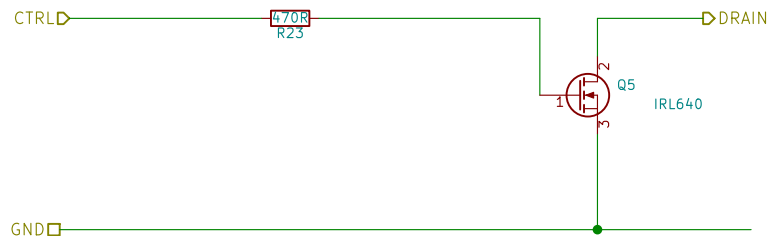
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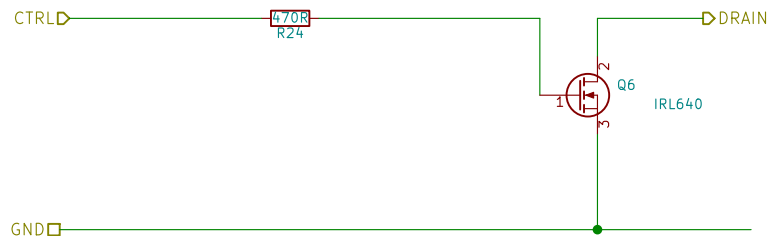
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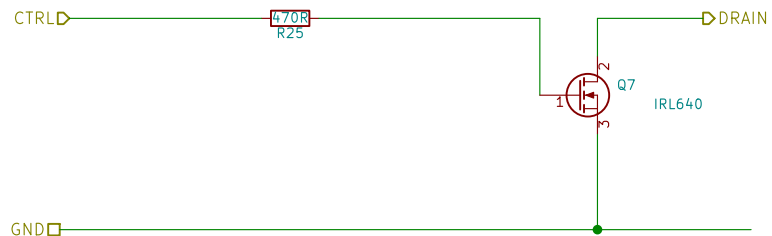
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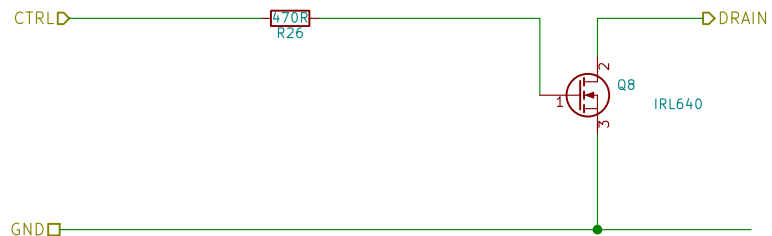
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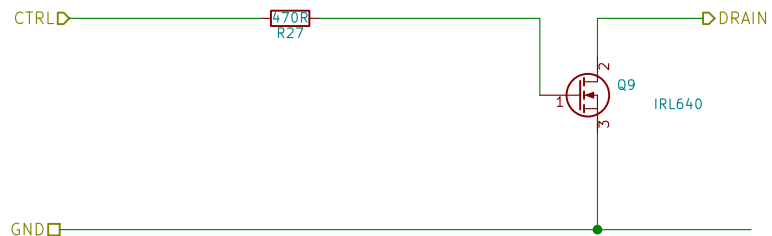
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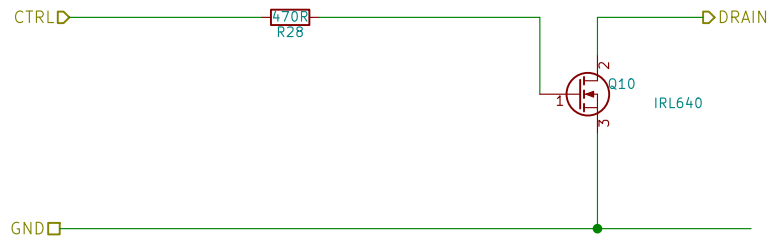
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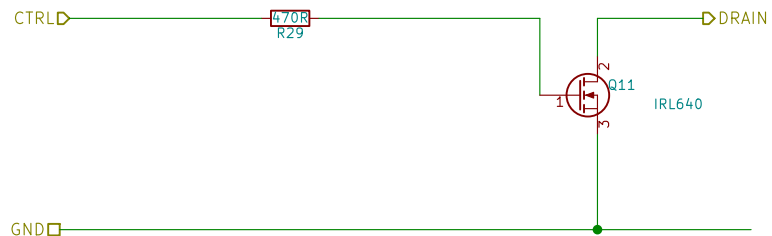
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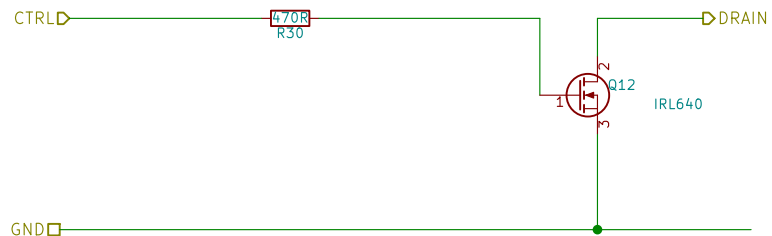
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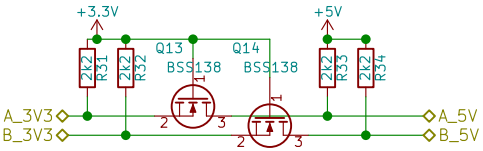
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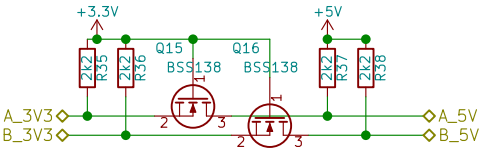
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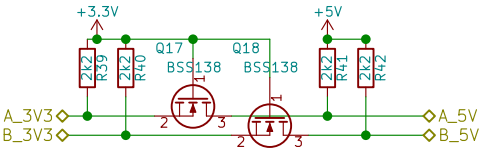
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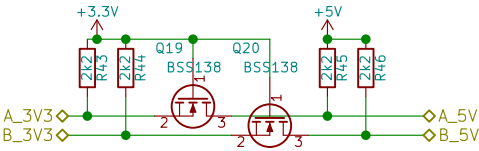
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