

Warning, configure PB0 and PB1 as open drain output with external pullup

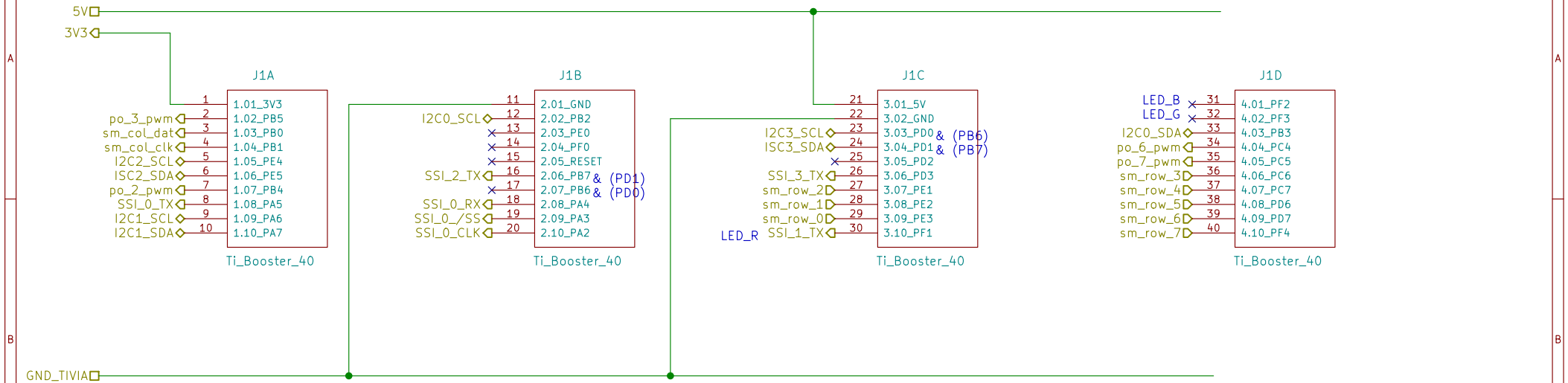


Table 2-3. J1 Connector⁽¹⁾

J1 Pin	GPIO	Analog Function GPIO AMSEL	On-board Function	Texas Instruments MCU Pin	GPIOCTL Register Setting														
					1	2	3	4	5	6	7	8	9	14	15				
					3.3V														
1.01	PB5	ANAL1	--	57	--	SSIFM	--	MOPWM	--	--	TI0CP1	CANETX	--	--	--				
1.02	PB0	USBDIO	--	40	UTXN	--	--	--	--	--	TI0CP9	--	--	--	--				
1.04	PB1	USBDIOBUS	--	46	UTXN	--	--	--	--	--	TI0CP9	--	--	--	--				
1.05	PE4	ANAL	--	58	US1N	--	IC2SCL	MOPWM	MOPWM	--	--	CANRX	--	--	--				
1.06	PE5	ANAL	--	60	US1N	--	IC2SDA	MOPWM	MOPWM	--	--	CANETX	--	--	--				
1.07	PB4	ANAL0	--	58	--	SSIOX	--	MOPWM	--	--	TI0CP9	CANRX	--	--	--				
1.08	PA5	--	--	27	--	SSIOX	--	--	--	--	--	--	--	--	--				
1.09	PA6	--	--	23	--	IC1SCL	--	MOPWM	--	--	--	--	--	--	--				
1.10	PA7	--	--	24	--	IC1SDA	--	MOPWM	--	--	--	--	--	--	--				

⁽¹⁾ Shaded cells indicate configuration for compatibility with the MSP430 LaunchPad.

Table 2-5. J3 Connector⁽¹⁾

J3 Pin	GPIO	Analog Function GPIO AMSEL	On-board Function	Texas Instruments MCU Pin	GPIOCTL Register Setting														
					1	2	3	4	5	6	7	8	9	14	15				
3.01					GND														
3.02					GND														
	POD ANT	--	61	SSIOX	SSIOX	IC2SCL	MOPWM	MOPWM	--	TI0CP9	--	--	--	--	--				
3.03	PB6	Connected to MSP430 Comparator 1 (PB)	--	1	--	SSIOX	--	MOPWM	--	--	TI0CP9	--	--	--	--				
	PC1	ANAL	82	SSIOX	SSIOX	IC2SDA	MOPWM	MOPWM	--	TI0CP1	--	--	--	--	--				
3.04	PB7	Connected to MSP430 Comparator 1 (PB)	--	8	--	SSIOX	--	MOPWM	--	--	TI0CP1	--	--	--	--				
3.05	PC2	ANAL	63	SSIOX	SSIOX	--	MOPWM	--	--	TI0CP9	USBDIO	--	--	--	--				
3.06	PC3	ANAL	64	SSIOX	SSIOX	--	--	--	--	--	TI0CP1	USBDIO	--	--	--				
3.07	PE1	ANAL	6	UTXN	--	--	--	--	--	--	--	--	--	--	--				
3.08	PE2	ANAL	7	--	--	--	--	--	--	--	--	--	--	--	--				
3.09	PE3	ANAL	6	--	--	--	--	--	--	--	--	--	--	--	--				
3.10	PE4	--	25	US1CS	SSIOX	--	--	MOPWM	--	TI0CP1	--	CAN	TRIS	--	--				

⁽¹⁾ Shaded cells indicate configuration for compatibility with the MSP430 LaunchPad.

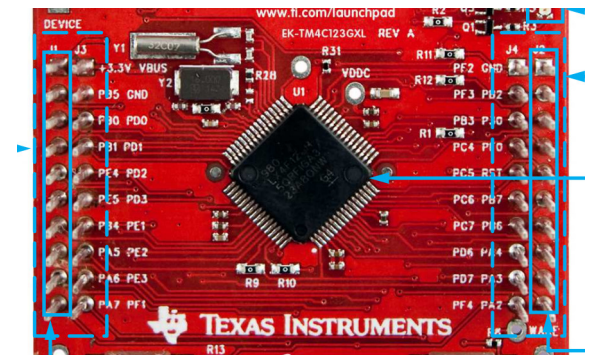
Table 2-4. J2 Connector⁽¹⁾

J2 Pin	GPIO	Analog Function GPIO AMSEL	On-board Function	Texas Instruments MCU Pin	GPIOCTL Register Setting														
					1	2	3	4	5	6	7	8	9	14	15				
2.01					GND														
2.02	PB2	--	--	47	--	--	IC2SCL	--	--	--	--	TI0CP9	--	--	--	--			
2.03	PE0	ANAL	--	9	UTXN	--	--	--	--	--	--	--	--	--	--	--			
2.04	PB0	--	--	28	US1N	SSIOX	CANRX	--	MOPWM	PA6	TI0CP9	NMI	CAN	--	--	--			
2.05	PB7	--	--	4	--	SSIOX	--	RESIST	--	--	TI0CP1	--	--	--	--	--			
2.06	PC1	ANAL	--	62	SSIOX	SSIOX	IC2SDA	MOPWM	MOPWM	--	TI0CP1	--	--	--	--	--			
2.07	PB0	ANAL	--	1	--	SSIOX	--	MOPWM	--	--	TI0CP9	--	--	--	--	--			
2.08	PA4	--	--	21	--	SSIOX	--	--	--	--	--	--	--	--	--	--			
2.09	PA3	--	--	20	--	SSIOX	--	--	--	--	--	--	--	--	--	--			
2.10	PA2	--	--	19	--	SSIOX	--	--	--	--	--	--	--	--	--	--			

⁽¹⁾ Shaded cells indicate configuration for compatibility with the MSP430 LaunchPad.

Table 2-6. J4 Connector

J4 Pin	GPIO	Analog Function GPIO AMSEL	On-board Function	Texas Instruments MCU Pin	GPIOCTL Register Setting														
					1	2	3	4	5	6	7	8	9	14	15				
4.01	PE2	--	--	Blue LED (R13)	30	--	SSIOX	--	MOPWM	--	TI0CP1	--	--	--	TRIS0				
4.02	PE3	--	--	Green LED (R13)	31	--	SSIOX	CANETX	--	MOPWM	--	TI0CP1	--	--	TRIS0				
4.03	PE3	--	--	48	--	--	SSIOX	--	--	--	TI0CP1	--	--	--	--				
4.04	PC4	EL+	--	18	US1N	US1N	--	MOPWM	--	EX1	TI0CP9	US1N	--	--	--				
4.05	PC5	EL+	--	15	US1N	US1N	--	MOPWM	--	PA1	TI0CP1	US1N	--	--	--				
4.06	PC6	EL+	--	16	US1N	--	--	--	--	PA1	TI0CP1	US1N	--	--	--				
4.07	PC7	EL+	--	13	US1N	--	--	--	--	PA1	TI0CP1	US1N	--	--	--				
4.08	PC8	--	--	10	US1N	--	--	--	--	PA1	TI0CP1	US1N	--	--	--				
4.09	PC9	--	--	10	US1N	--	--	--	--	PA1	TI0CP1	US1N	--	--	--				
4.10	PE4	--	--	Use SW1 (R13)	5	--	--	--	MOPWM	--	TI0CP1	US1N	--	--	--				

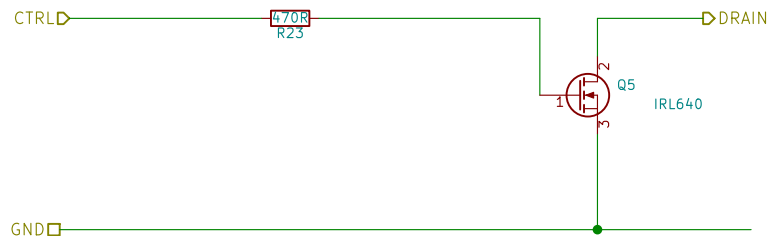


* Remove R10 on the launchpad evaluation board to avoid 2 GPIO pins connected in parallel on 2.06 and 3.04

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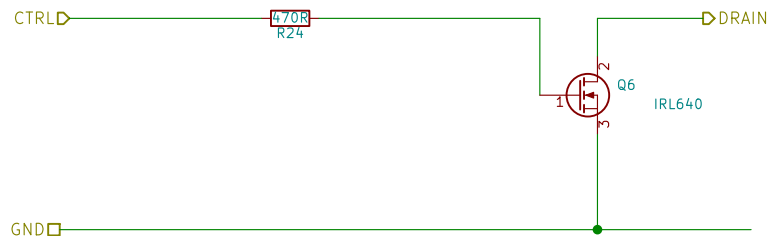
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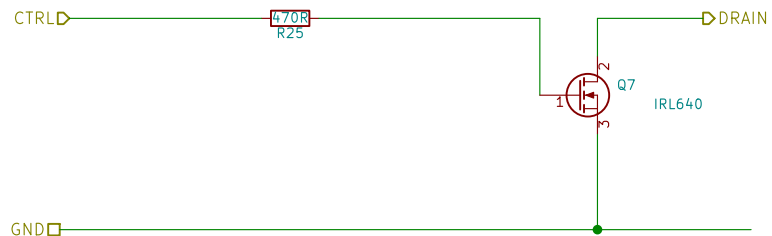
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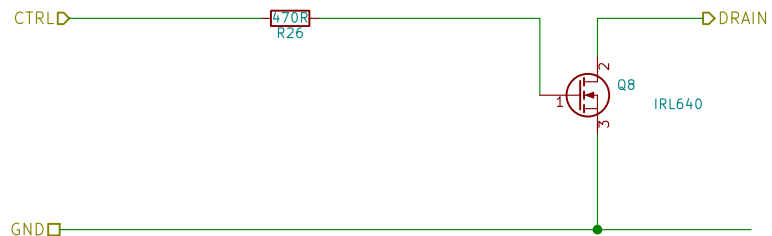
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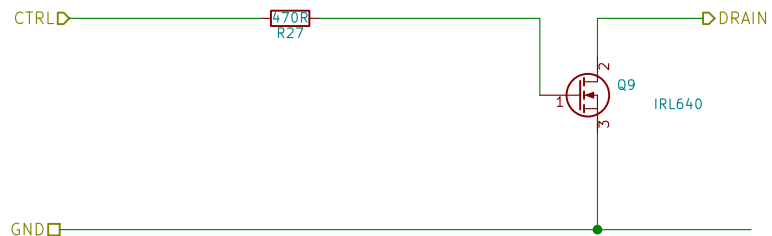
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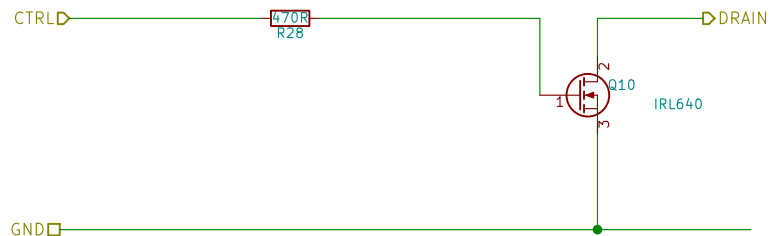
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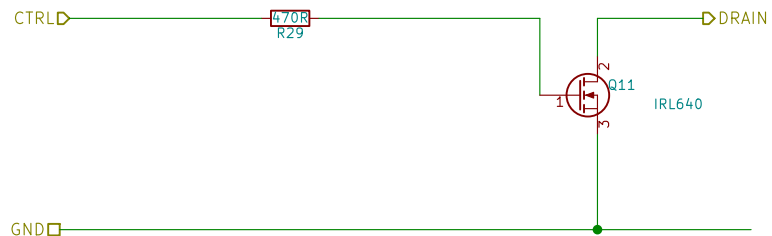
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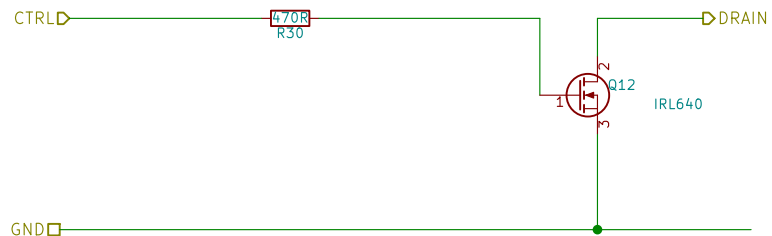
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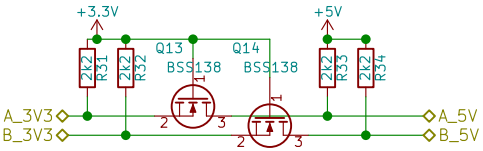
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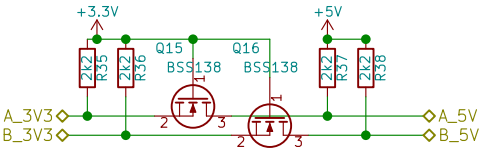
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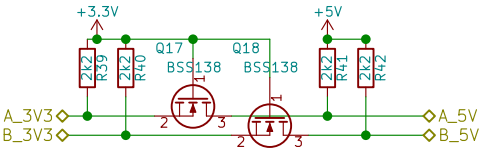
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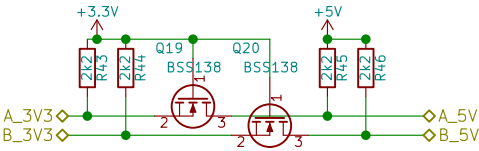
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