HW01 S18 **NAME:Michael Gould**

CS332

A word is equal to 12 bits

3 BIT OPCODES

1 1 0	LOAD MBR FROM MEM
1 1 1	ADD MBR FROM MEM
1 0 1	STOR MBR TO MEM

The system will fetch and execute 3 lines of code only.
The PC register will be incremented by 1 after each fetch.
Show final values in binary below after execution.

some of the known values
prior to the start of execution

PC REGISTER

0 0 0 0 1 1 1 1 1 1 1 1

MEMORY	addr
0 0 0 0 0 0 0 0 0 1 1 1	250
0 0 0 0 0 0 0 0 1 0 0 1	251
0 0 0 0 0 0 0 0 0 1 0 0	252
1 1 0 0 1 1 1 1 1 0 1 1	253
1 1 0 0 1 1 1 1 1 0 1 0	254
1 1 0 0 1 1 1 1 1 0 1 0	255
1 1 1 1 0 0 0 0 0 1 0 0	256
1 0 1 0 1 1 1 1 1 0 1 0	257
1 1 0 0 1 1 1 1 1 0 0 0	258
1 1 1 1 1 1 1 1 1 1 1 1	259
0 0 0 0 0 0 0 0 1 0 0 1	260
0 0 0 0 0 0 0 0 0 1 1 0	261

after execution:

memory value must be in binary

address value must be in decimal

register value must be in binary

addr	MEMORY
255	110011111010
256	111100000100
257	101011111010
250	1000

enter the memory value and address
only if the original value changed

REGISTERS	
PC	100000010
MBR	1000
IR	111100000100
MAR	100000100

20 points total