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REVISION	DESCRIPTION	DATE	APPROVED

STM32G4_DRV8301_Board

hardware V1.0

Designed by: JingLei

Table of contents

- Sheet 1: Project overview (this page)
- Sheet 2:
- Sheet 3:

Legend

- General comment such as function title, configuration, ...
- Text to be added to silkscreen.
- Warning text.

Notes to generate the board layout.

OPEN PLATFORM LICENSE AGREEMENT

Top
STM32G4_DRV8301_hardware_TOP_V1.0.SchDoc

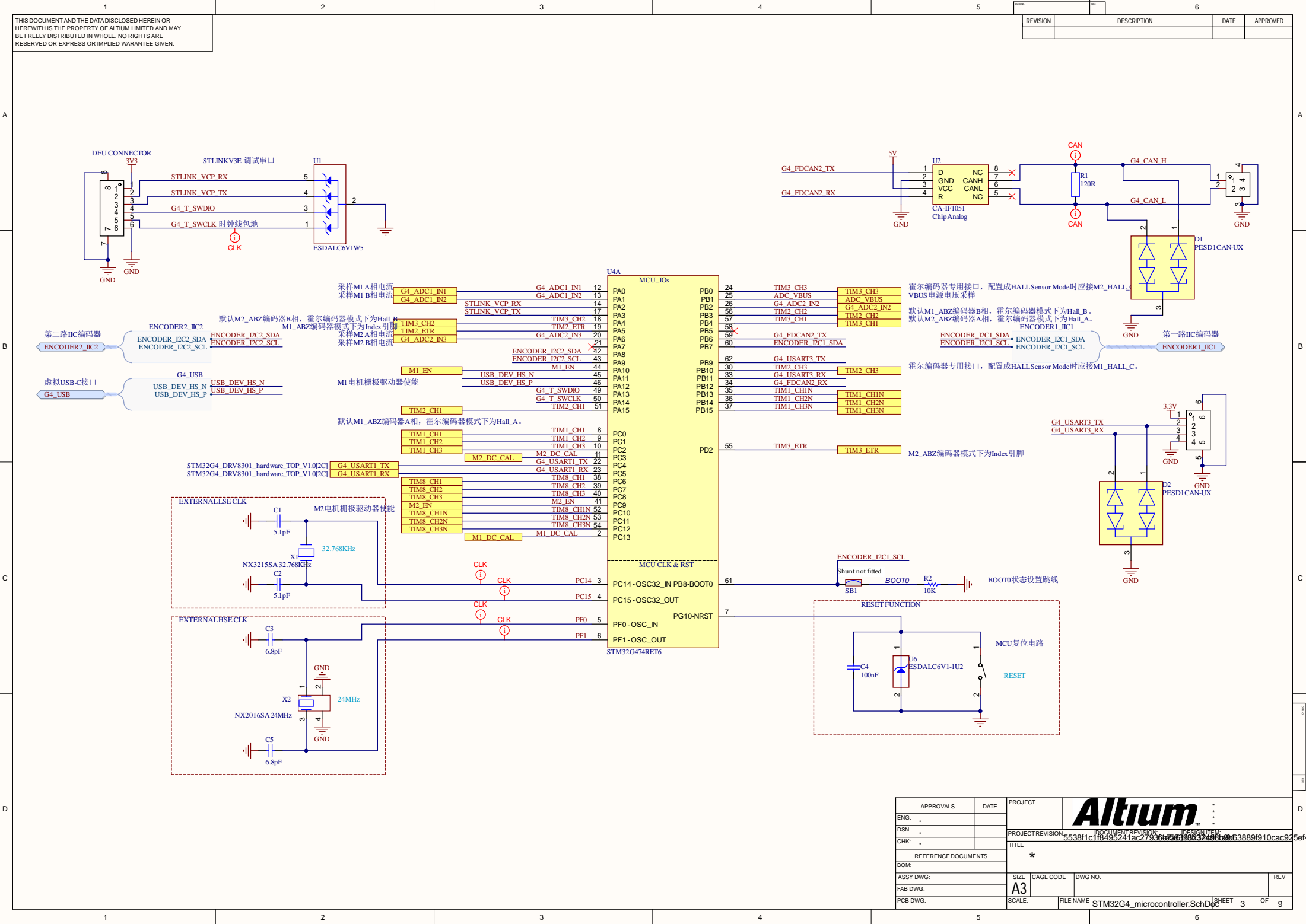


APPROVALS	DATE	PROJECT	Altium			
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DSN:						
CHK:						
REFERENCE DOCUMENTS		TITLE				
BOM:		★				
ASSY DWG:	SIZE	CAGE CODE	DWG NO.			REV
FAB DWG:	A3					
PCB DWG:	SCALE:	FILE NAME	Project_overview.SchDoc	SHEET	1	OF 9

1



FILE NAME	STM32G4_Drv8301_hardware_TOP_V2.0.SchDoc
	6



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A

A

B

B

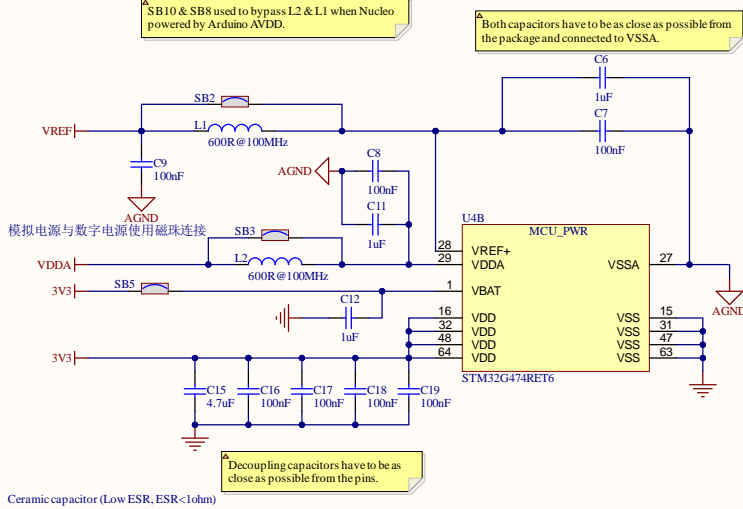
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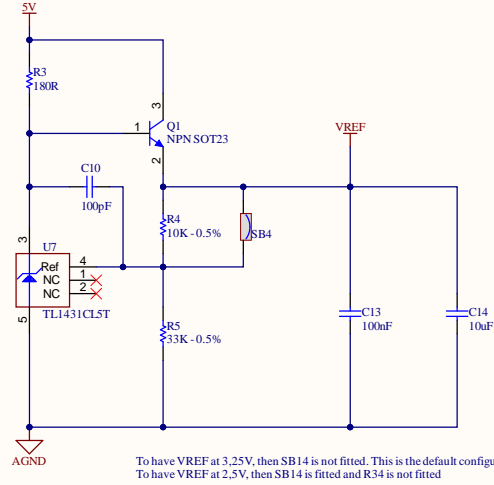
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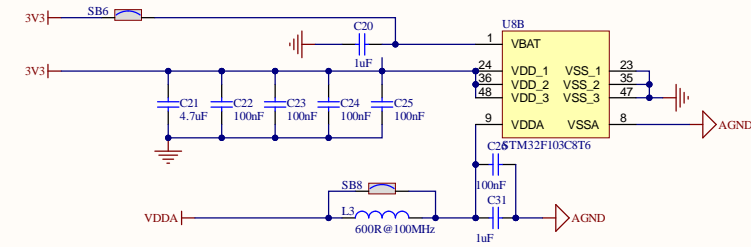
G474 POWER



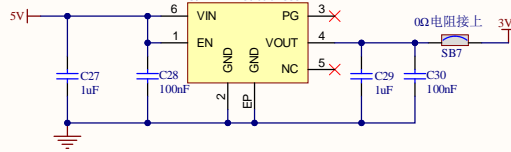
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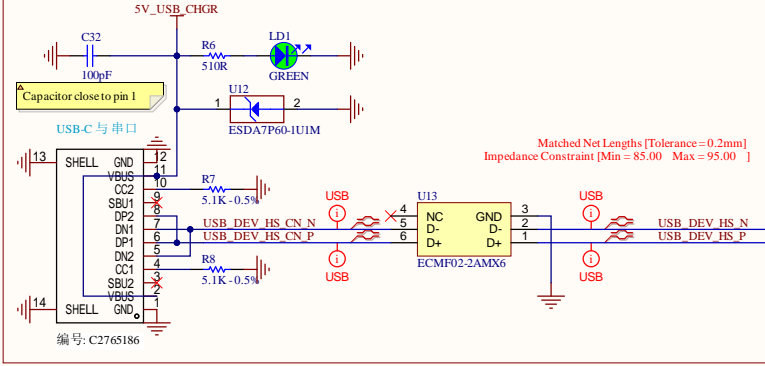
F103 POWER



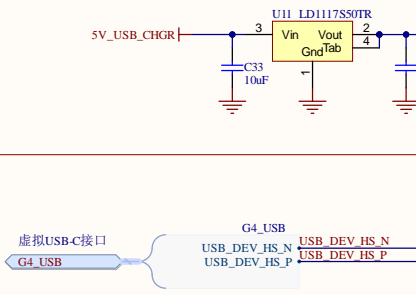
VDD PWR



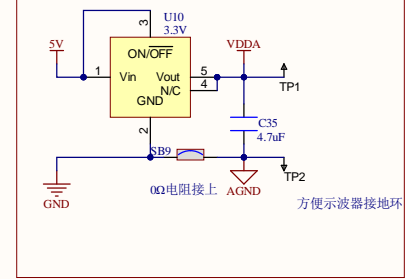
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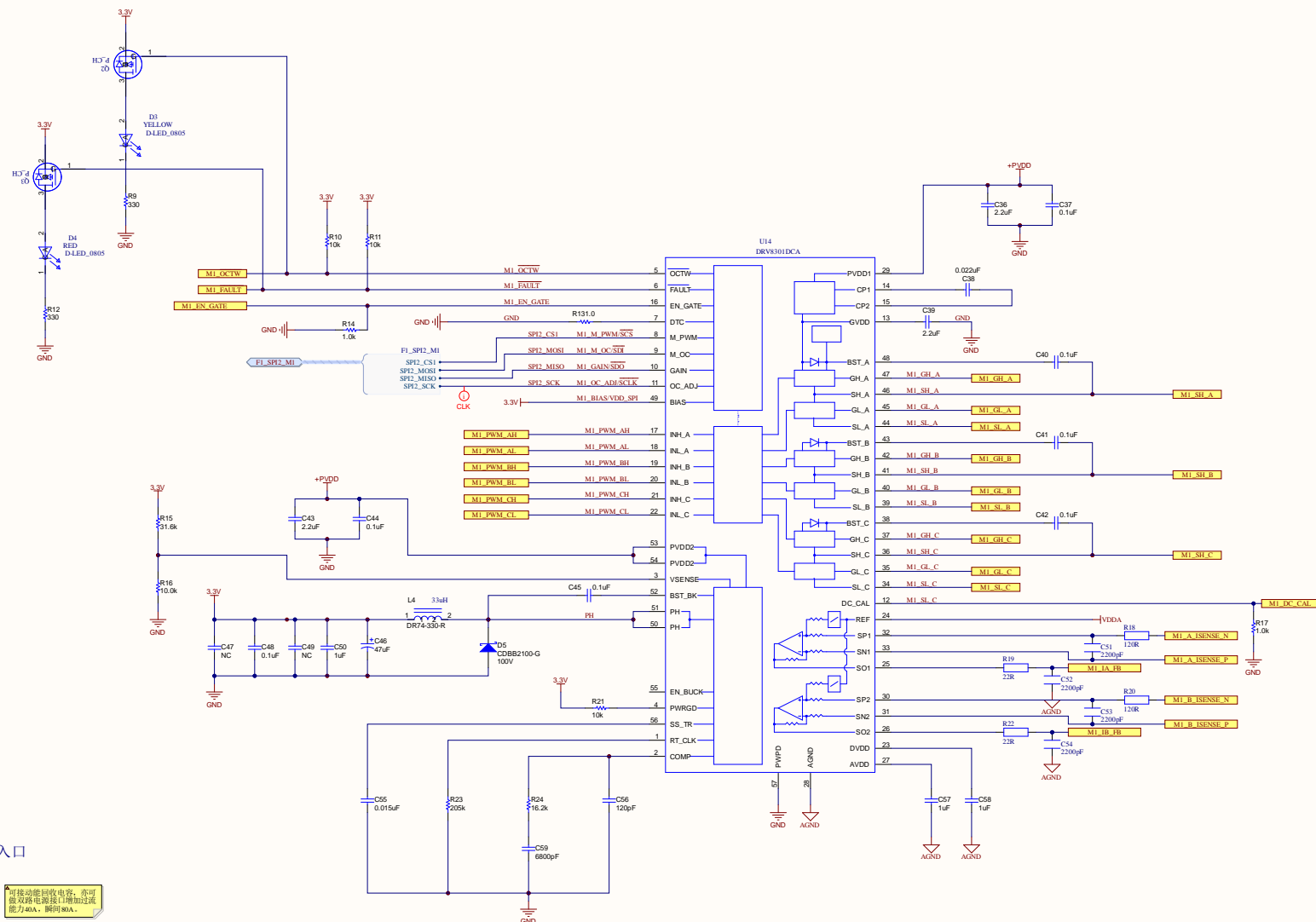
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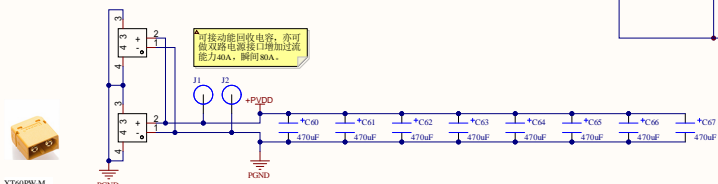
VDDA Power



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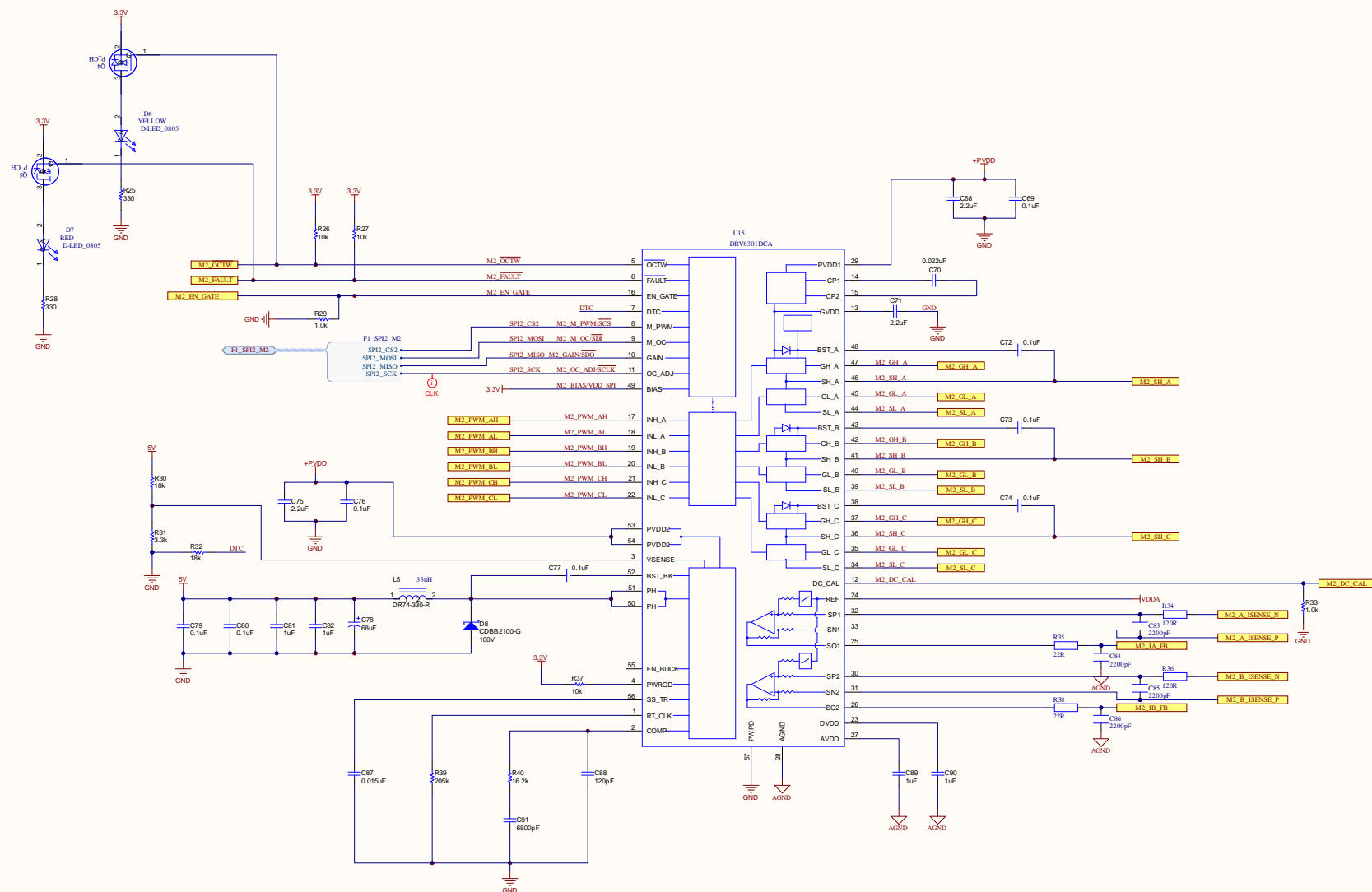



总电源接入口

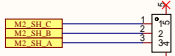
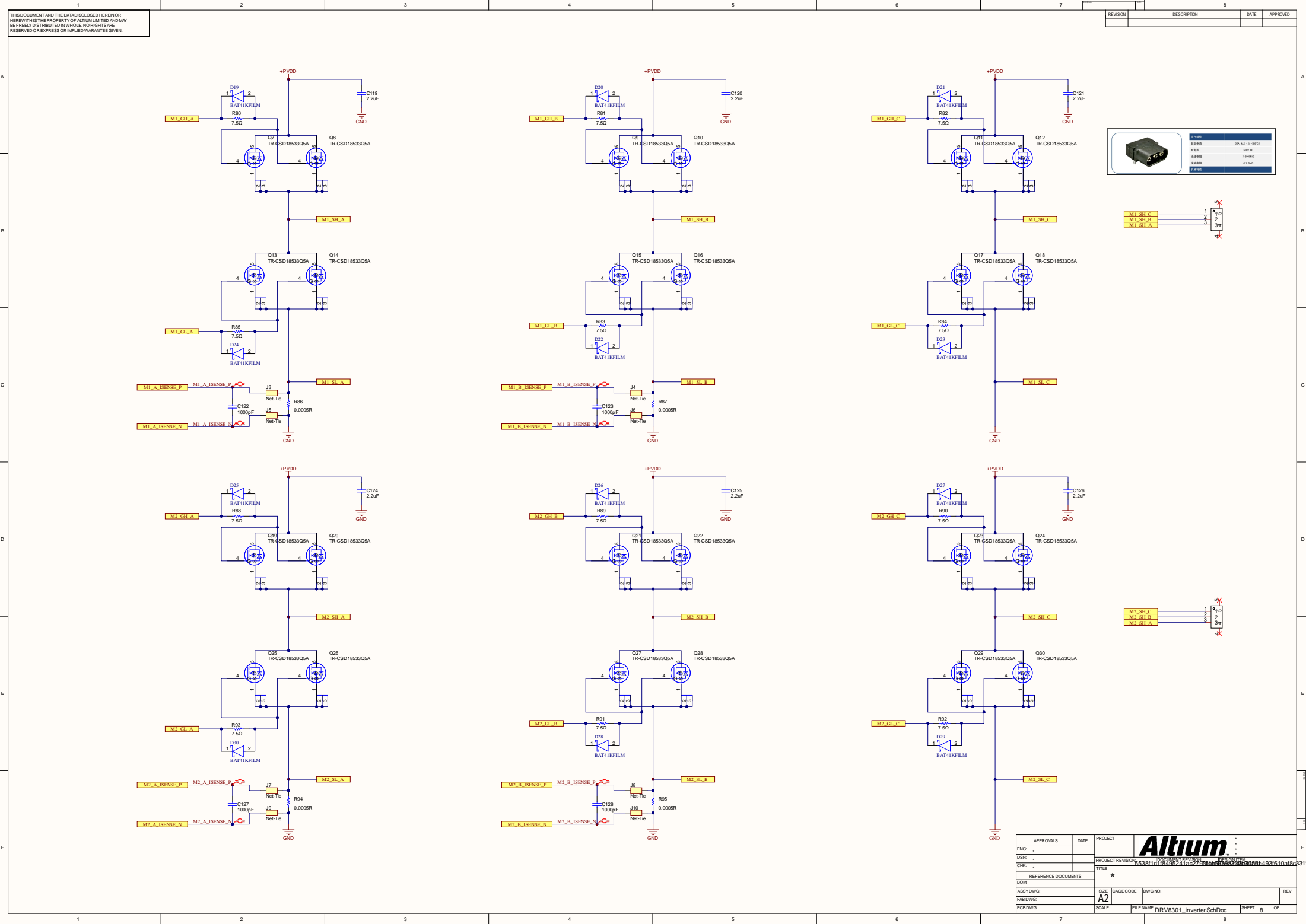


电气性能	测试方法	特性
耐电压	UL1977	DC 500V
额定电流	UL1977	20A
接触电阻	GB/T5095.2	0.6mΩ
绝缘电阻	GB/T5095.2	2000MΩ
瞬时电流	IEA 1min <80℃	40A
温升	UL1977	20A 4h <60℃
爬电距离	GB/T2900.18	4.85mm

[illegible]



APPROVALS		DATE	PROJECT			
ENG:						
DES:						
CHK:						
REFERENCE DOCUMENTS			TITLE			
BOM			★			
ASSY DWG:			SIZE	CAGE CODE	DWG NO.	REV
FAB DWG:			A2			
PCB DWG:			SCALE:	FILE NAME	DR2 /RV8301 driver SchDoc	SHEET 6 OF



APPROVALS	DATE	PROJ.BCT	Altium	
ENG				
DES				
CHK				
REFERENCE DOCUMENTS				
ASSEMBLY DWG:			SIZE	CAGE CODE
FAB DWG:			A2	DWG NO.
PCB DWG:			SCALE	FILENAME
			DRV8301_inverter_SchDoc	SHEET 8 OF

