

# STM32G4\_DRV8301\_Board

hardware V1.0

Designed by: JingLei

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- Sheet 1: Project overview (this page)
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## Legend

- General comment such as function title, configuration, ...
- Text to be added to silkscreen.
- Warning text.

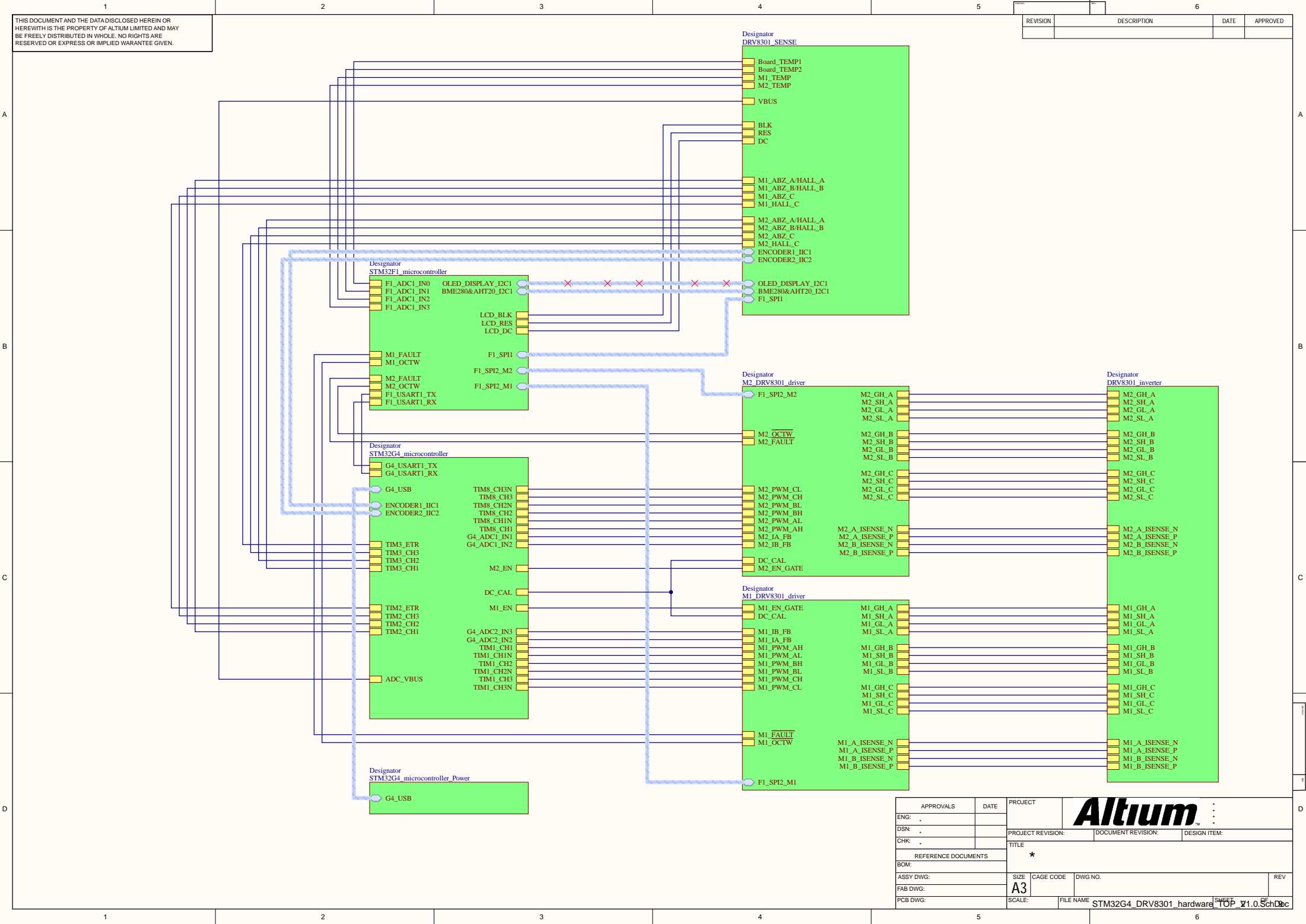
Notes to generate the board layout.

## OPEN PLATFORM LICENSE AGREEMENT

Top  
STM32G4\_DRV8301\_hardware\_TOP\_V1.0.SchDoc



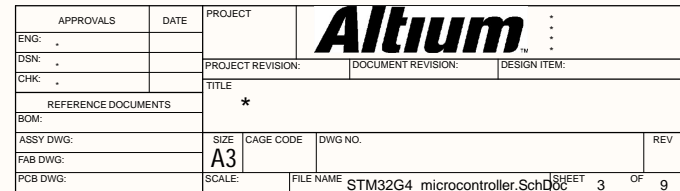
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ENG:						
DSN:						
CHK:						
REFERENCE DOCUMENTS			*  <			



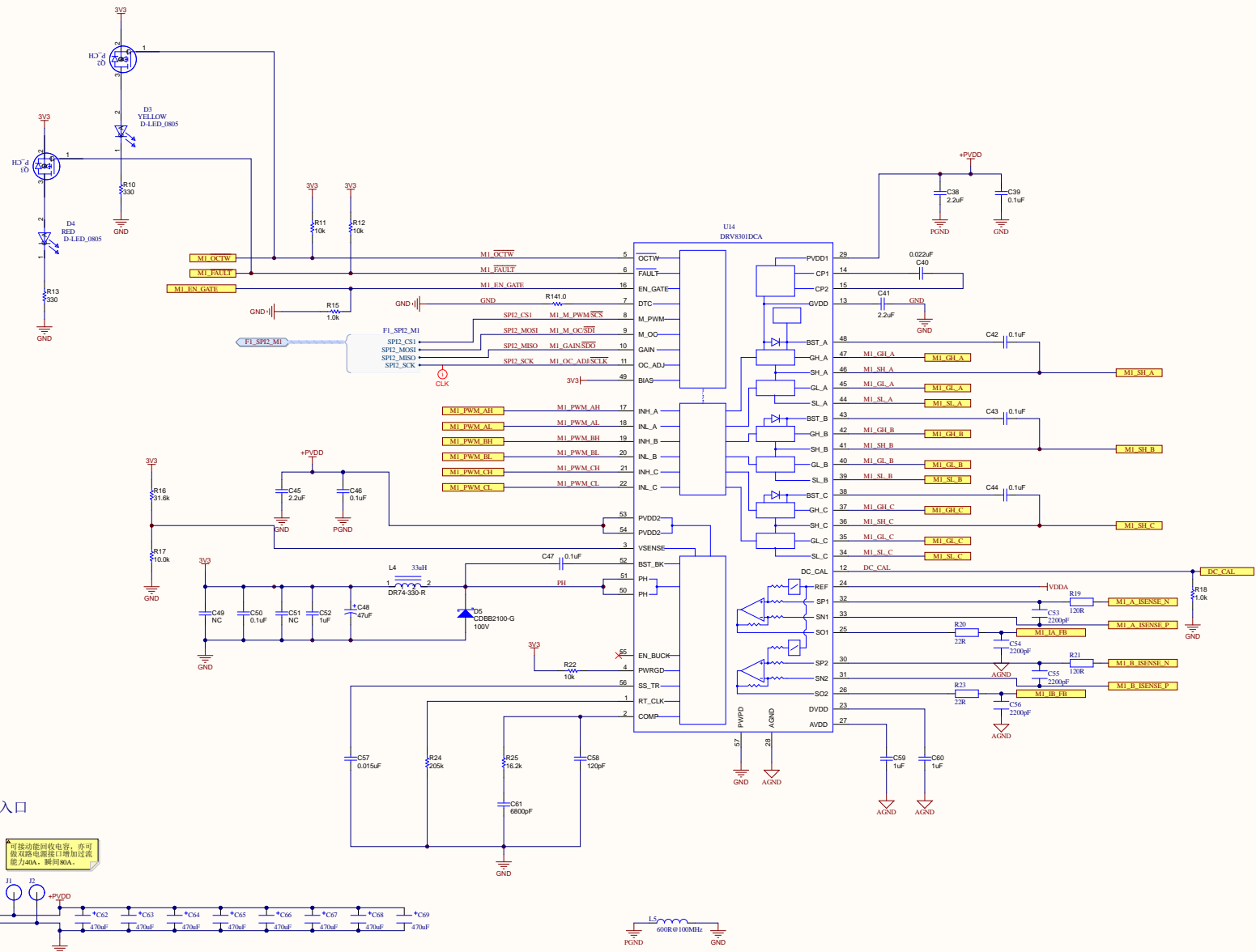
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REVISION	DESCRIPTION	DATE	APPROVED

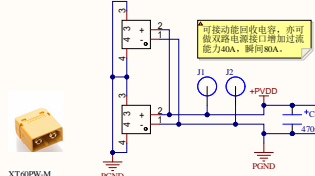
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REFERENCE DOCUMENTS				TITLE			
BOM:				*			
ASSY DWG:				SIZE	CAGE CODE	DWG NO.	REV
FAB DWG:				A3			
PCB DWG:				SCALE:	FILE NAME	STM32G4_DRV8301_hardware_TOP_V1.0.SchDoc	





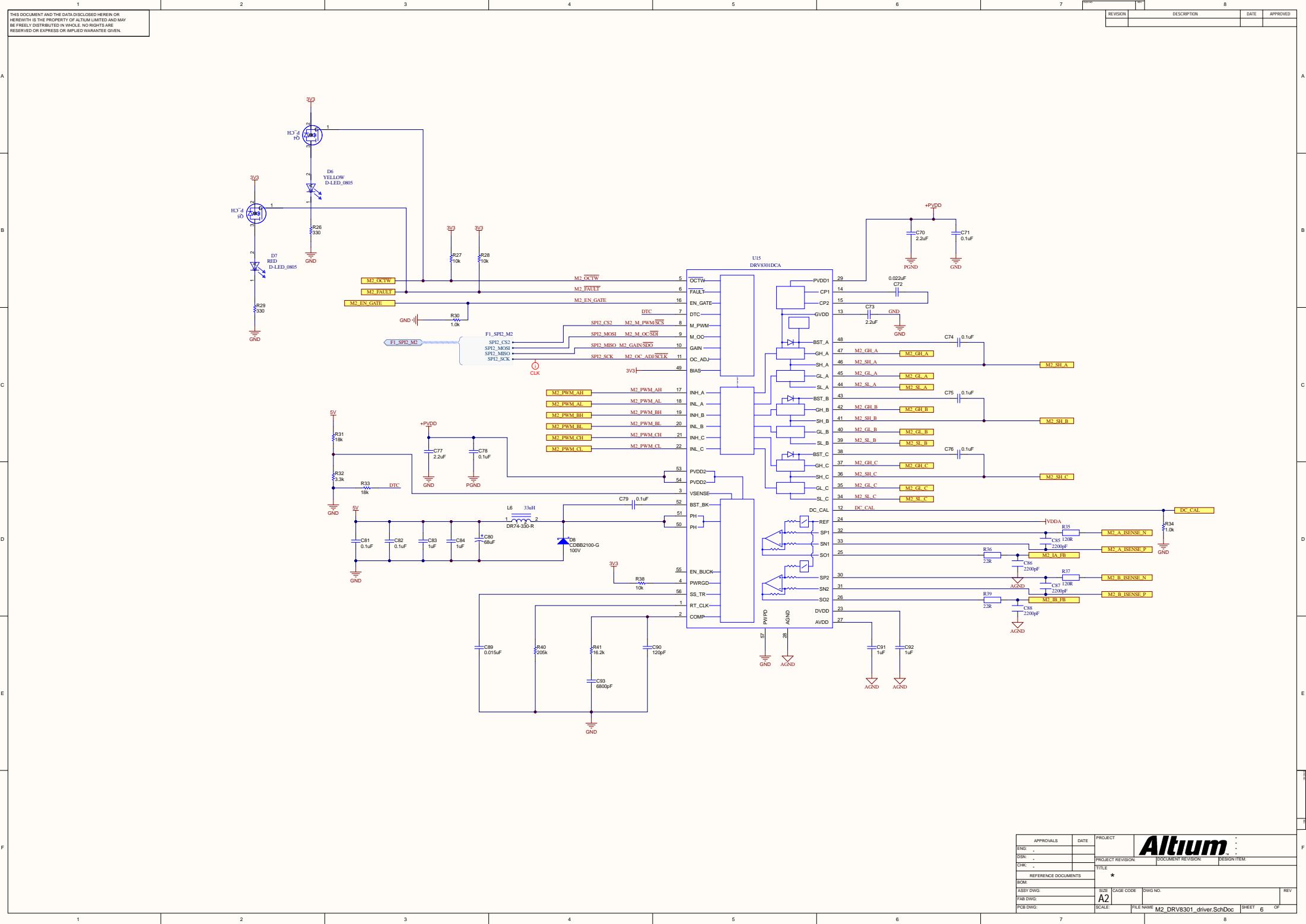


## 总电源接入口



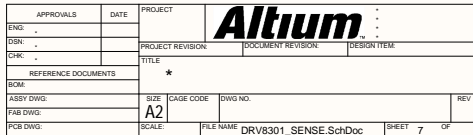
电气性能	测试方法	特性
额定电压	UL 1977	DC 500V
额定电流	UL 1977	20A
接触电阻	GB/T5095.2	0.60mΩ
绝缘电阻	GB/T5095.2	2000MΩ
额定电流	ΔA Imin < 80℃	40A
温升	UL 1977	20A 4h < 60℃
爬电距离	GB/T2950.18	4.86mm

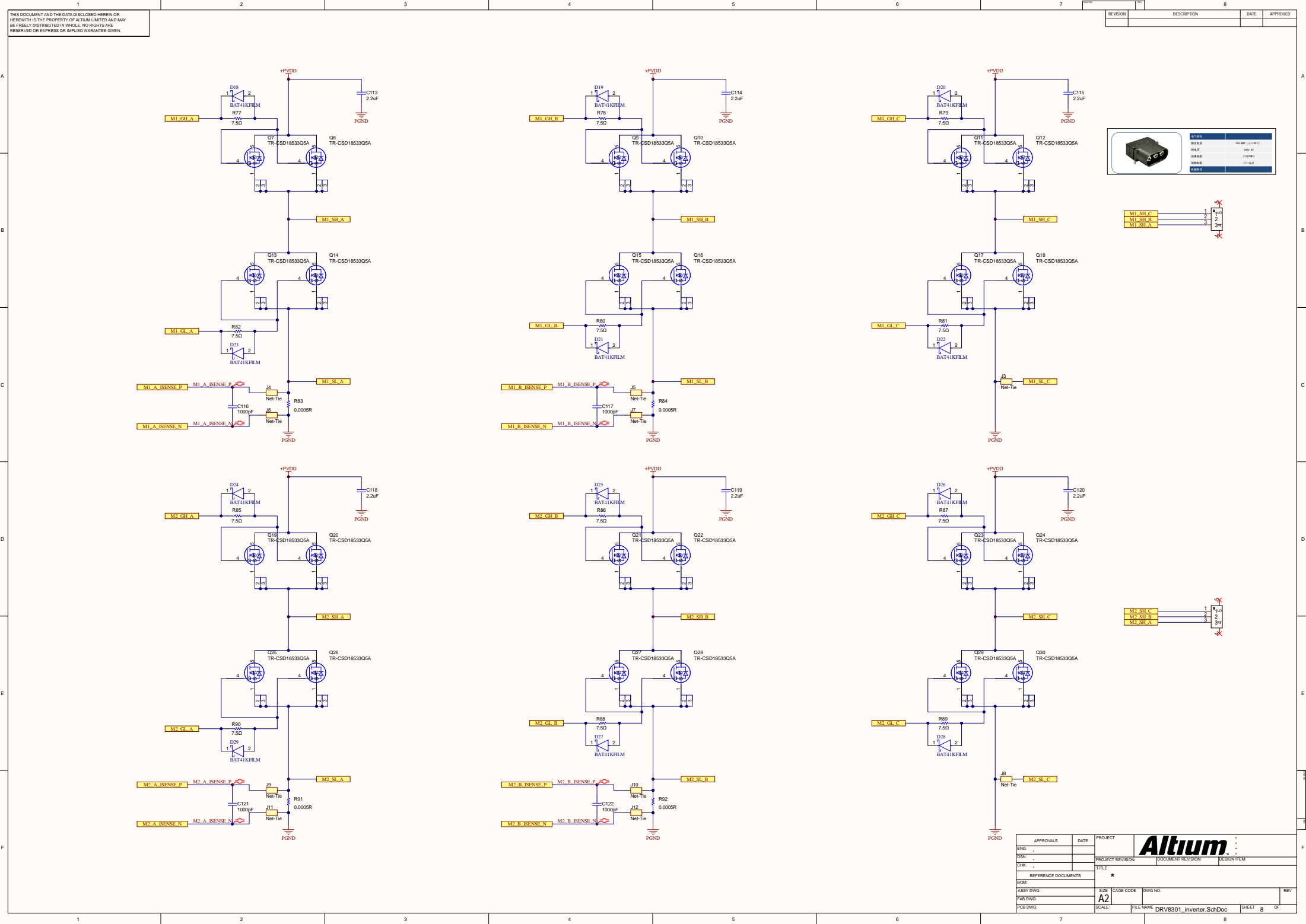
APPROVALS		DATE	PROJECT		<b>Altium</b>	
ENG			PROJECT REVISION		DOCUMENT REVISION	DESIGN ITEM
CHG - BOOSTX-DRV30X						
REFERENCE DOCUMENTS			* DRV30X REV5			
ISSUE		CAGE CODE		DWG NO	REV	
ASBY DWG:		A2		1	4	
FAB DWG:		Nicholas Oborny				
PCB DWG:		SCALE	FILE NAME	M1-DRV30X1_driver.SchDoc		SHEET 5 OF



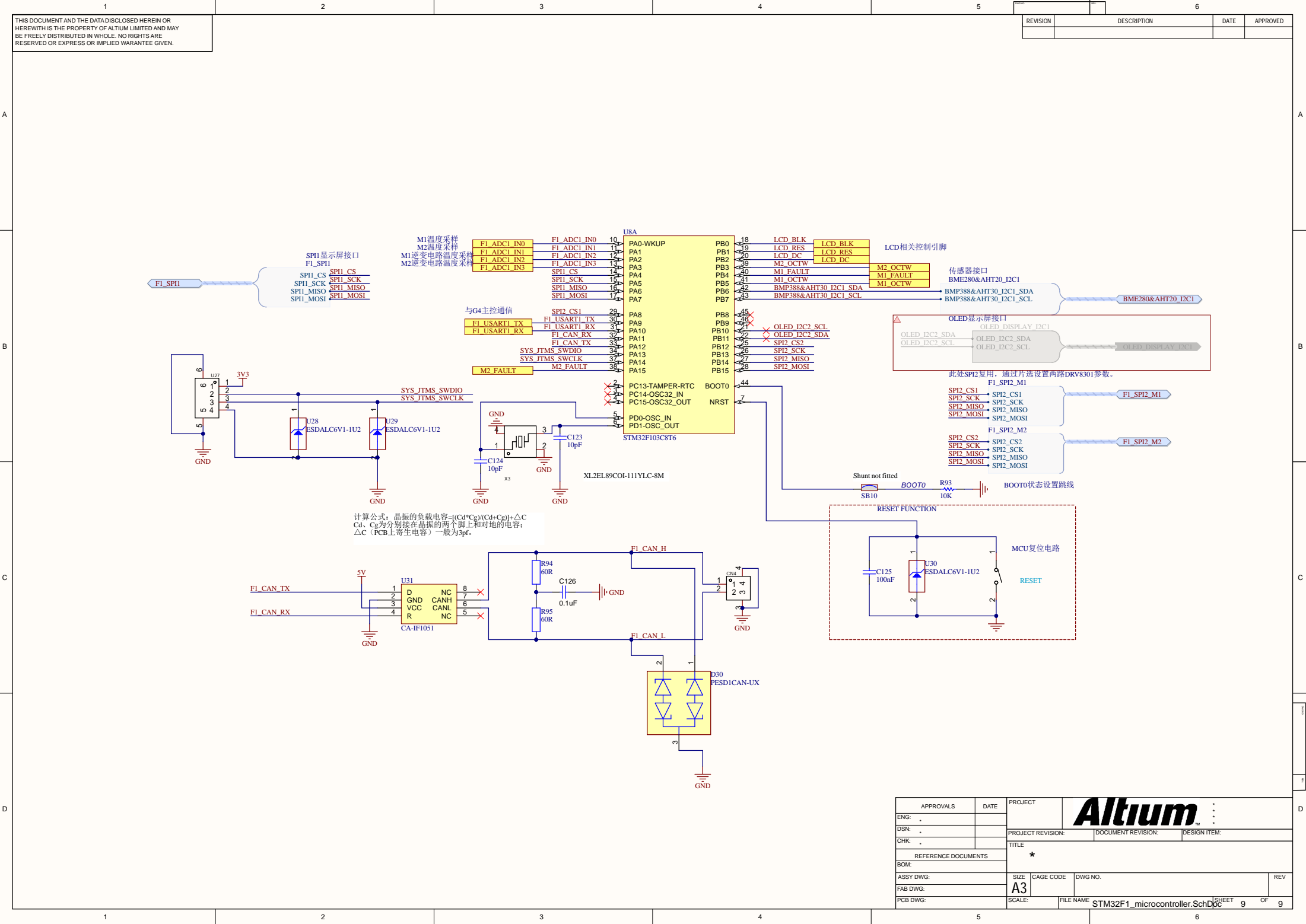
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ENG			PROJECT REVISION		DOCUMENT REVISION	DESIGN ITEM
DSN			TITLE			
CHK			★			
REFERENCE DOCUMENTS						
BOA						
ASBY DWG			SIZE	CAGE CODE	DWG NO.	REV
FAB DWG			A2			
PCB DWG			SCALE	FILE NAME		M2_DRV8301_driver.SchDoc
					SHEET	6 OF










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REVISION	DESCRIPTION	DATE	APPROVED

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DSN:	*		TITLE				★	
CHK:	*		REFERENCE DOCUMENTS					
BOM:								
ASSY DWG:		SIZE	CAGE CODE	DWG NO.		REV		
FAB DWG:		A3						
PCB DWG:		SCALE:	FILE NAME	STM32F1_microcontroller.SchDoc		SHEET 9 OF 9		