

Homework #1

- 1) What is a netlist?
- 2) Why is an HDL program technology independent?
- 3) What is the purpose of simulation?
- 4) Briefly describe the 3 inputs to a synthesizer.
- 5) What is a design flow?
- 6) What is the difference between the syntax and semantics of a program statement?
- 7) Express 93 decimal as an 8-bit binary string using the Verilog syntax.

Each question is worth 2 points.