LUT structure

ADDR[3:0]

0000   0001   0010   MUX   1111

D

SRL16 Structure

DIN
CLK

Q15 or
MC15

A[3:0]   0000 0001 0010 0011 0100 0101 0110 0111 1000 1001 1010 1011 1100 1101 1110 1111

D

x465_02_040203

NOTE: Now when configured $Q \rightarrow D$ connections have been added