SHI-YU LIN

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SUMMARY

Highly motivated computer engineering student seeking full time Software development position.

EDUCATION

Arizona State University, Tempe, AZ   Jan. 2019 – Dec. 2020(Expected)
Master of Science (Major: Computer Engineering)   GPA: 3.47/4.0
Selected Courses: Computer Architecture, Embedded Operating System Internals, Machine Learning for Smart Grid, VLSI Design, Foundations of Algorithms, Data Mining, Cloud Computing

National Sun Yat-Sen University (NSYSU), Kaohsiung City, Taiwan   Sept.2012 – Jun. 2016
Bachelor of Science (Major: Electrical Engineering)   GPA: 3.09/4.3

TECHNICAL SKILLS

Programming Language: C, C++, Python, Java, Verilog, Shell scripting, Matlab
Tools and Framework: Linux, Visual Studio, Git, OpenCV, Tensorflow, Raspberry Pi 3, Virtuoso, Hspice, Django, SQLite

PROFESSIONAL EXPERIENCE

Accton Technology Corporation, Tainan City, Taiwan   Feb. 2017 – May. 2018
Hardware Engineer, Full time
- Designed Ethernet switch for enterprises data center; featured in high speed port up to 100G.
- Verified and maintained functionality within EMC, thermal, Signal Integrity standards.

Silicon Motion Technology, Hsinchu City, Taiwan   July. 2019 – August. 2019
System Integration Engineer, Intern
- Developed and optimized Analog to Digital Convertor on 32-bit ARM Cortex M7 processor STM32 EVB board from scratch.
- Measured NVME and UFS SSD current within 5% mA tolerance at standby mode and heavy loading mode.

ACADEMIC RESEARCH AND PROJECTS

Side Project
- Work on object detection with Tensorflow and Raspberry Pi to detect Poker.
- Detect object and send an email to notify user.

Arizona State University   Spring 2019
Final Project, CSE551 Foundation of Algorithm
- Implemented algorithm to solve BCRP-MNCC, a variant of Minimum spanning tree problem with given budget.

Assignments, CSE520 Computer Architecture   Spring 2019
- Cache replacement policy SHIP / Branch Predictor gDAC implemented on GEM5 simulator for X86 processors.

Final Project, EEE525 VLSI Design   Fall 2019
- Designed system to perform 1024 bits multiplication and square root.
- Scaling and analyzing between power, area and clock frequency tradeoff.

National Sun Yat-Sen University   Aug. 2015 – Jan. 2016
Project: Novel comb spectrum CDMA system using perfect Gaussian integer
- Applied Matlab to rebuild the model in QPSK and 16QAM modulation.
- Analyzed and plotted the improvement of bit error rate performance.