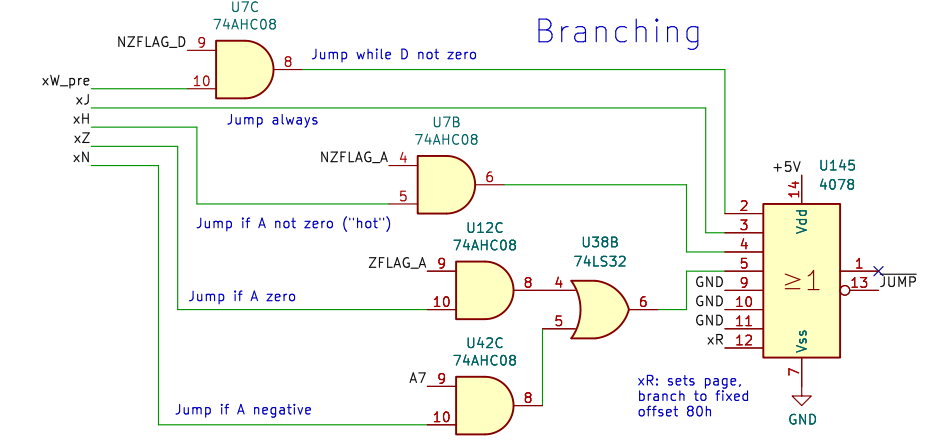
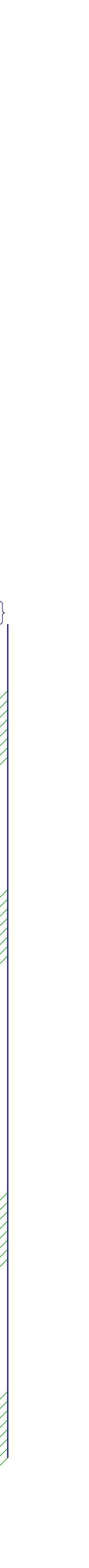
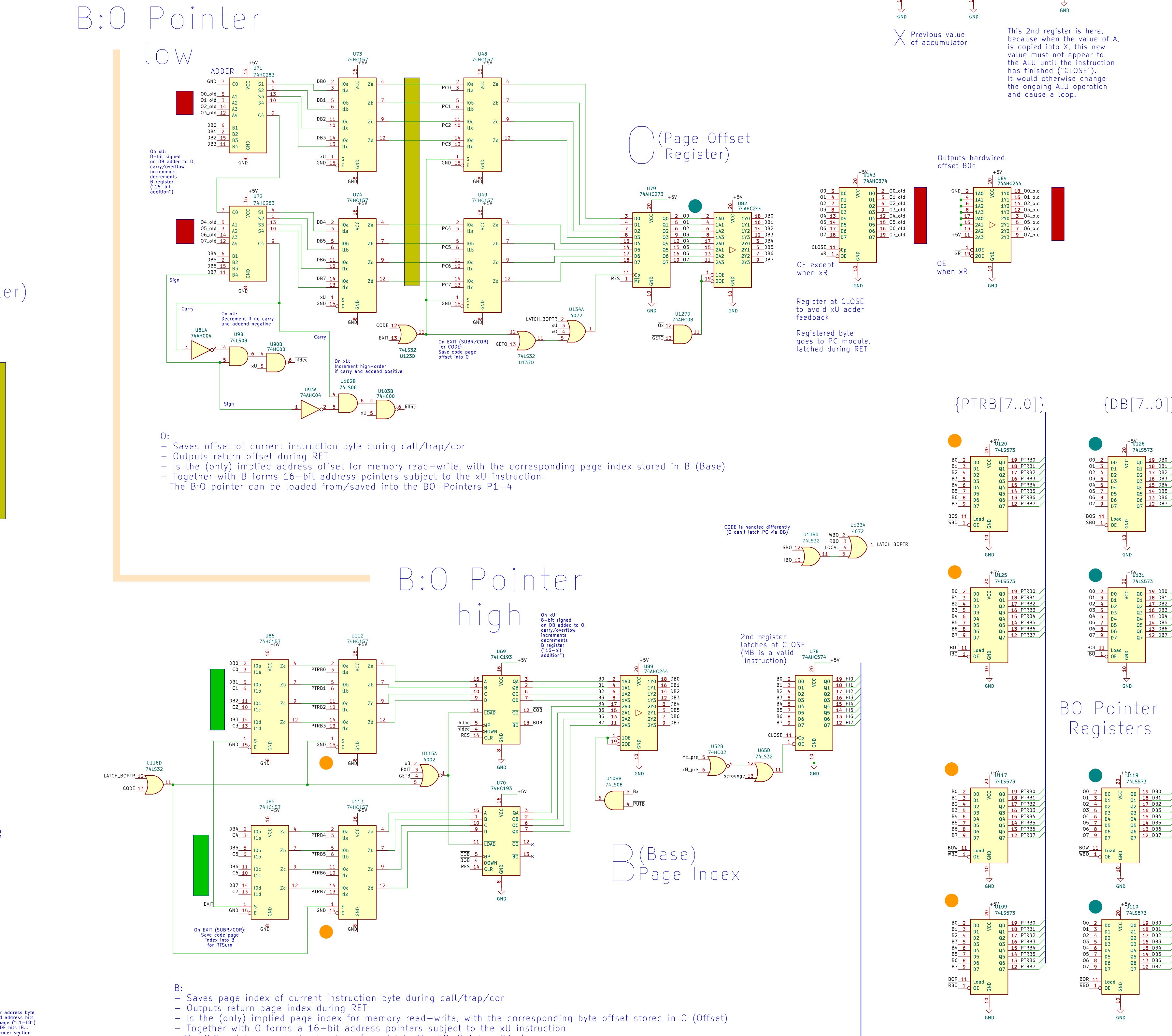
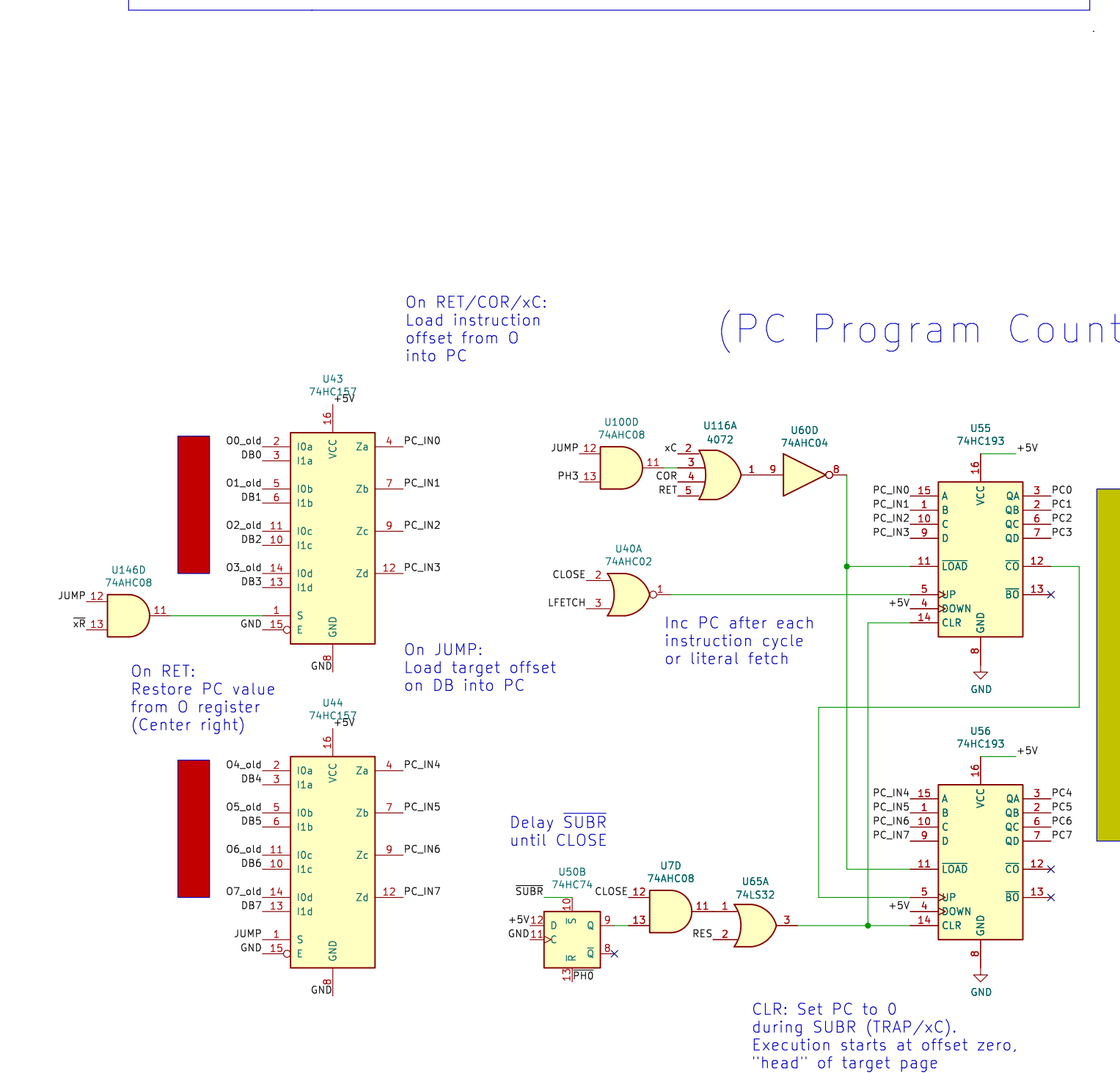
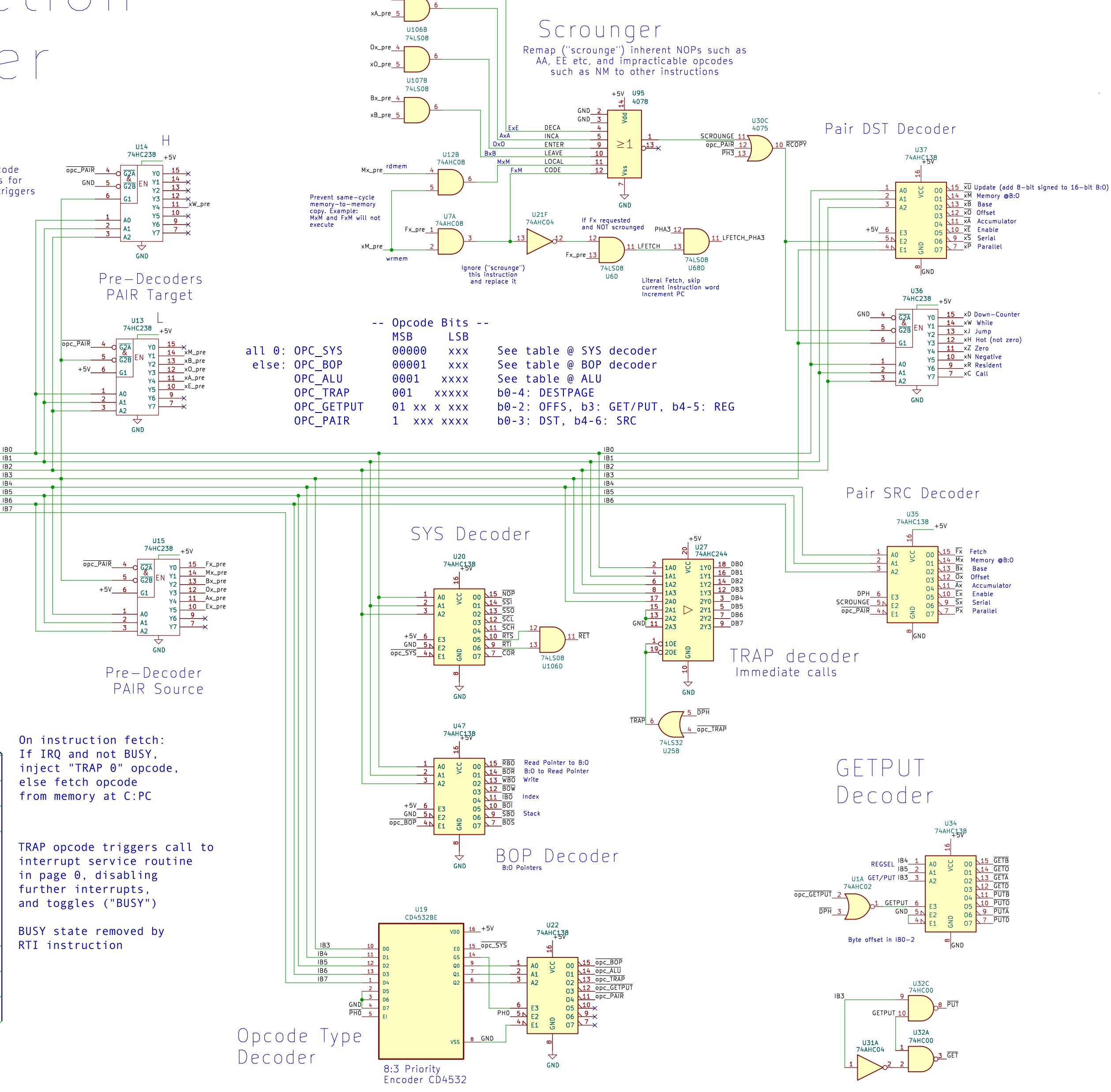
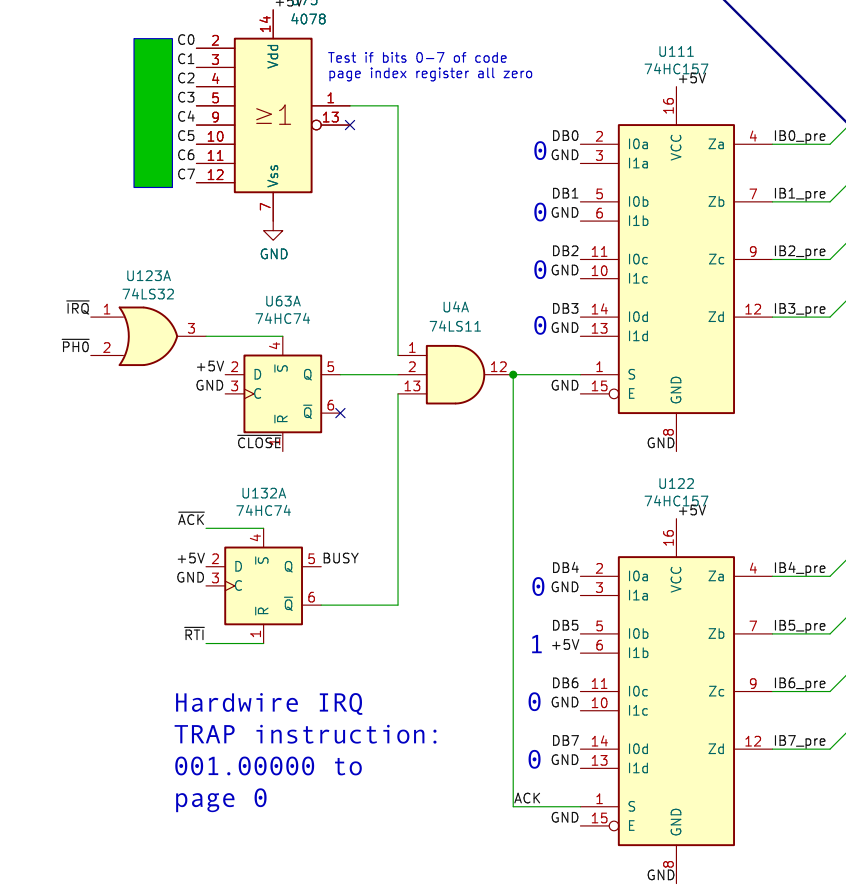
[illegible]

Start, latch  
instruction or  
interrupt vector

Interrupts disabled when "BUSY"  
and when running code in page 0  
(See code page index reg  
schematics bottom right)



## Input/Output

