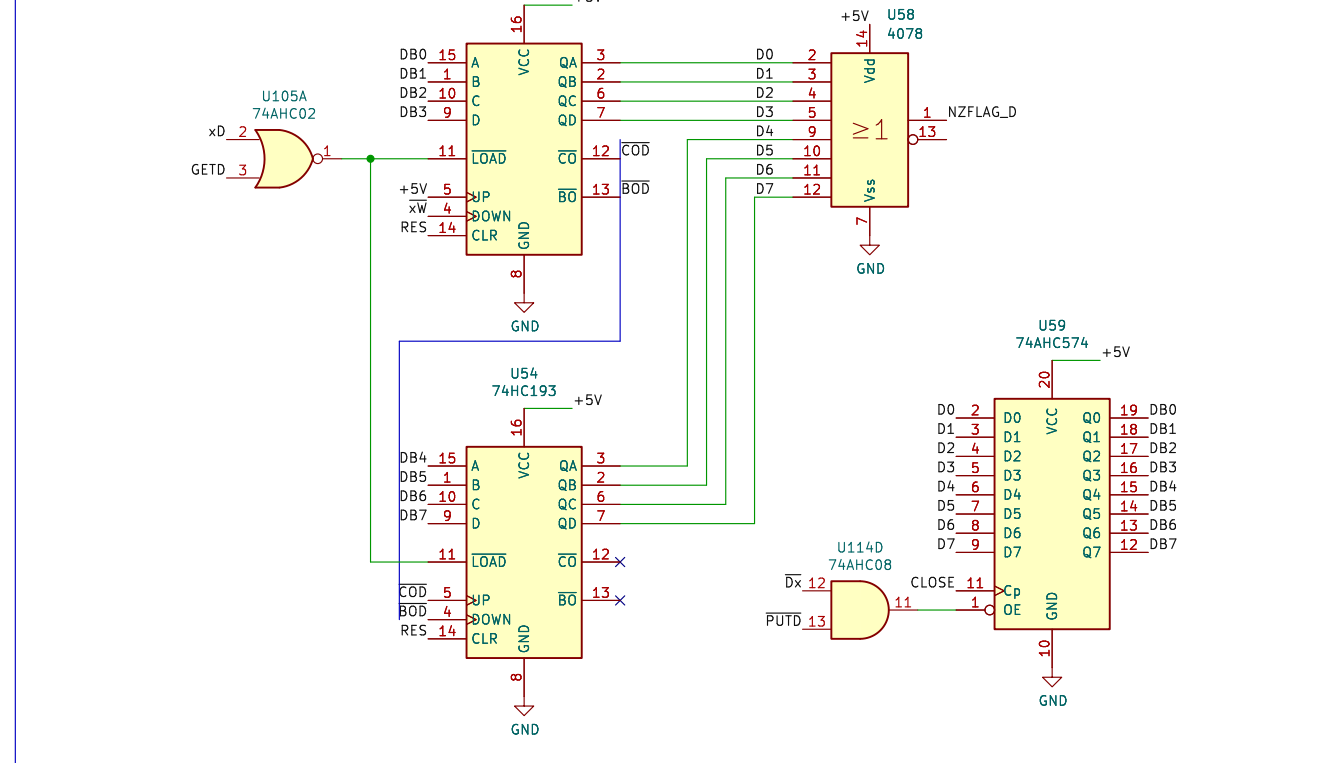
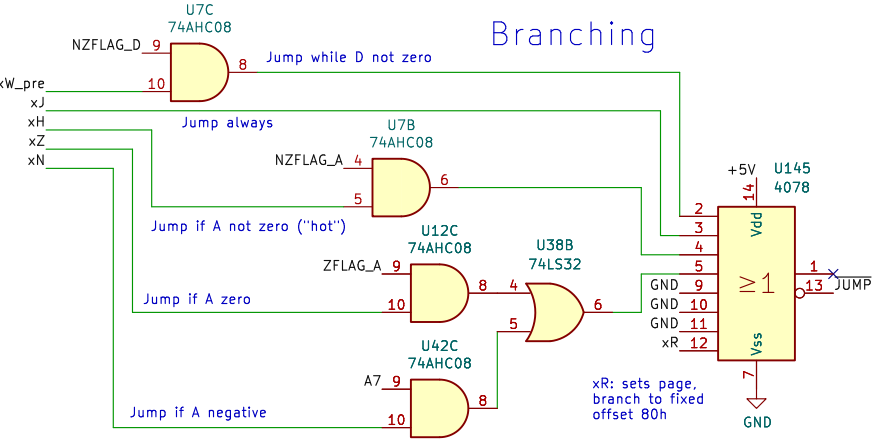


# Sonne-8 Microcontroller

## Reference Schematics

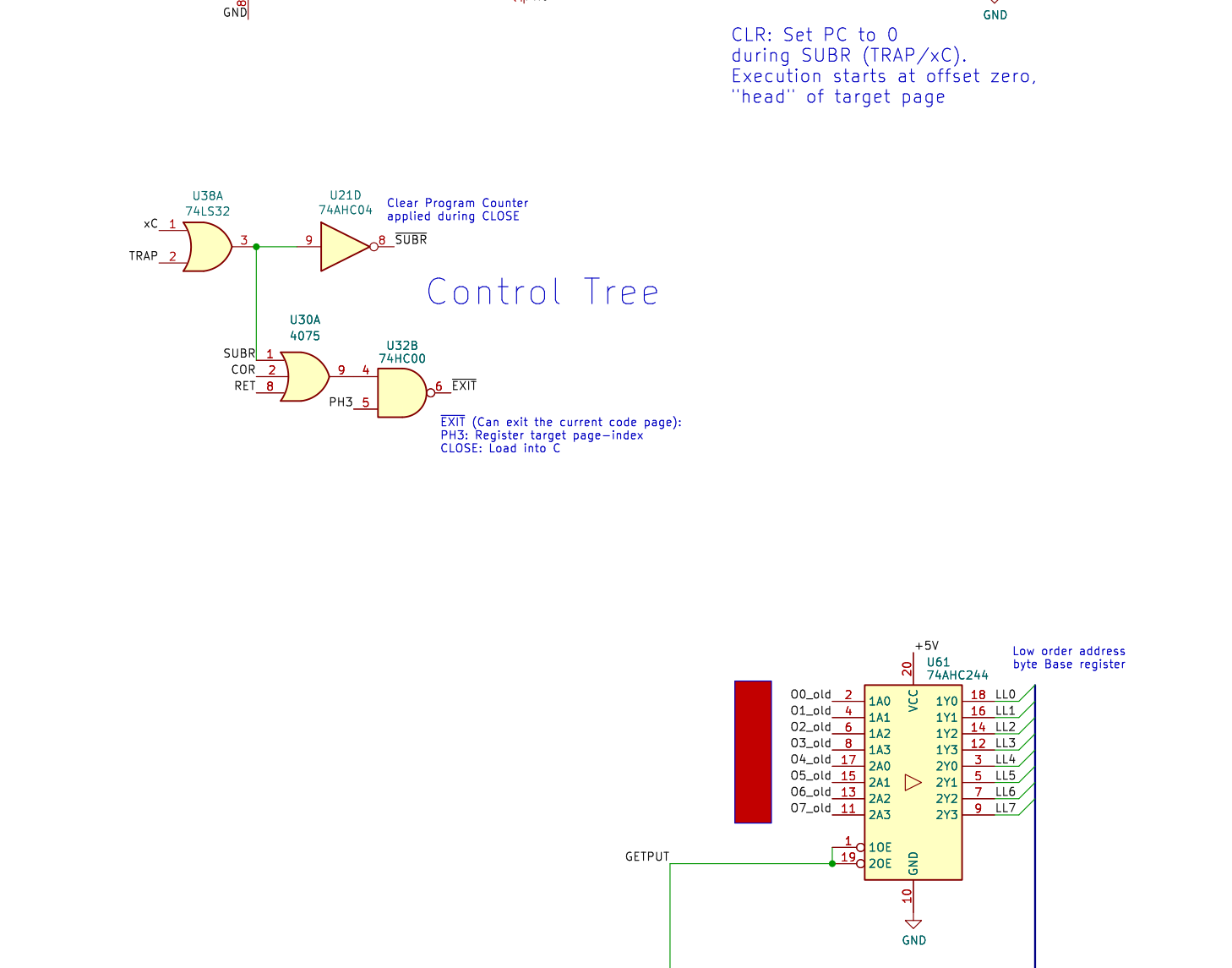
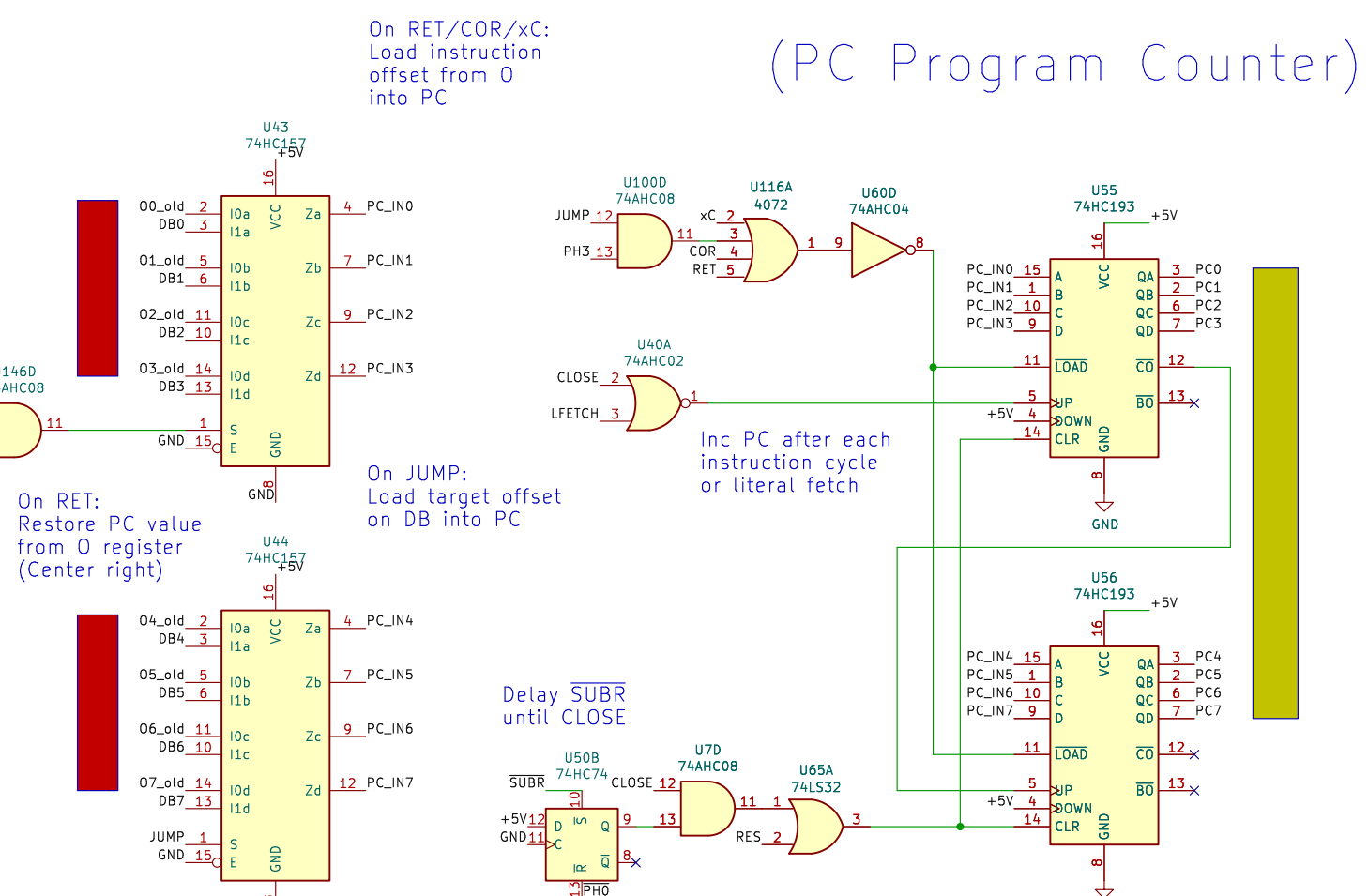
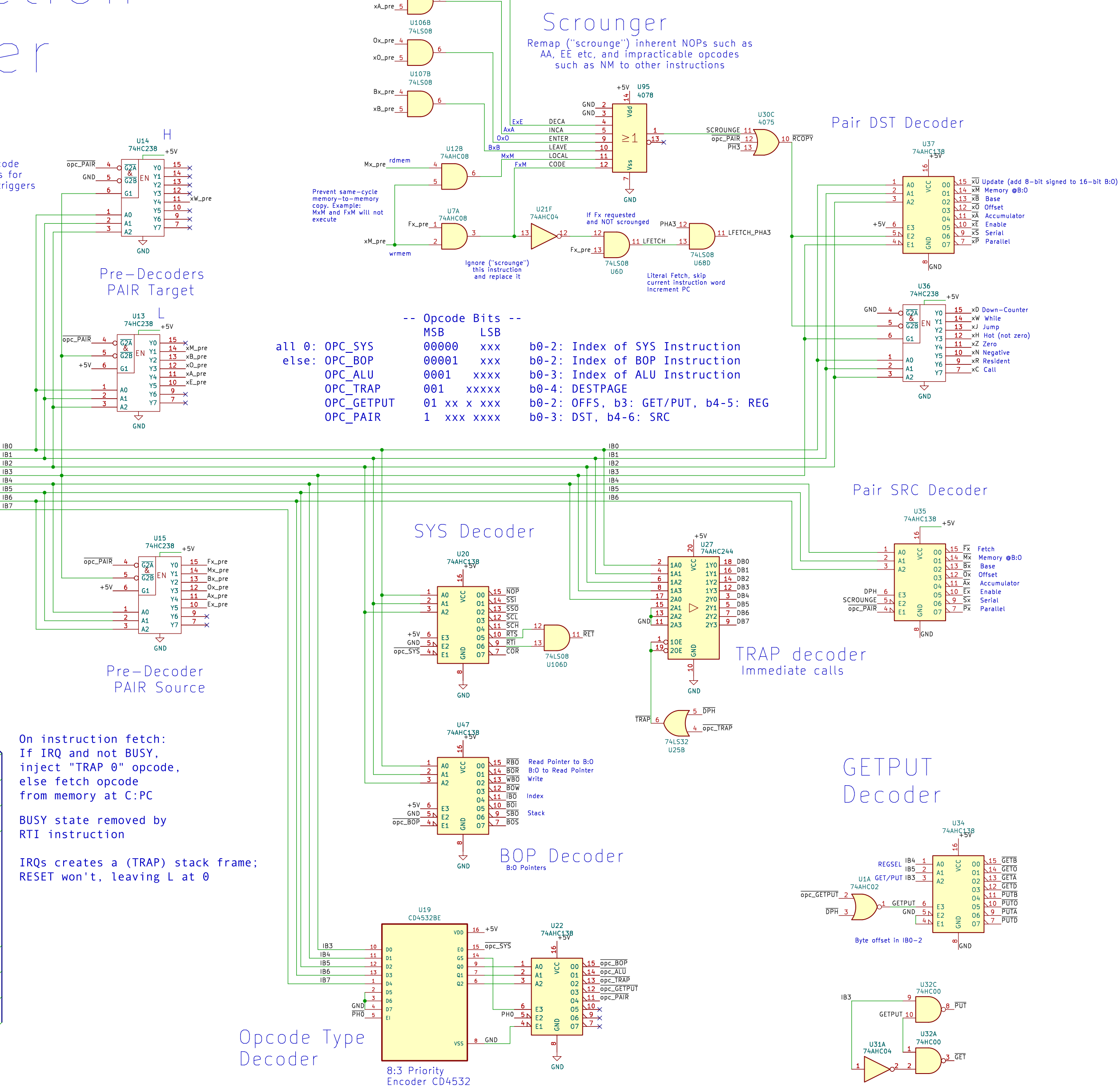
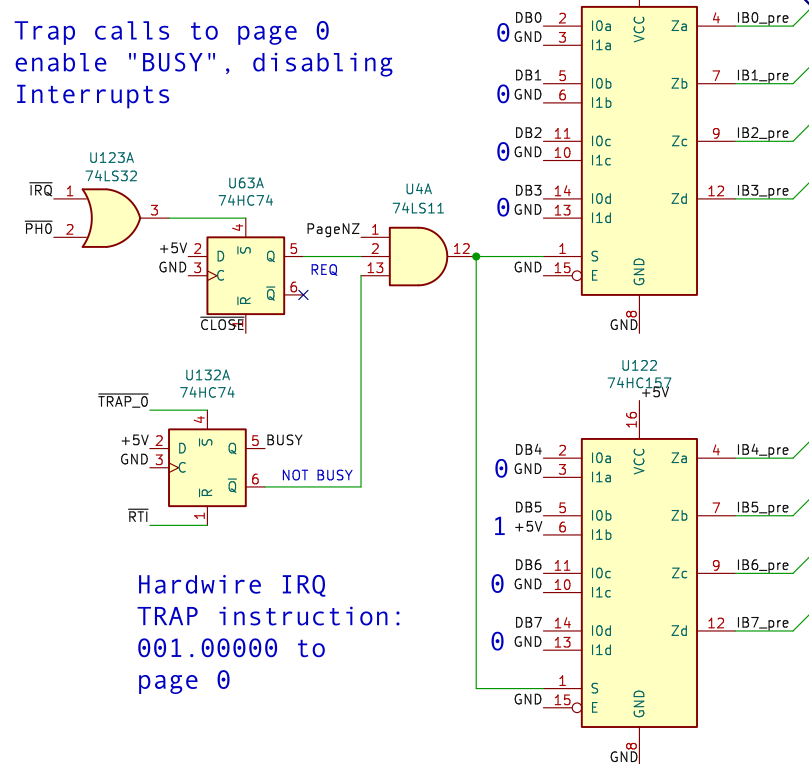
### Rev. Myth

## D(OWN) COUNTER Inner-loops

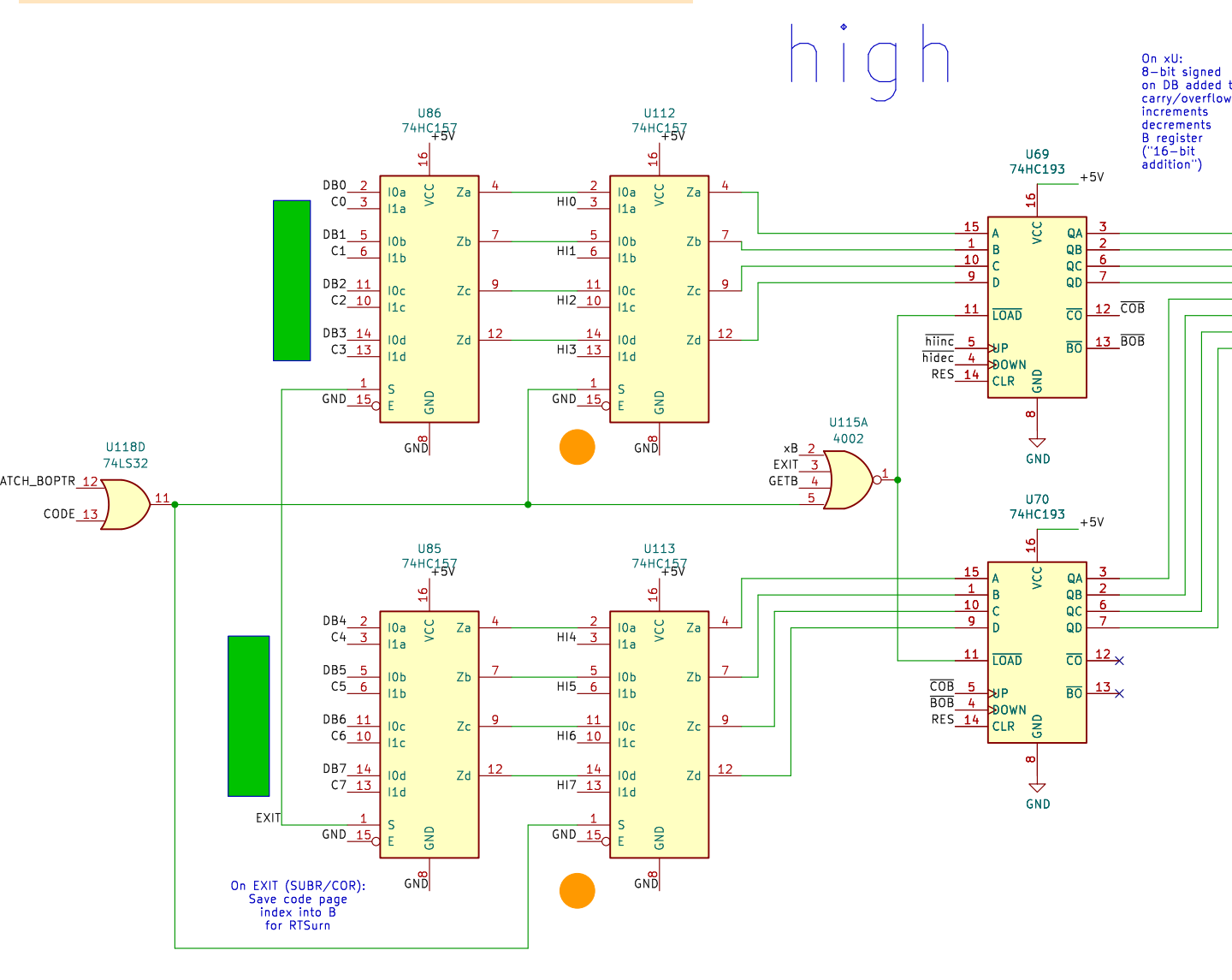
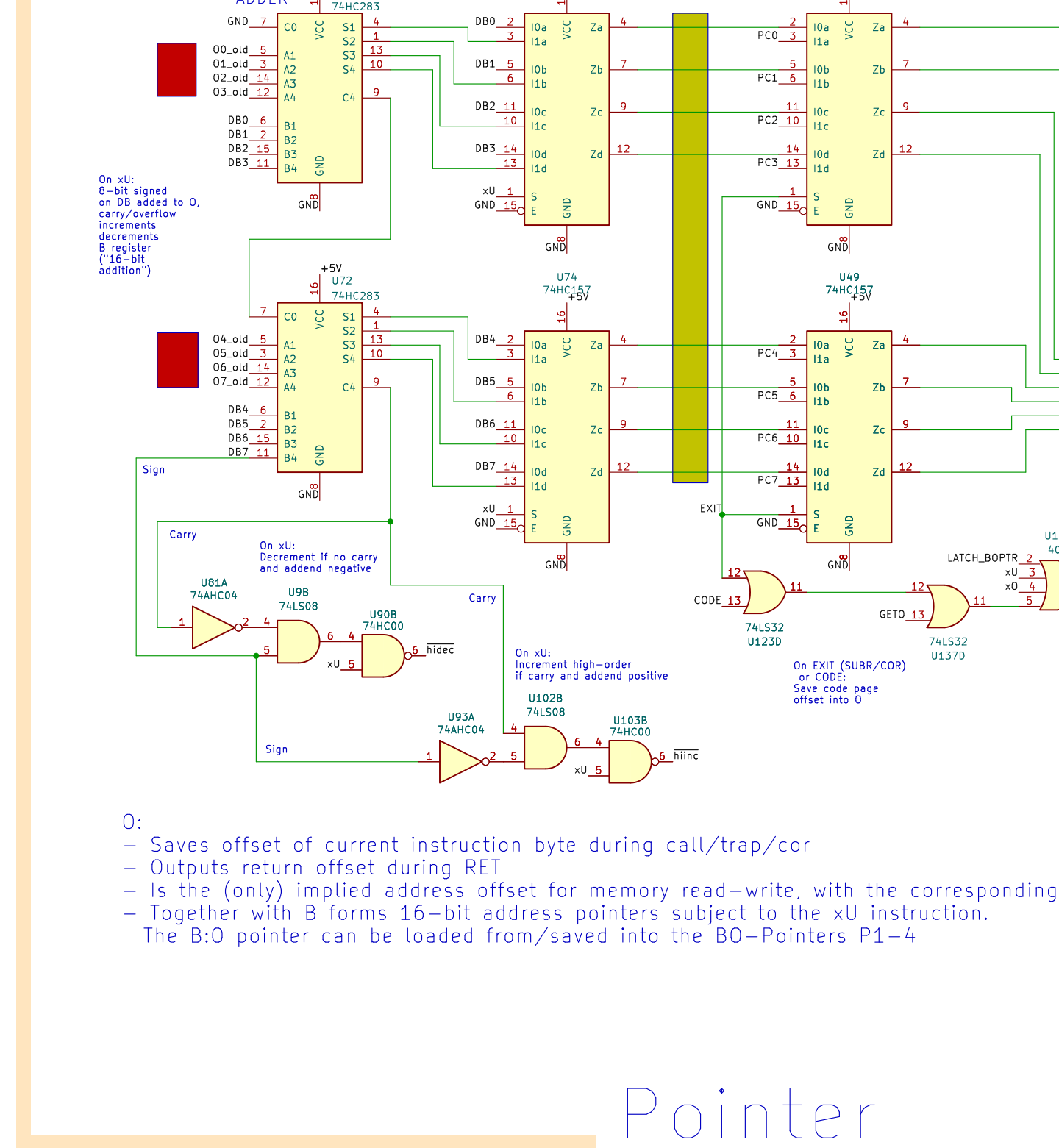


## Instruction Decoder

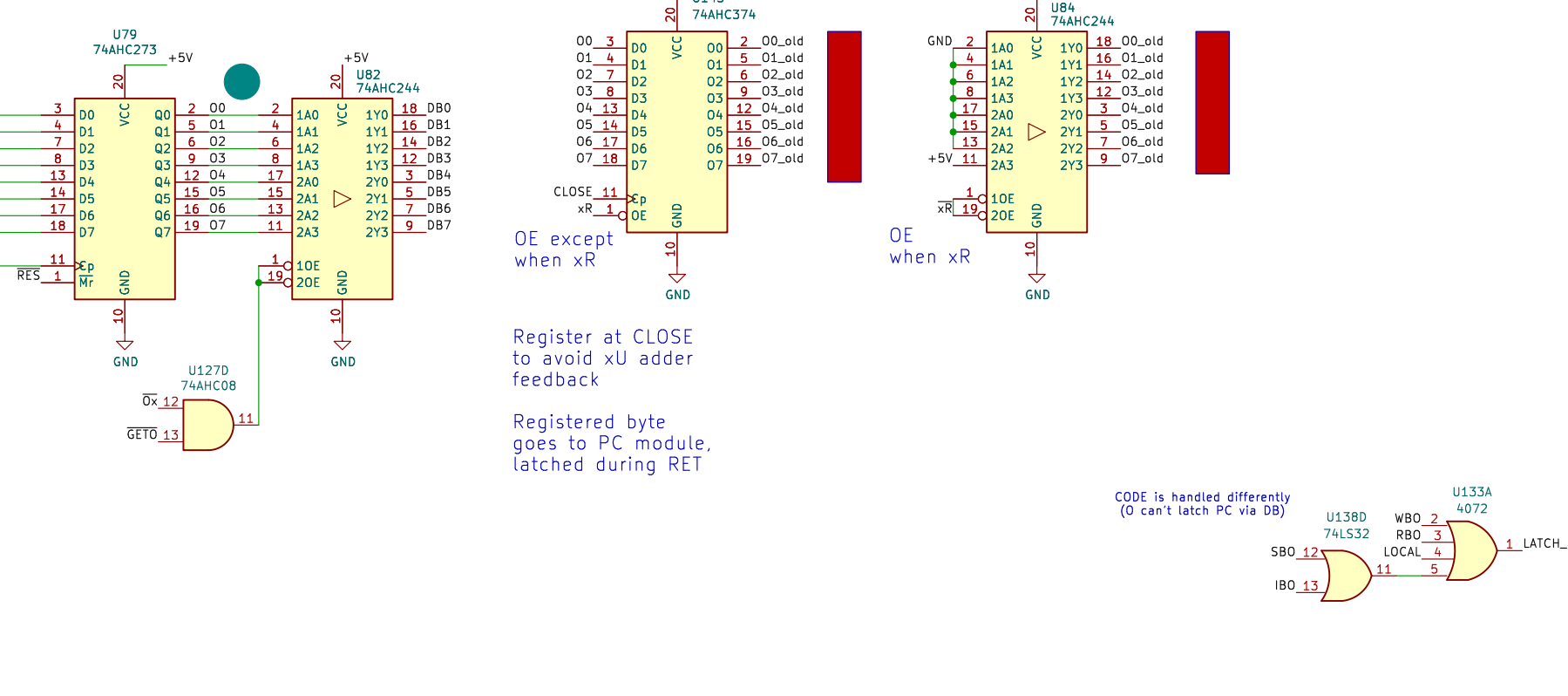
Start, latch instruction or interrupt vector



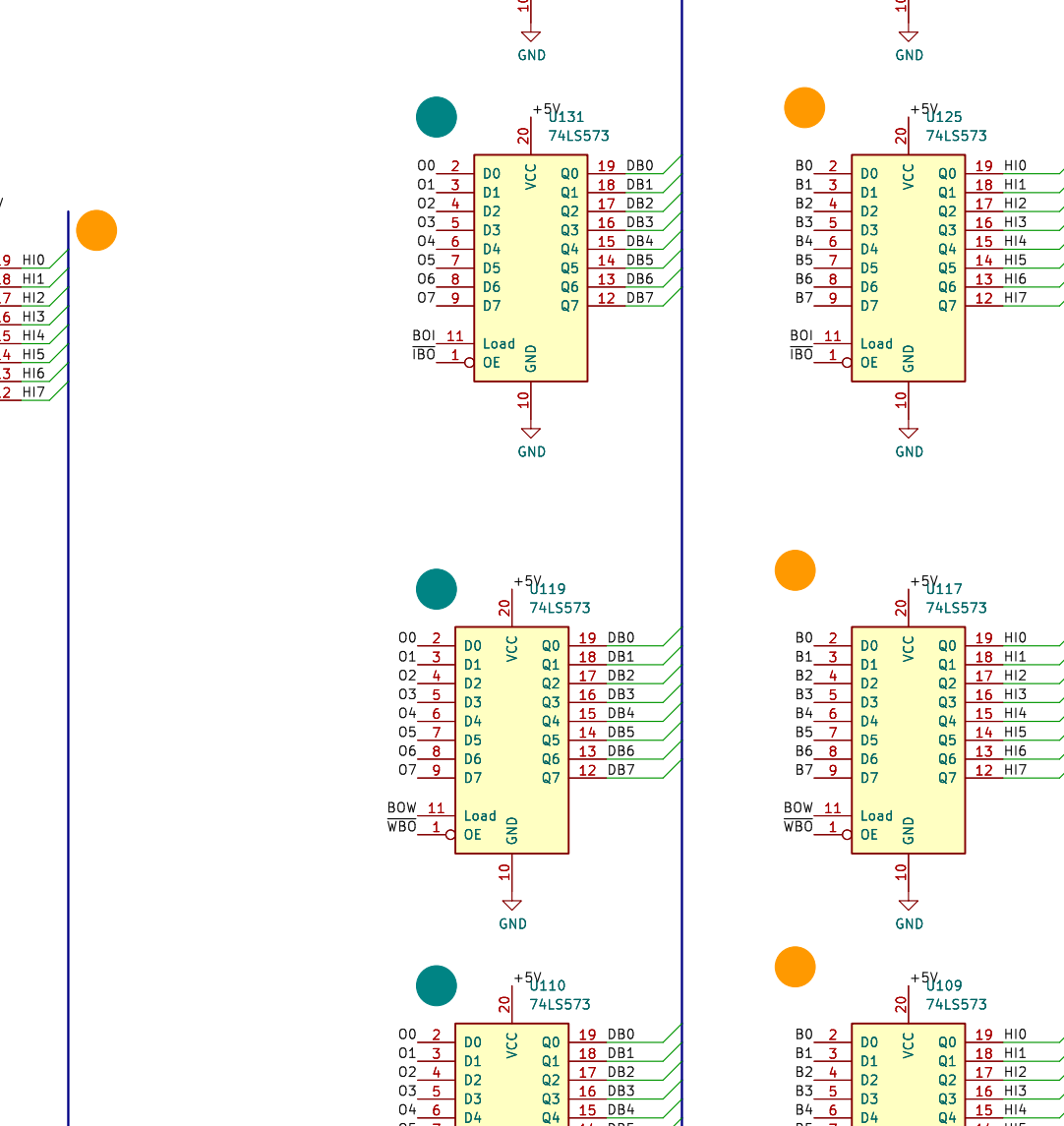
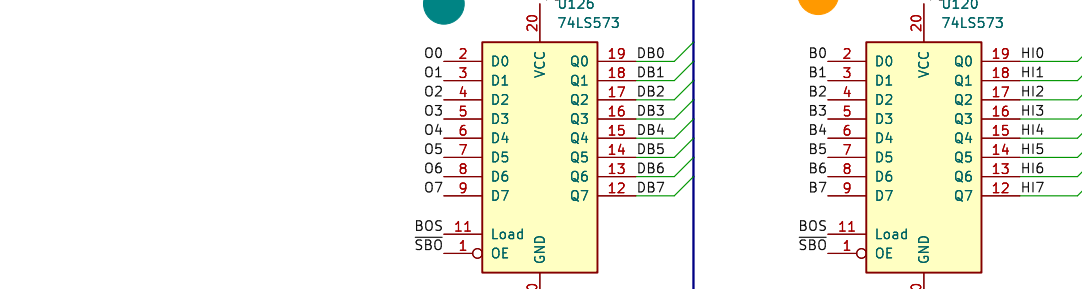
## Pointer low



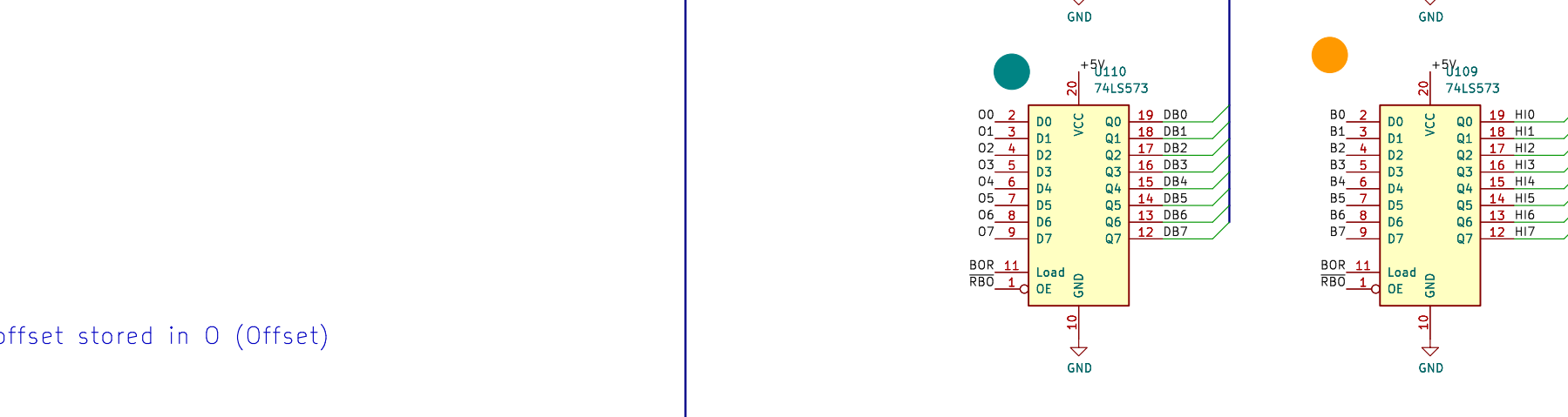
## O (Page Offset Register)



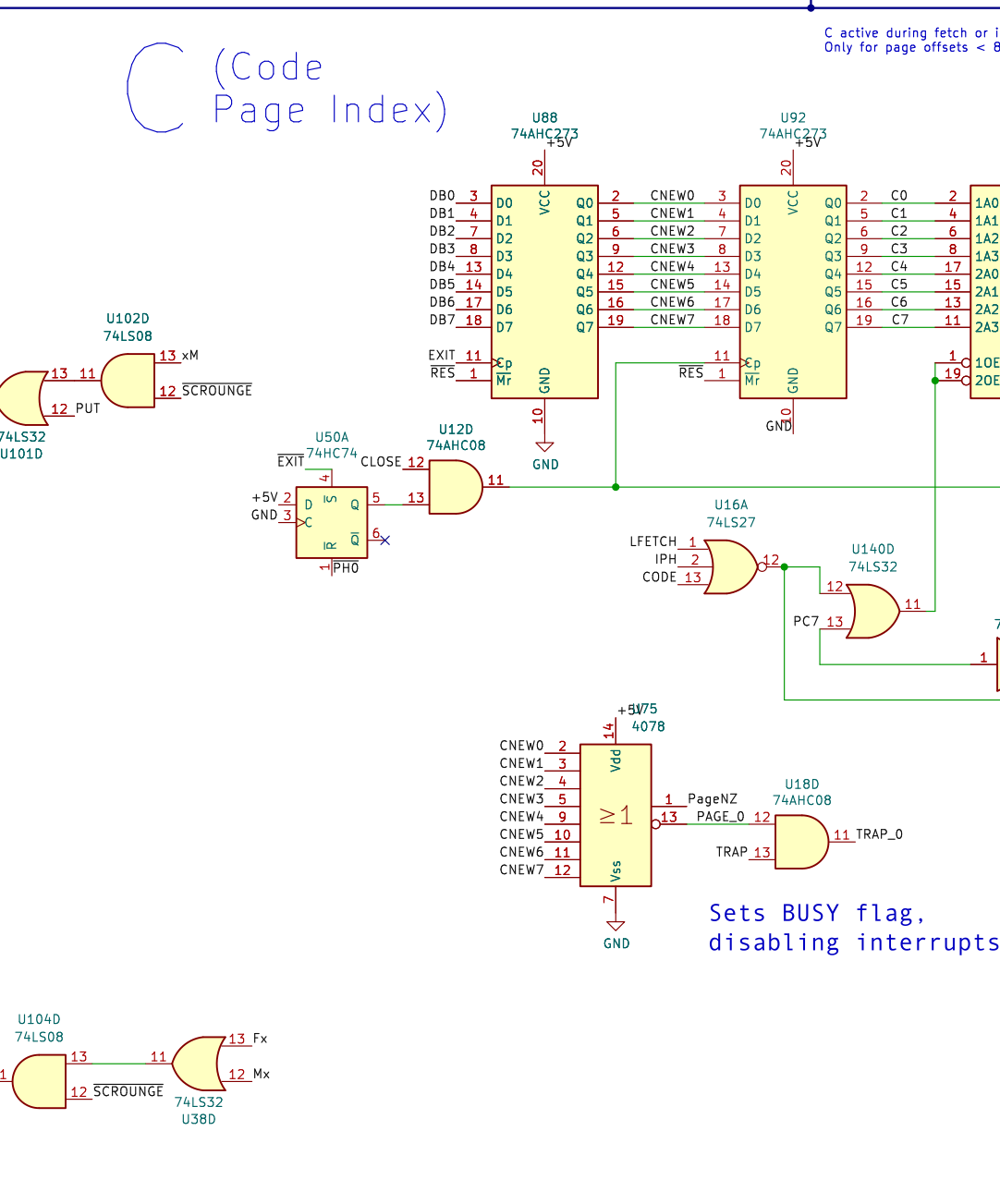
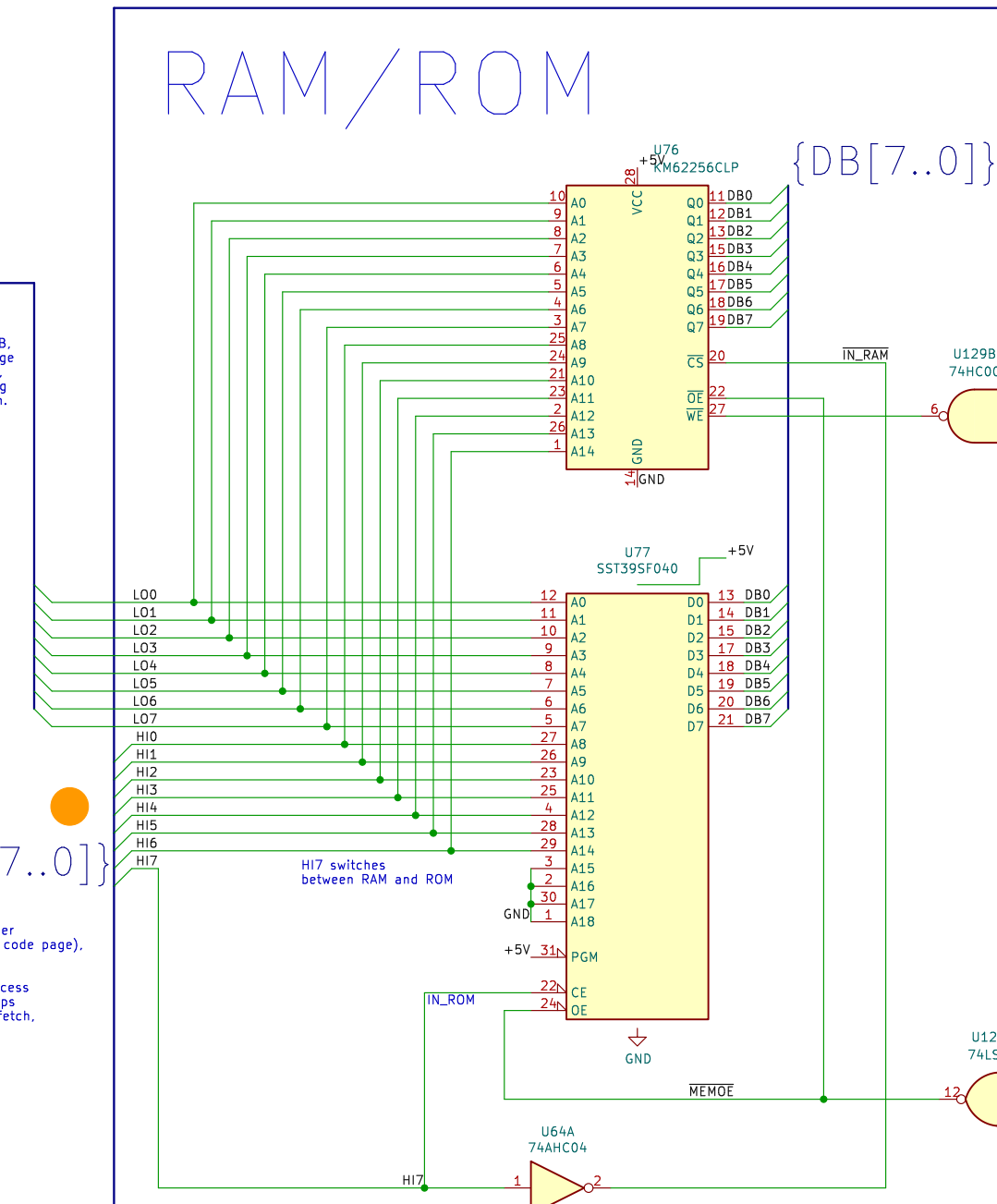
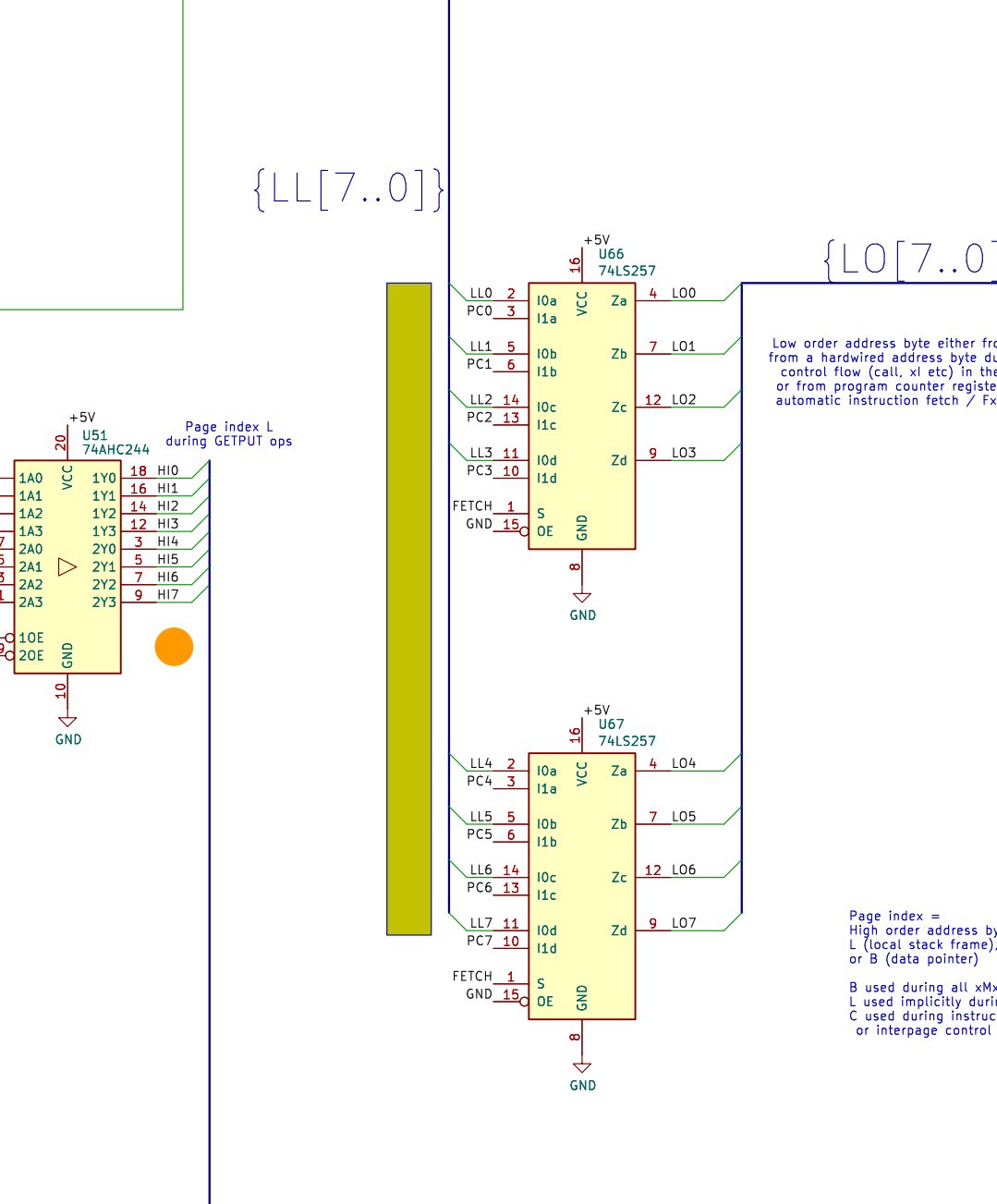
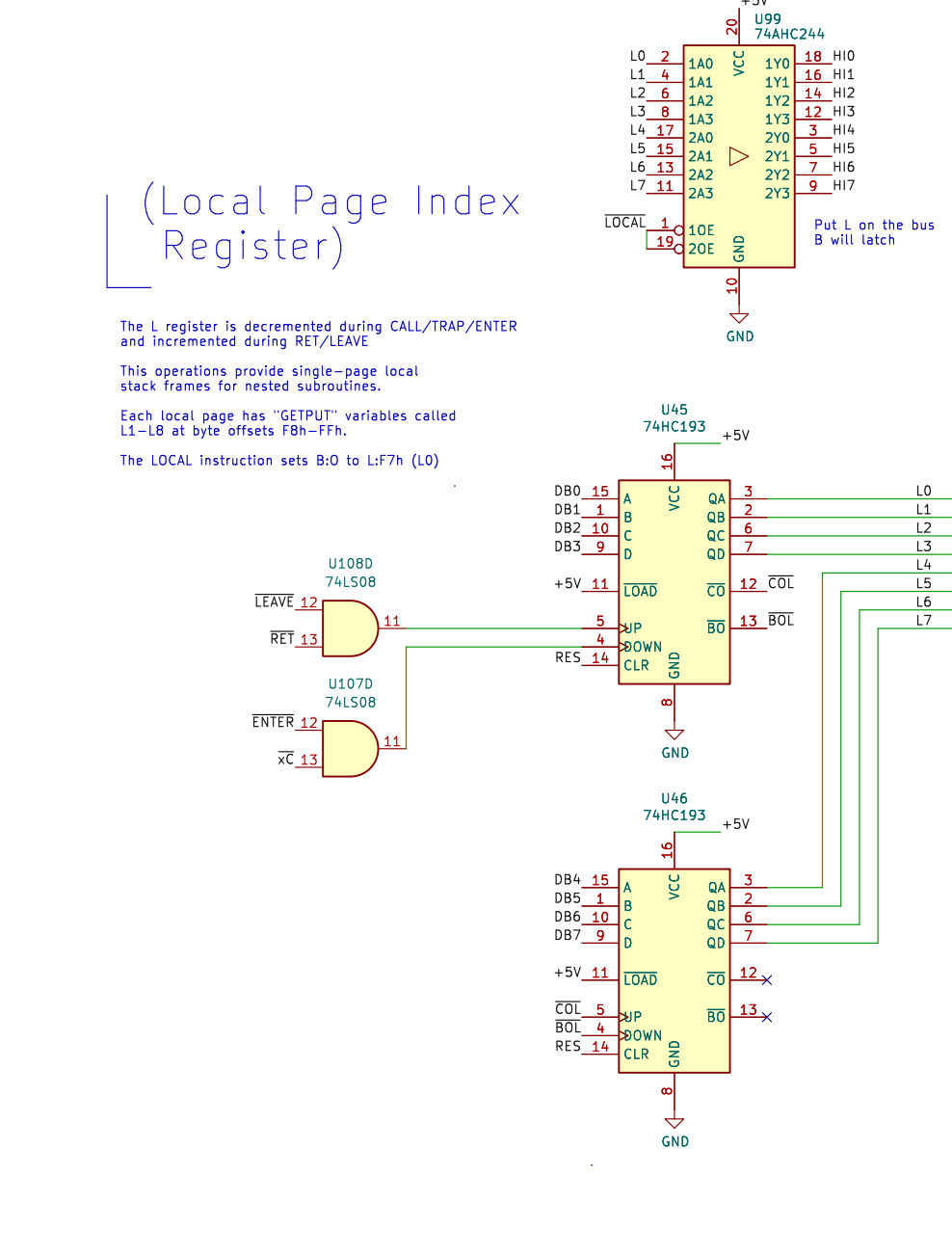
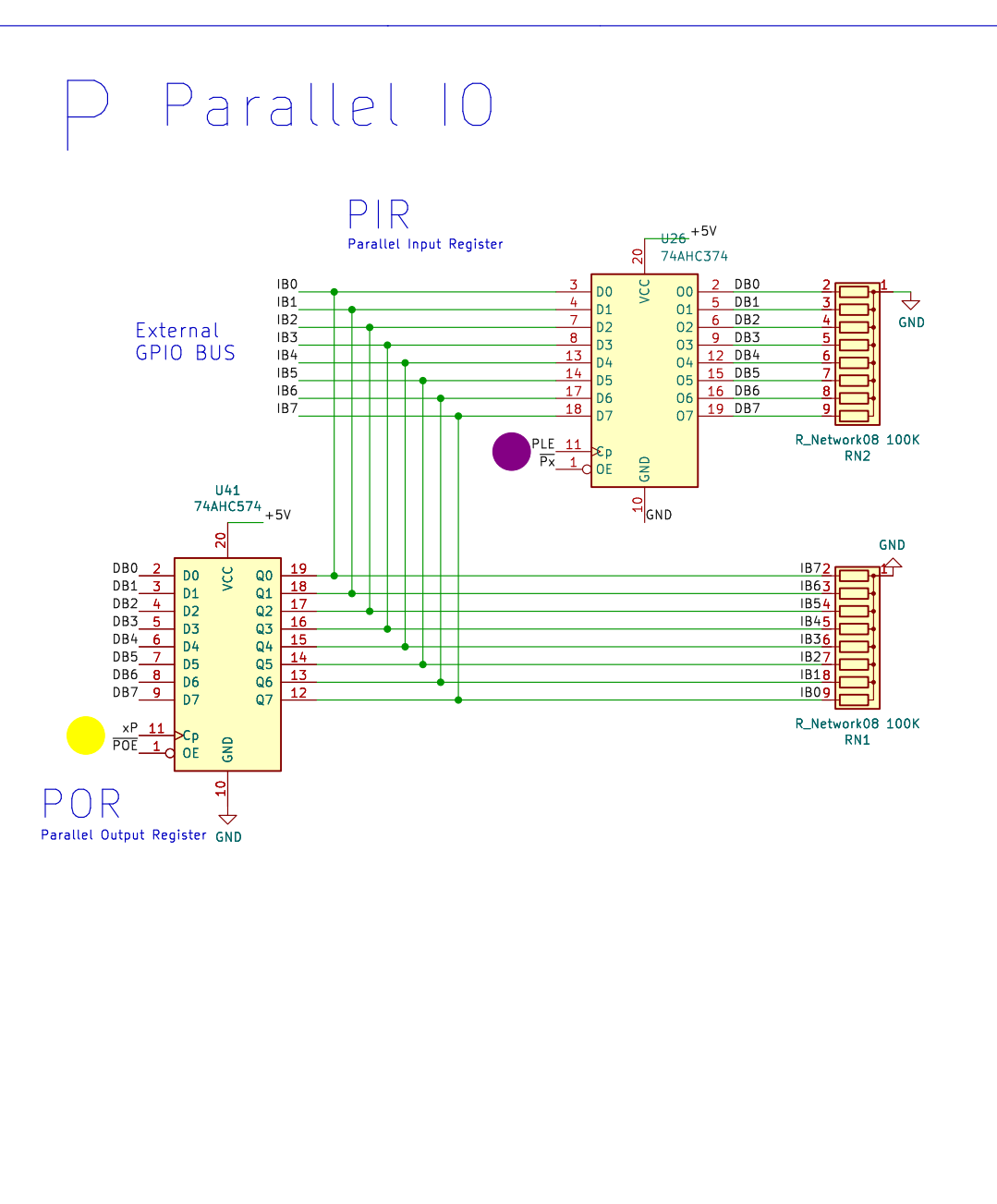
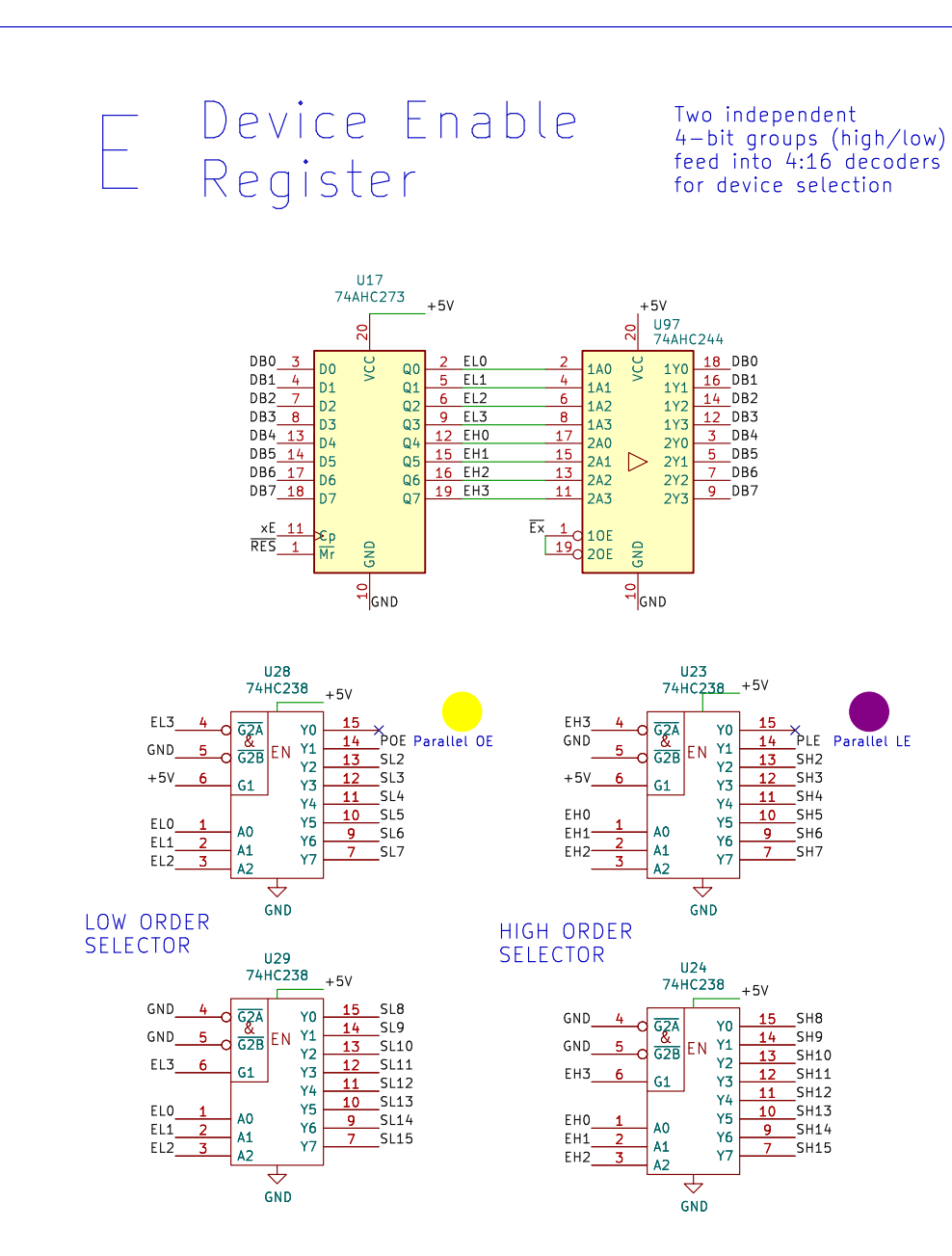
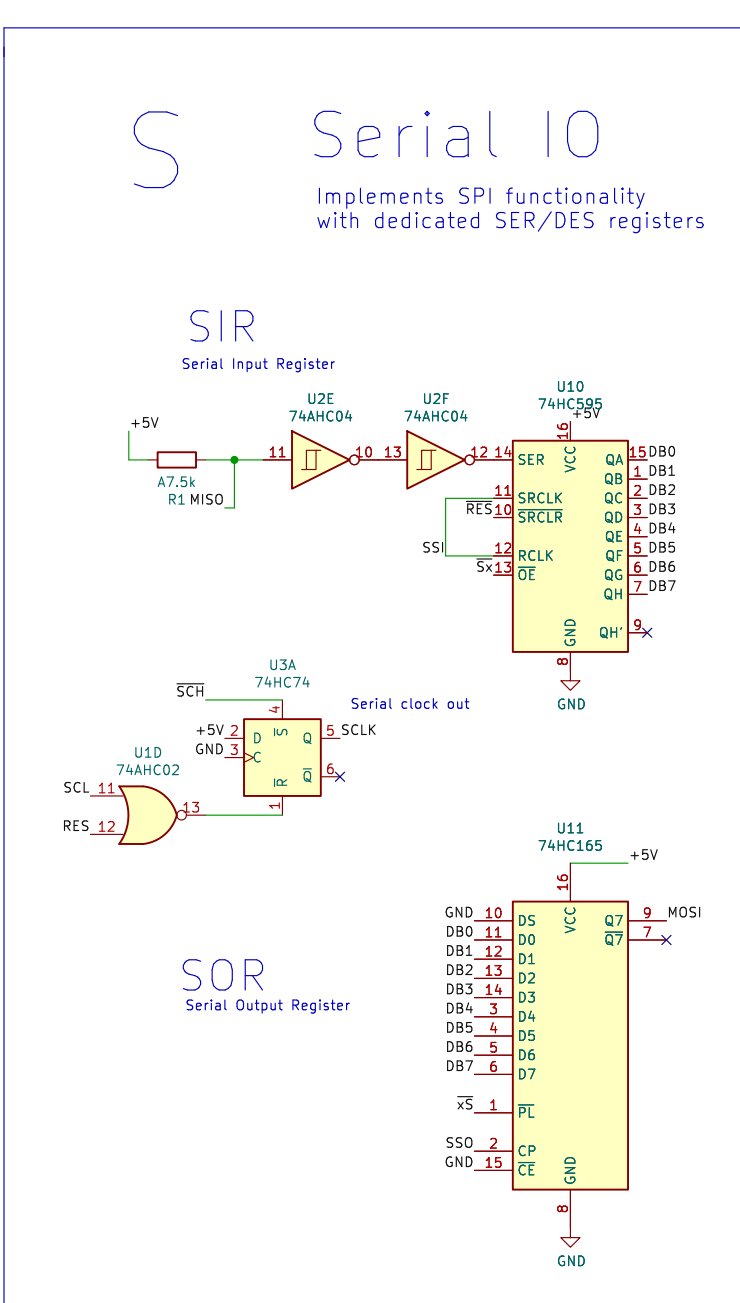
## BO Register



## B (Base) Page Index



## Input/Output



## RAM/ROM

