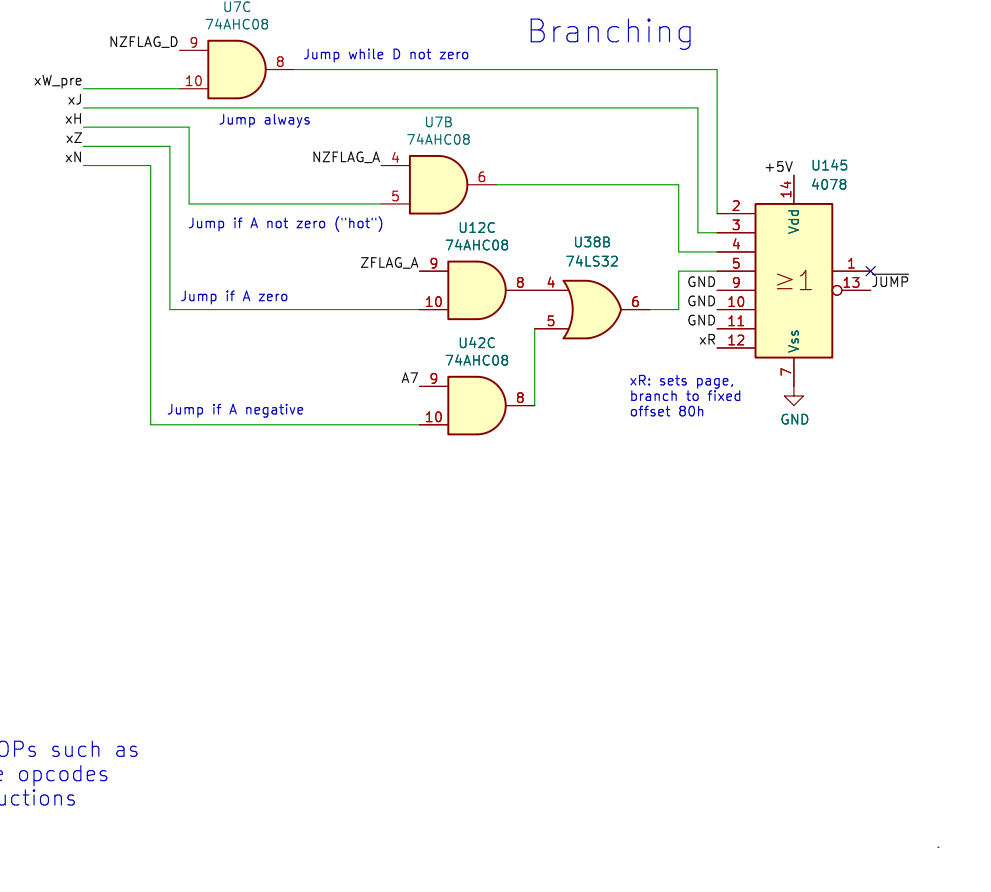
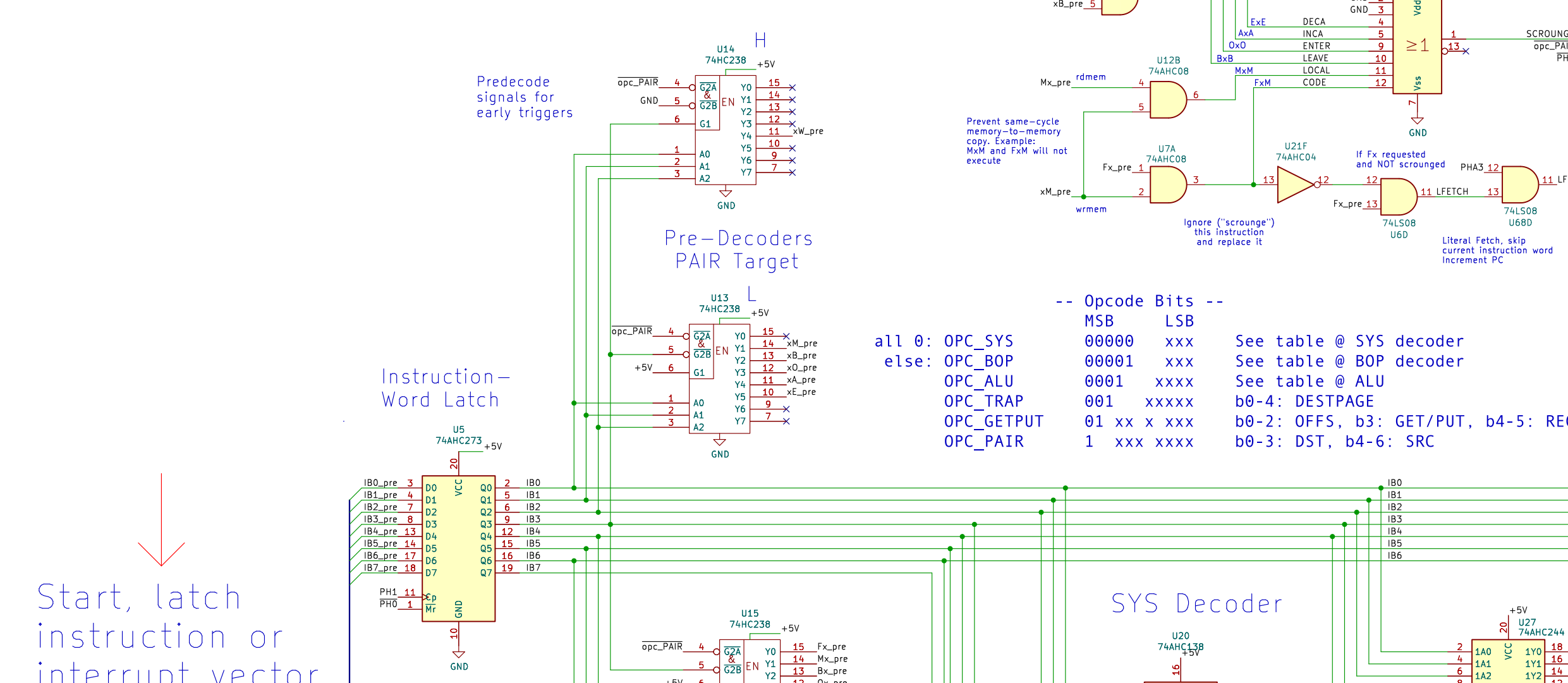
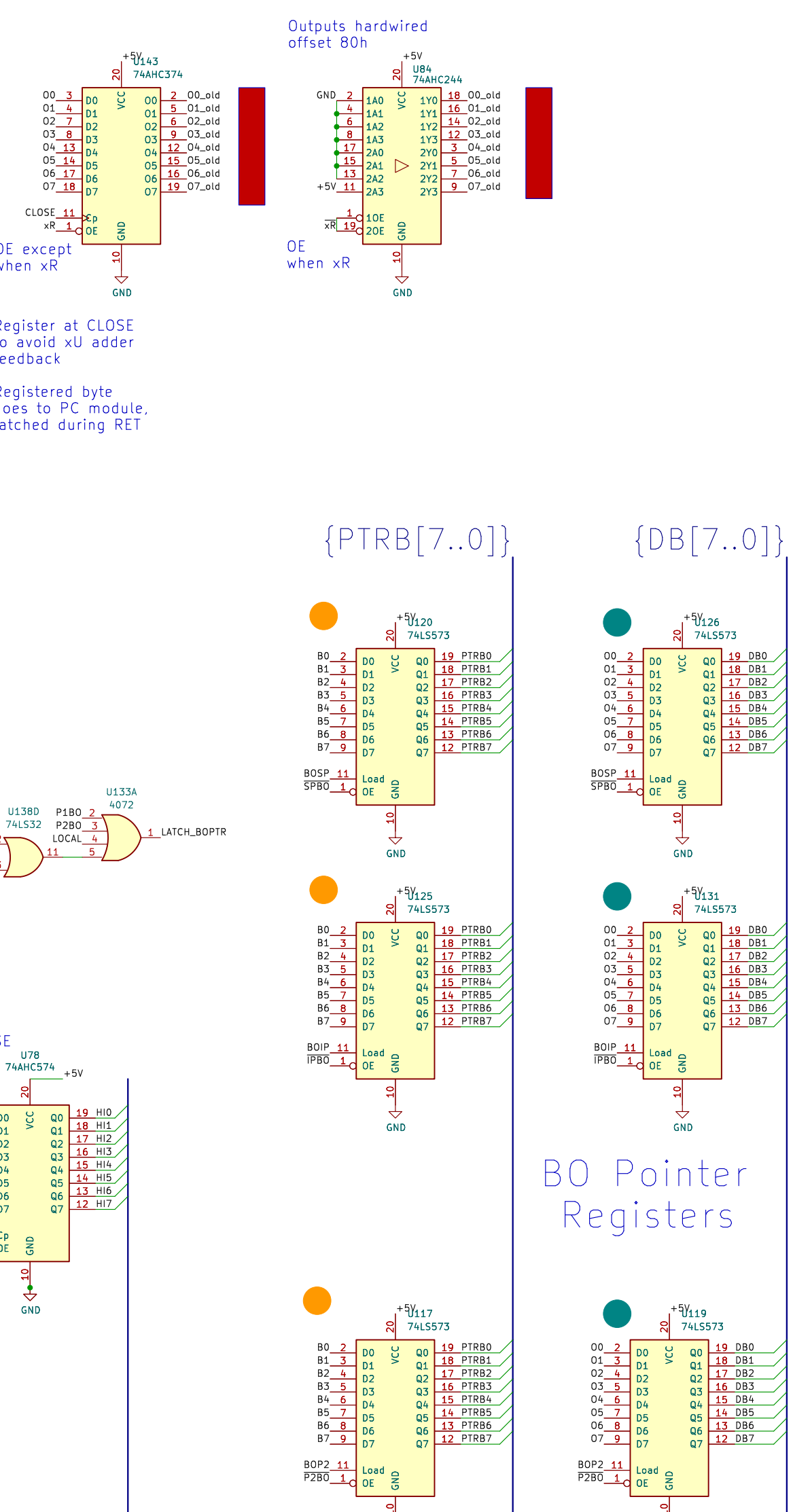
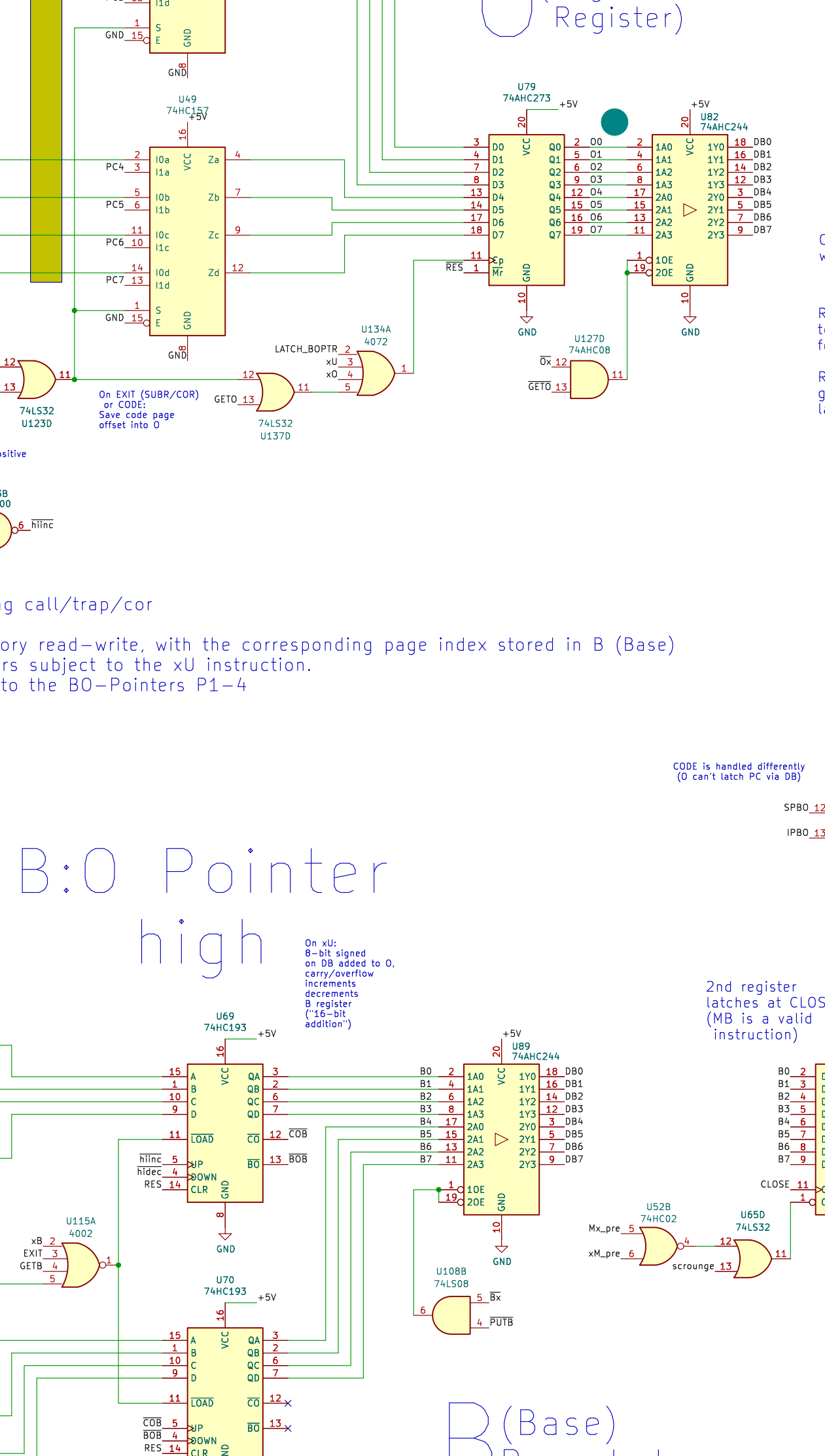
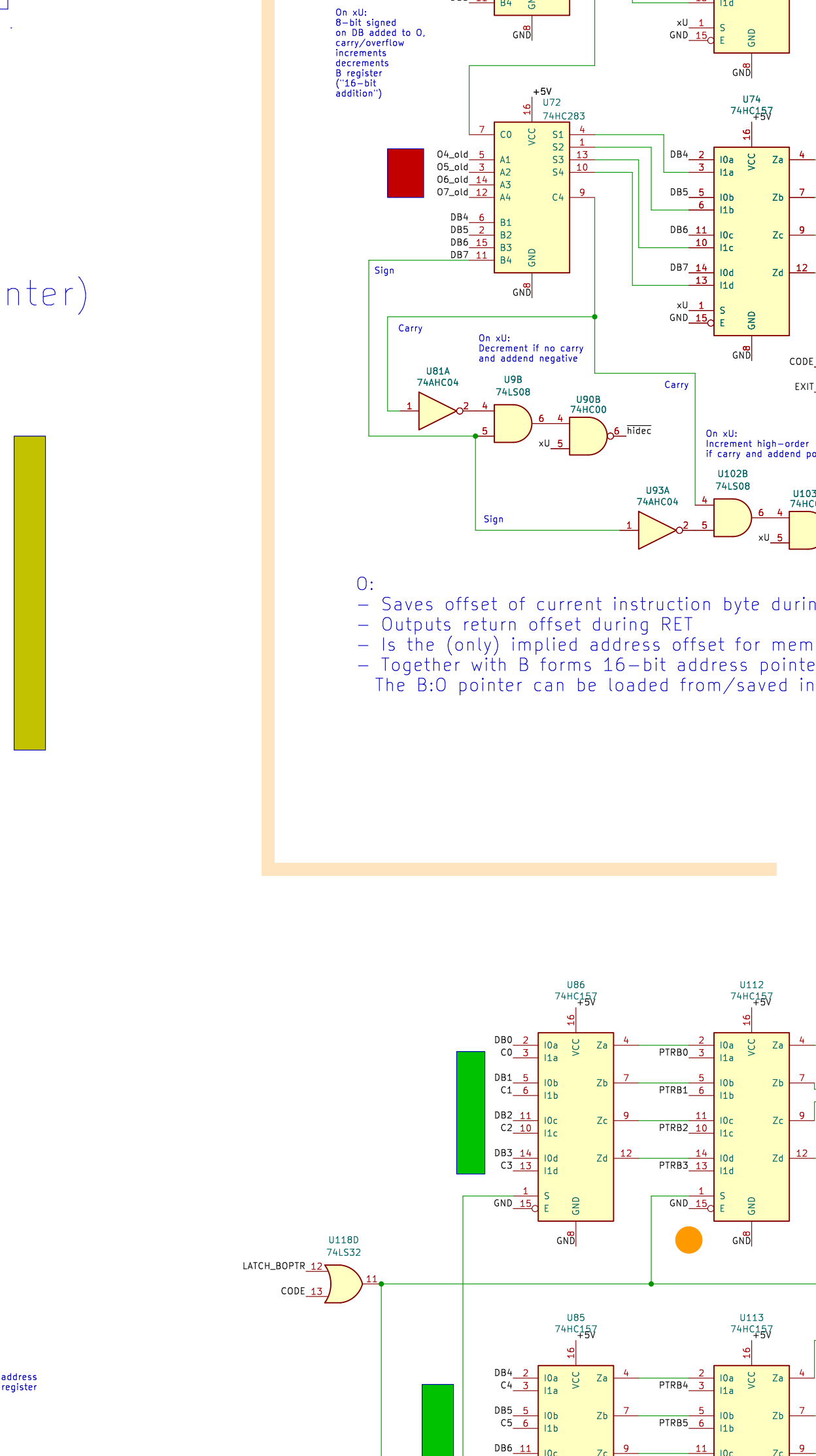
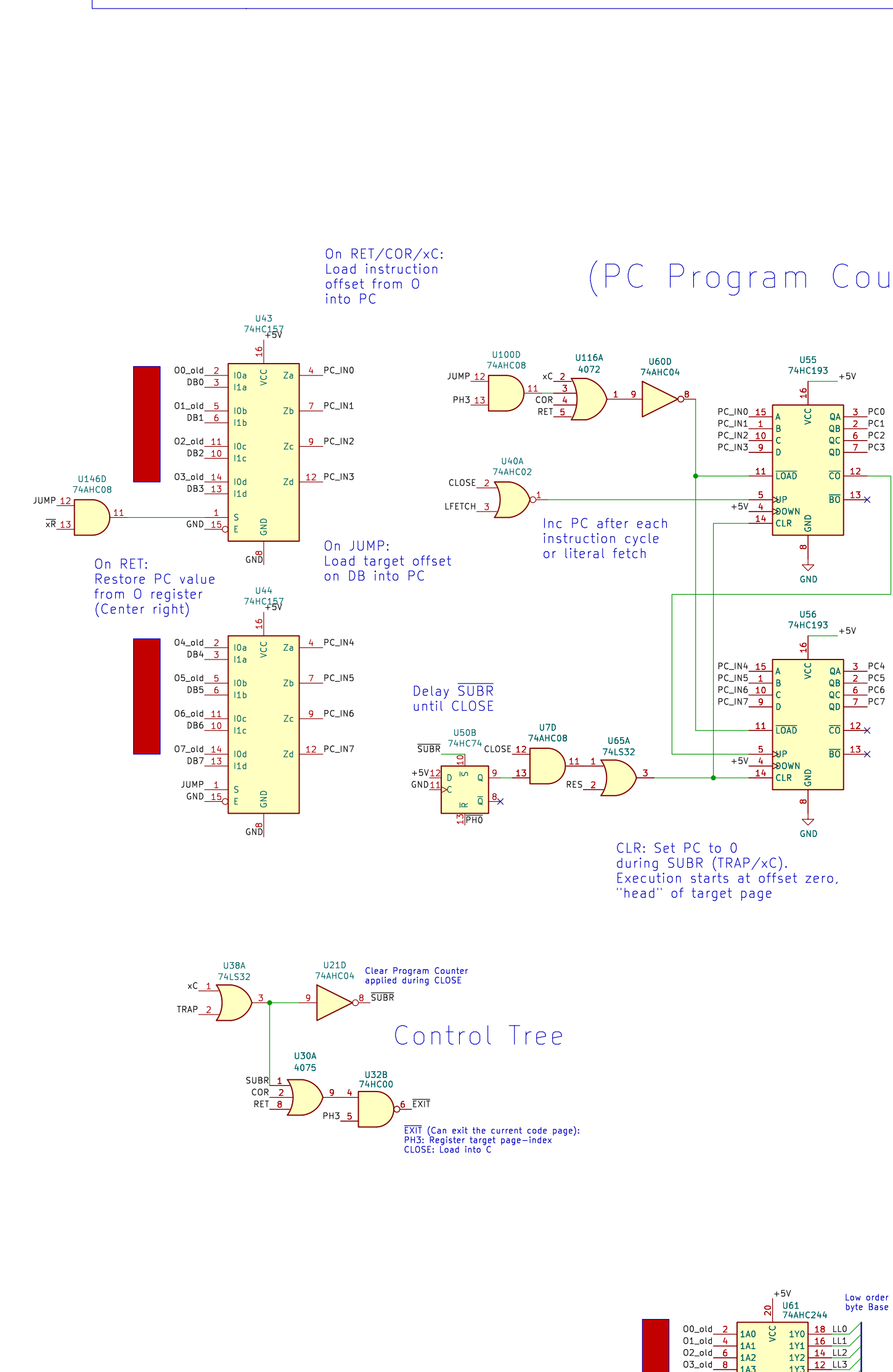
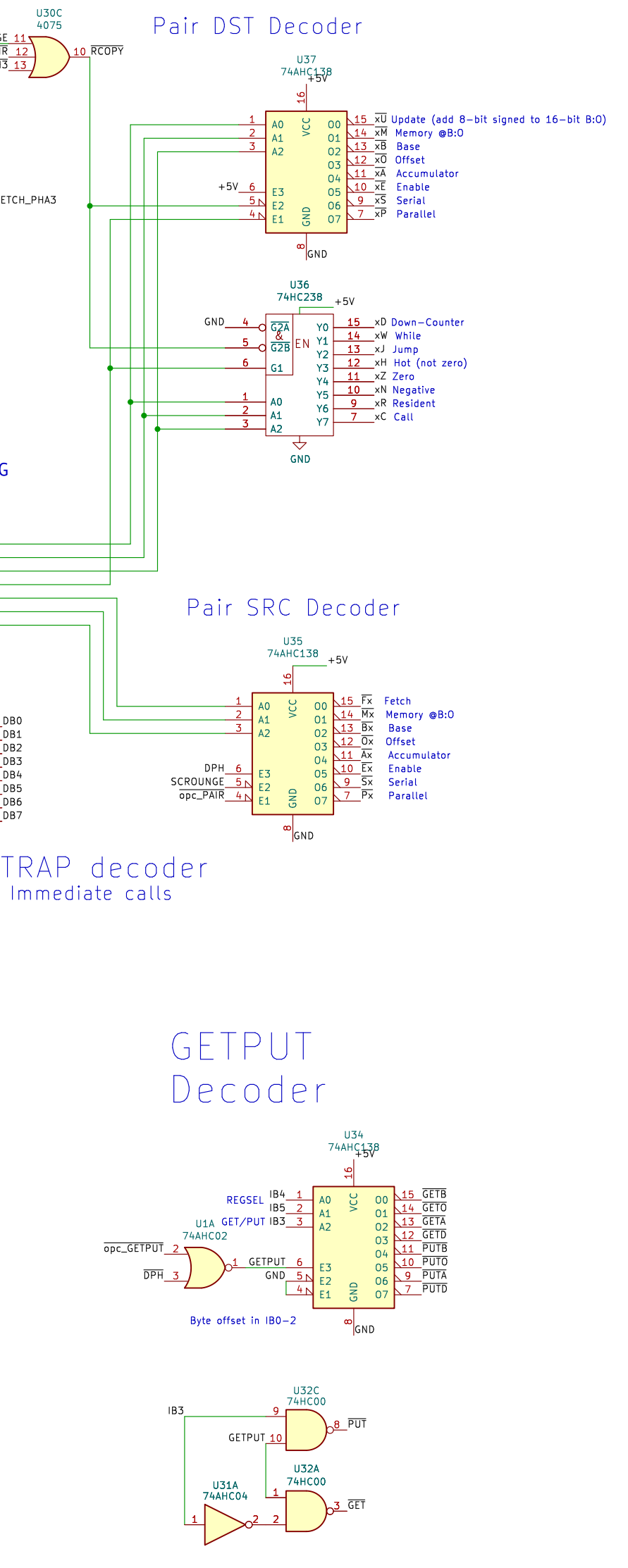
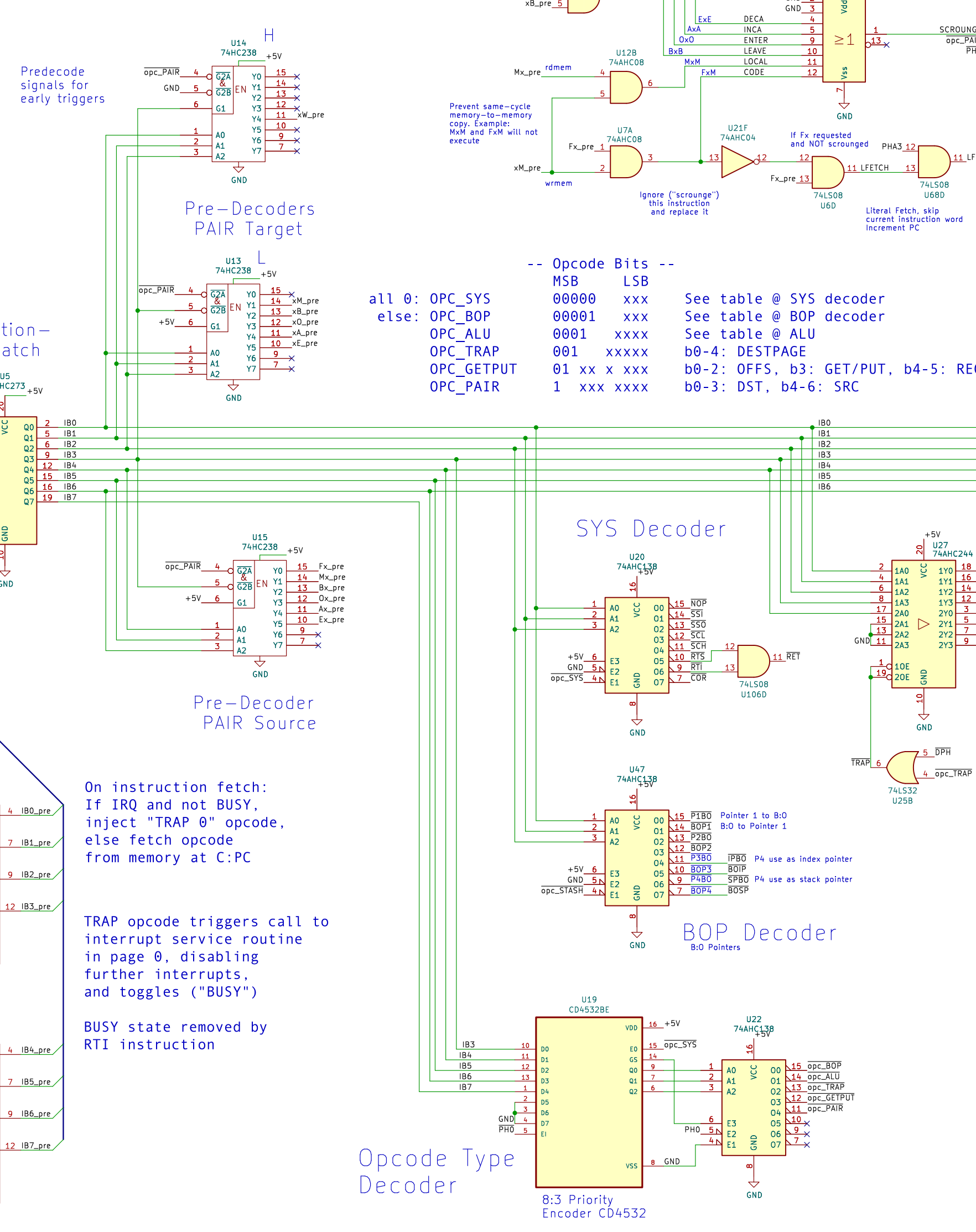
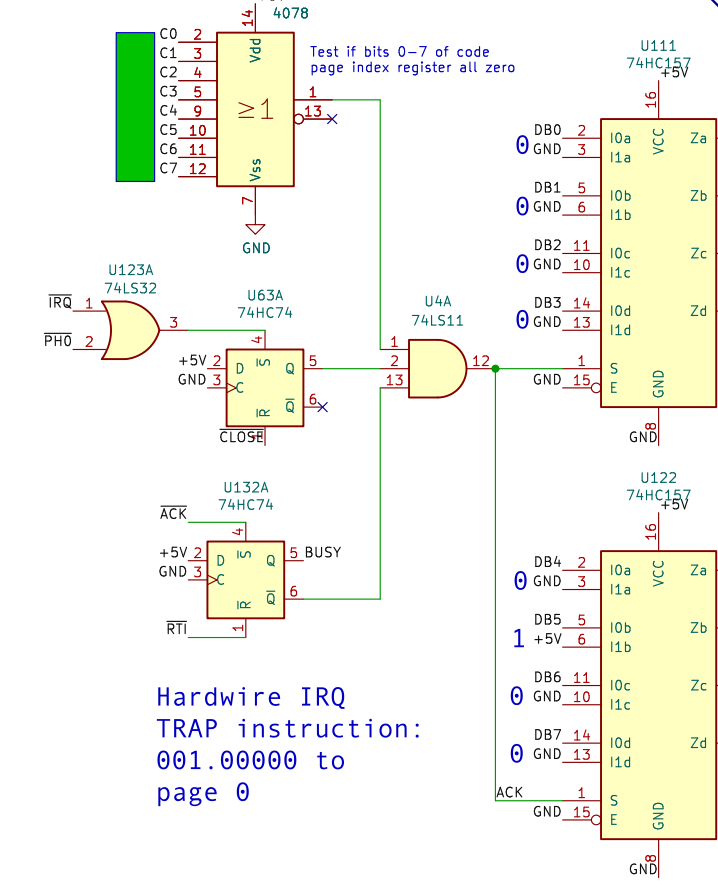
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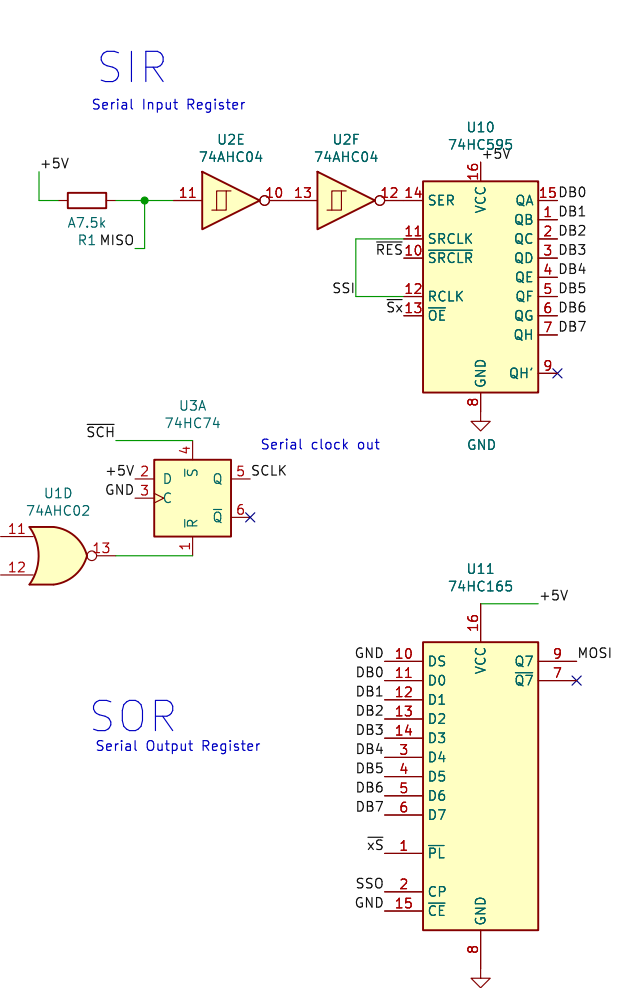
Scrounger



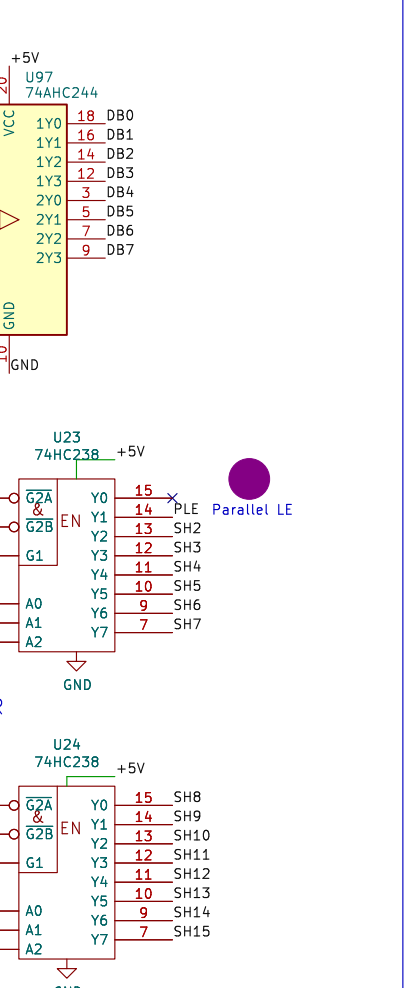
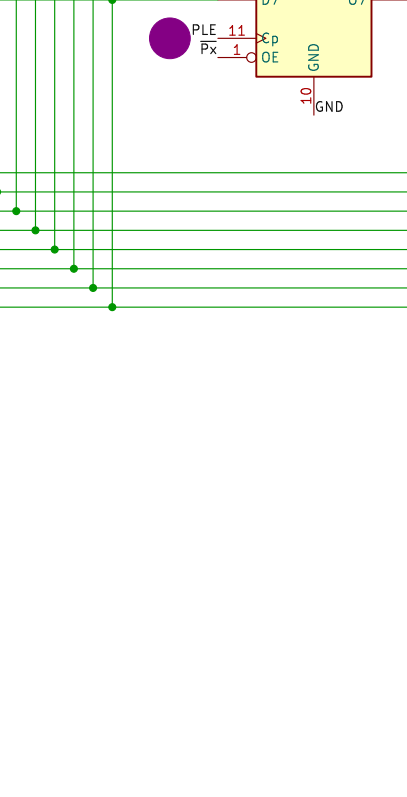
Interrupts disabled when "BUSY"
and when running code in page 0
(See code page index reg
schematics bottom right)



S Serial IO
Implements SPI functionality
with dedicated SER/DES registers



Two independent
4-bit groups (high/low)
feed into 4:16 decoders
for device selection

[illegible]

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