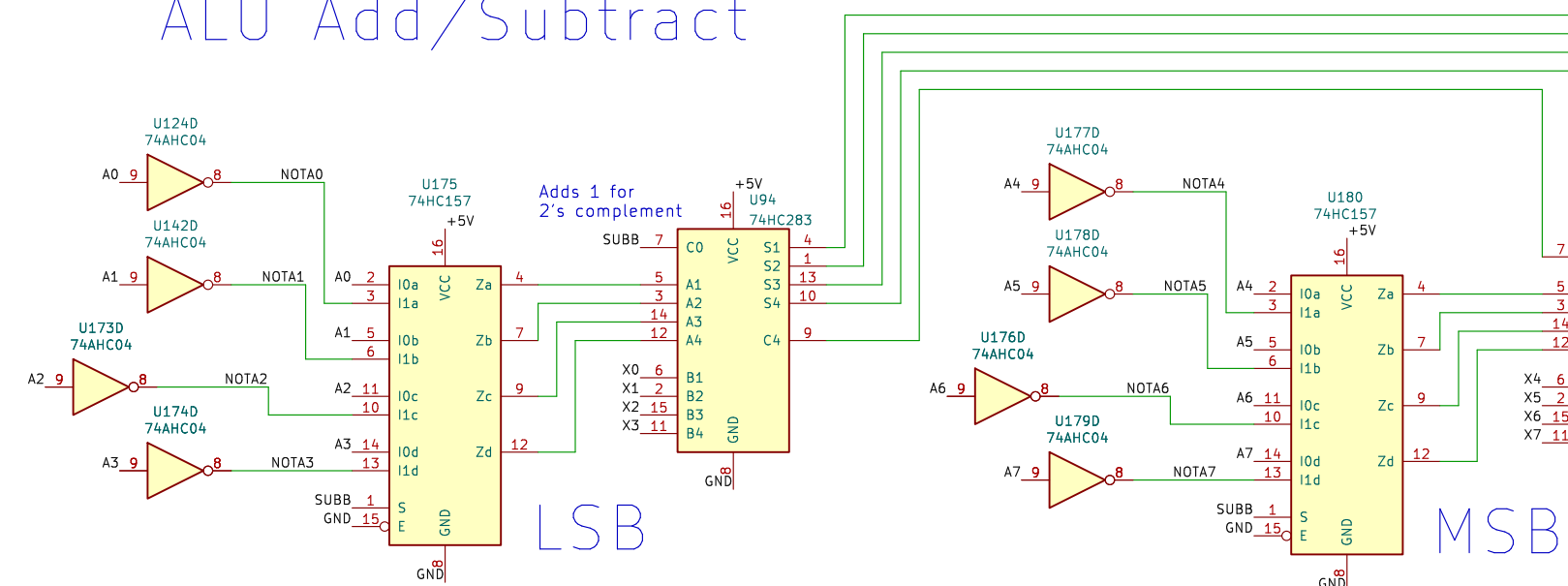
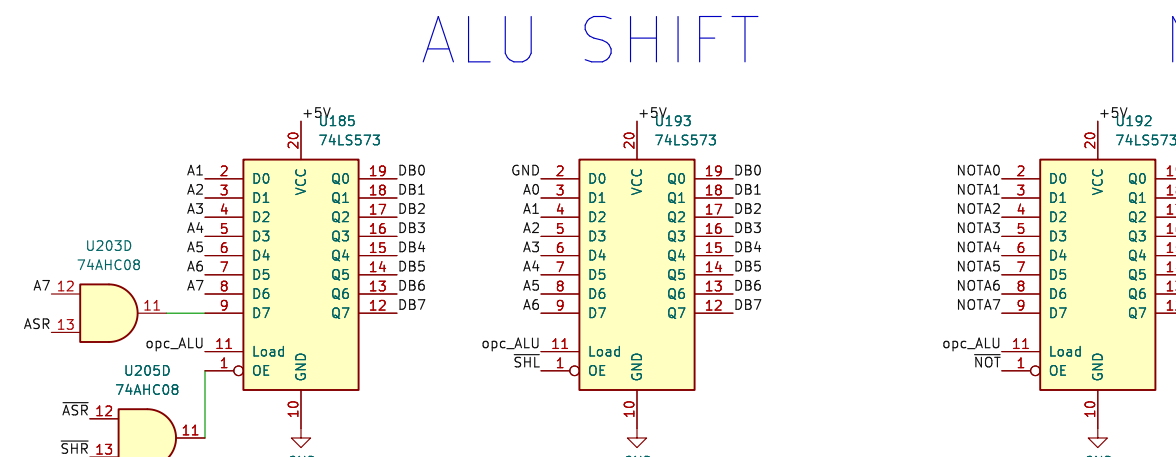


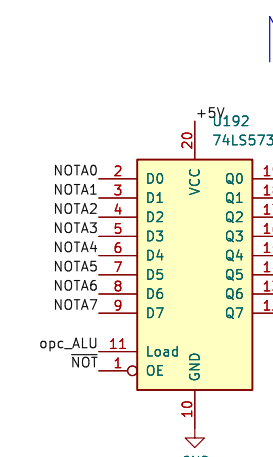
ALU Add/Subtract



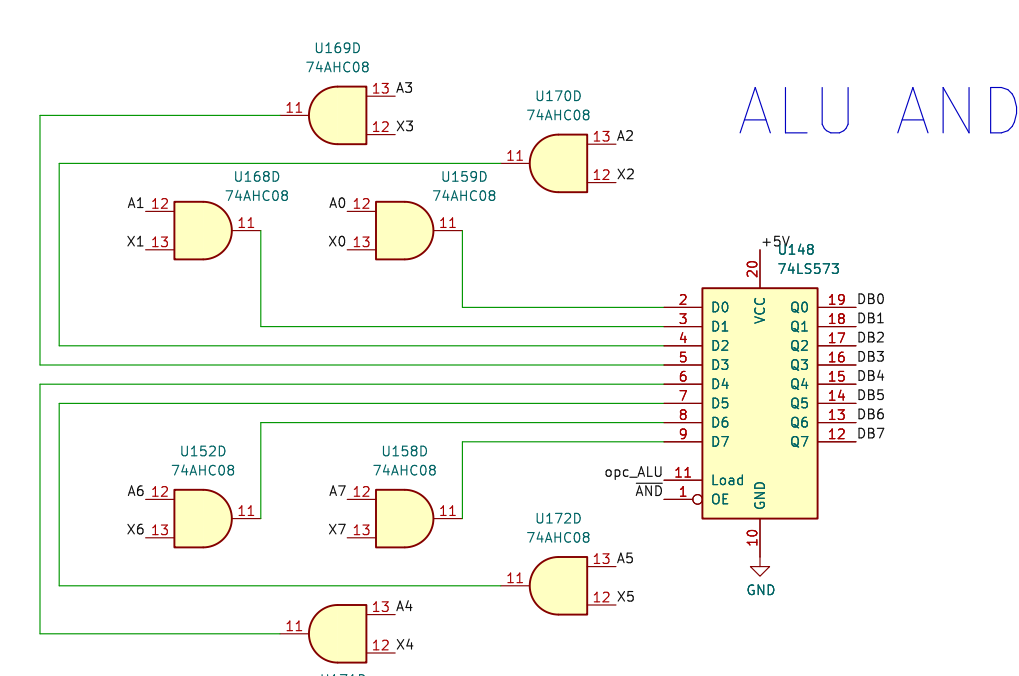
ALU SHIFT



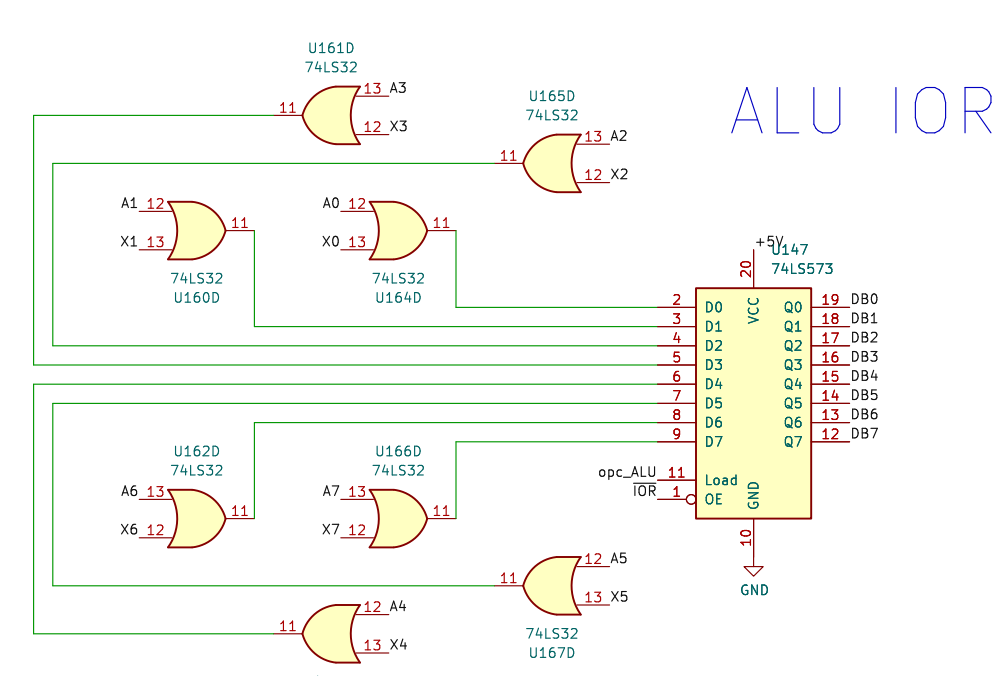
NOT



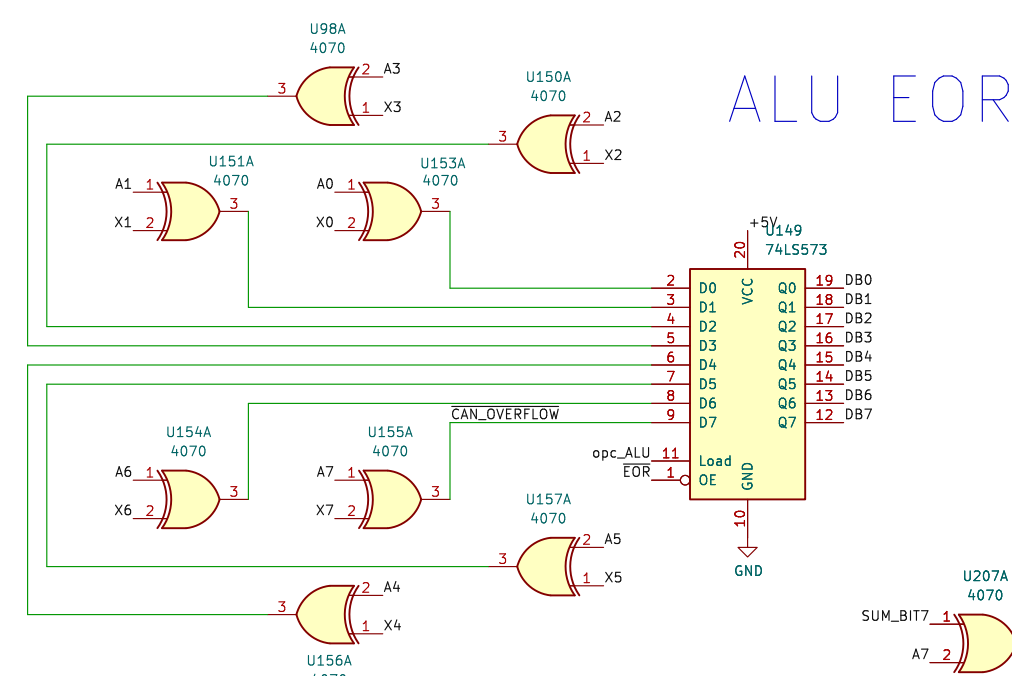
ALU AND



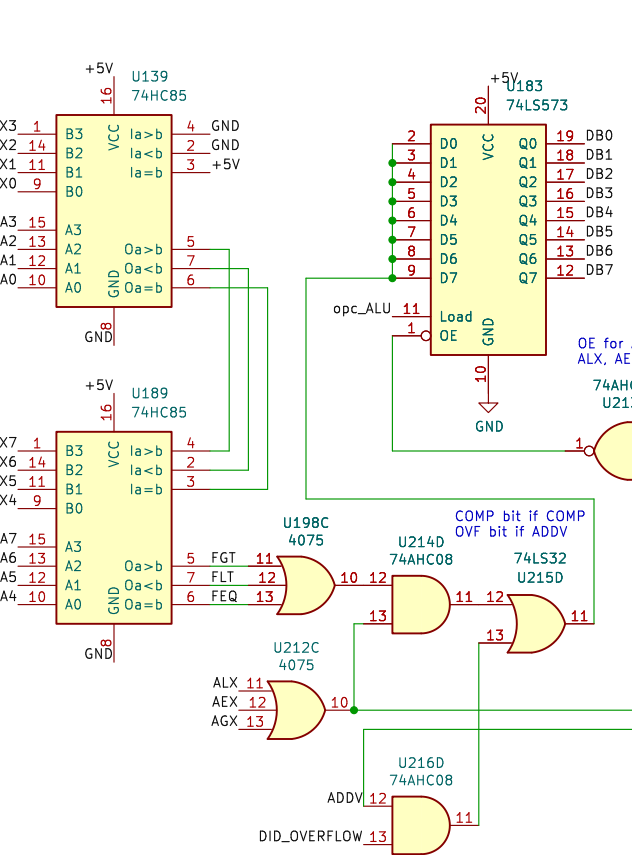
ALU IOR



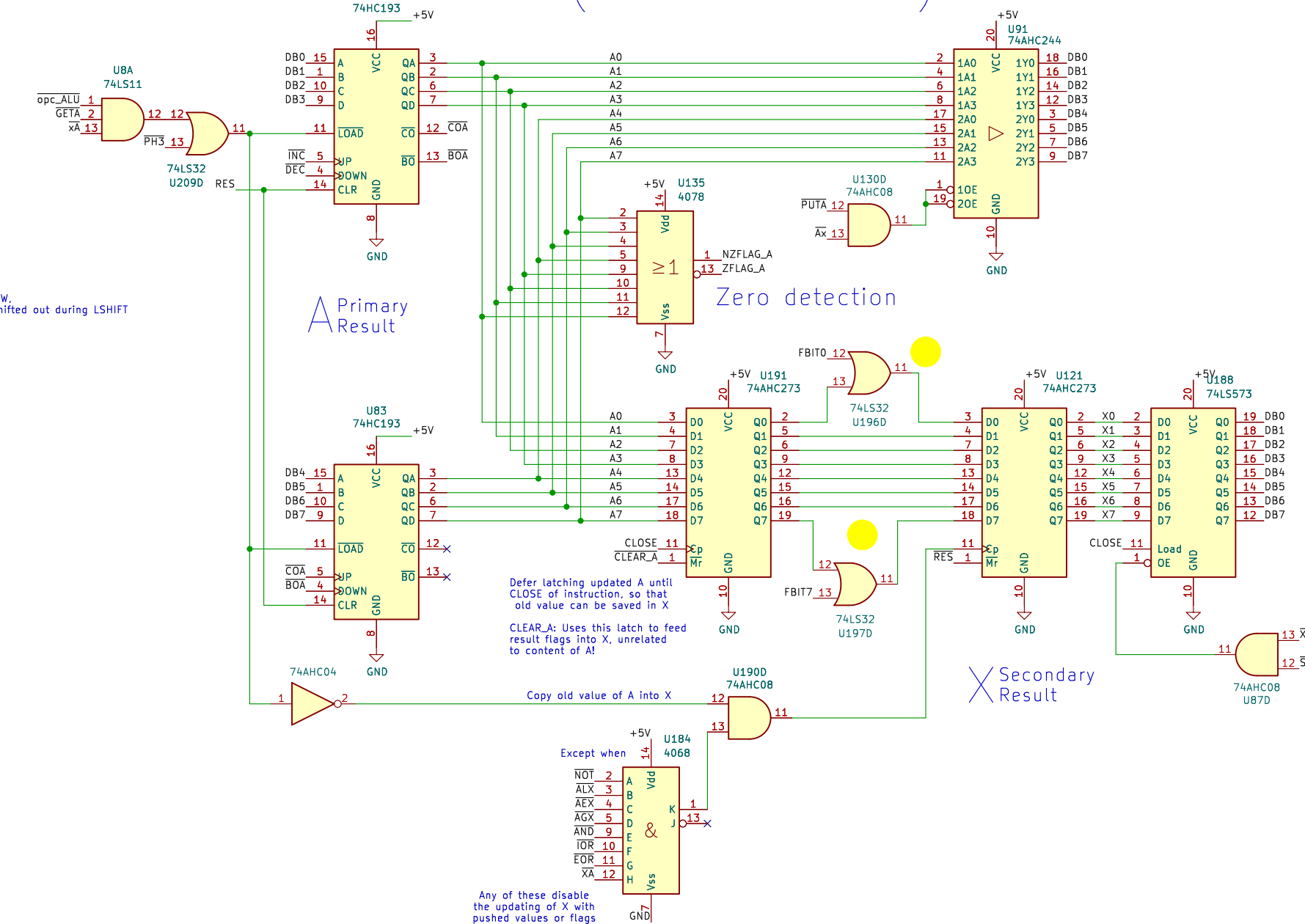
ALU EOR



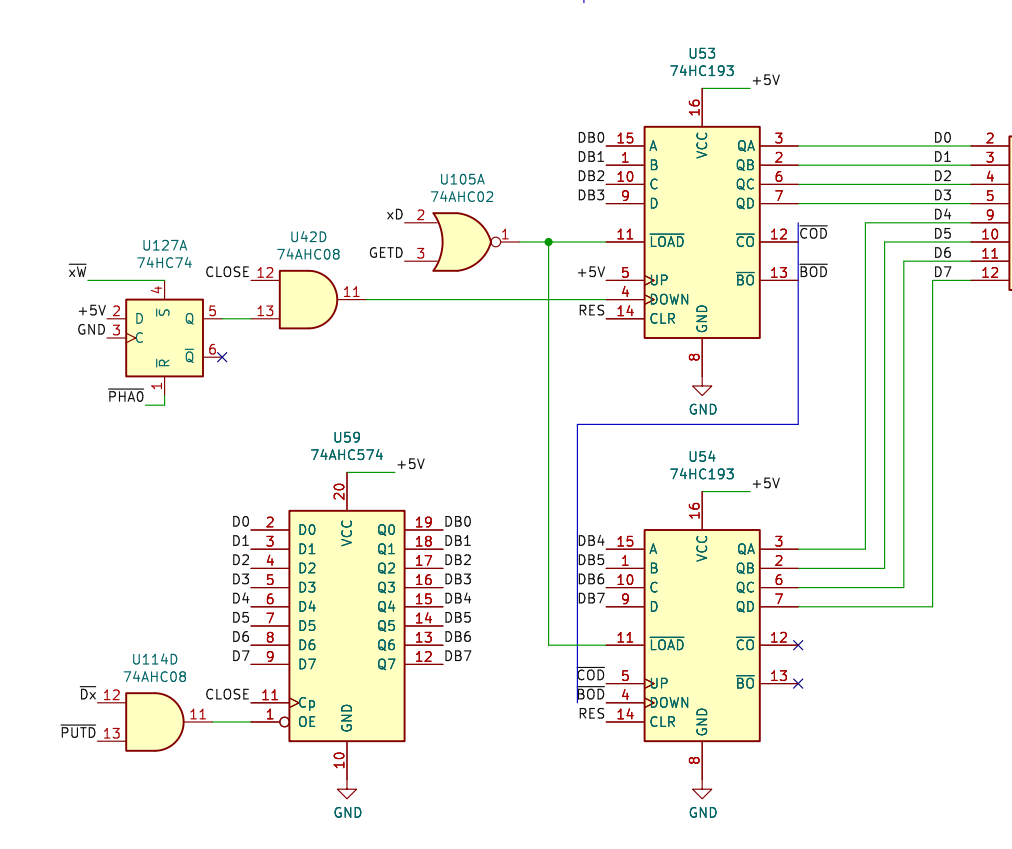
ALU COMPARATOR



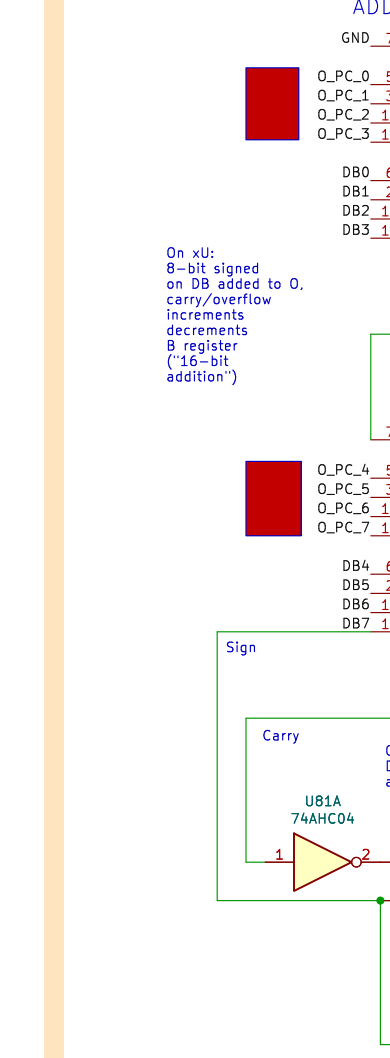
AX (Accumulator)



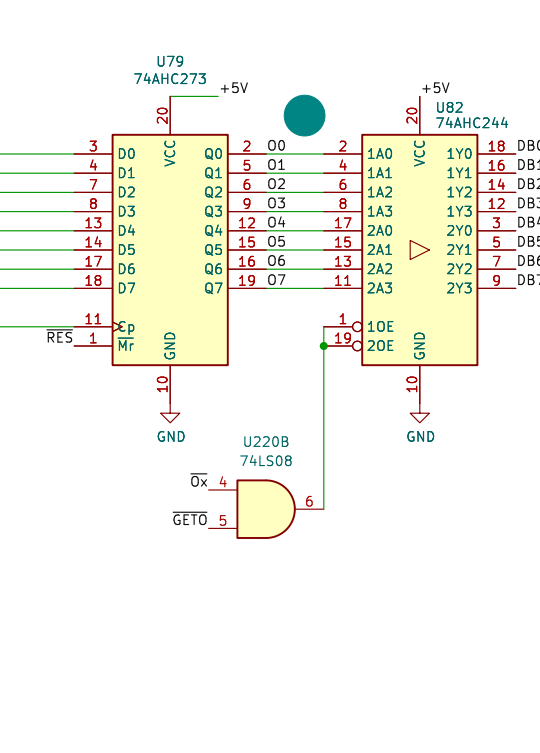
D(OWN) COUNTER Inner-loops



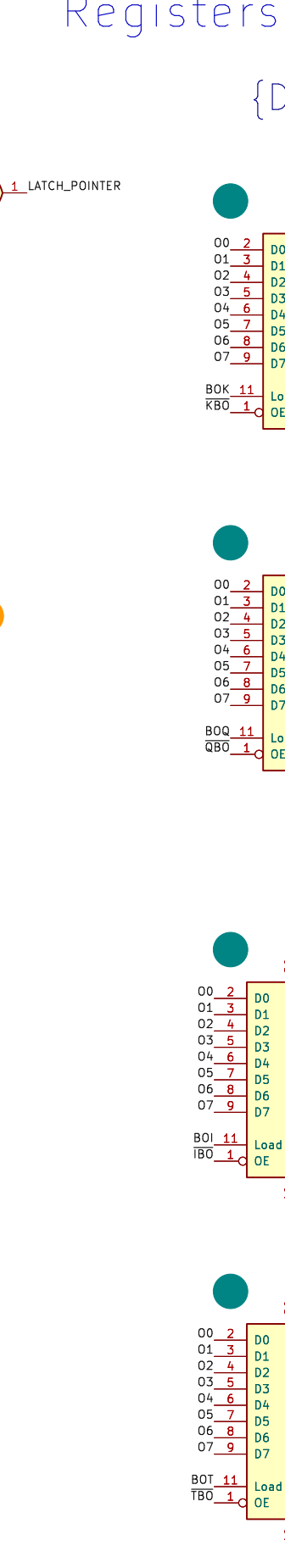
Pointer low



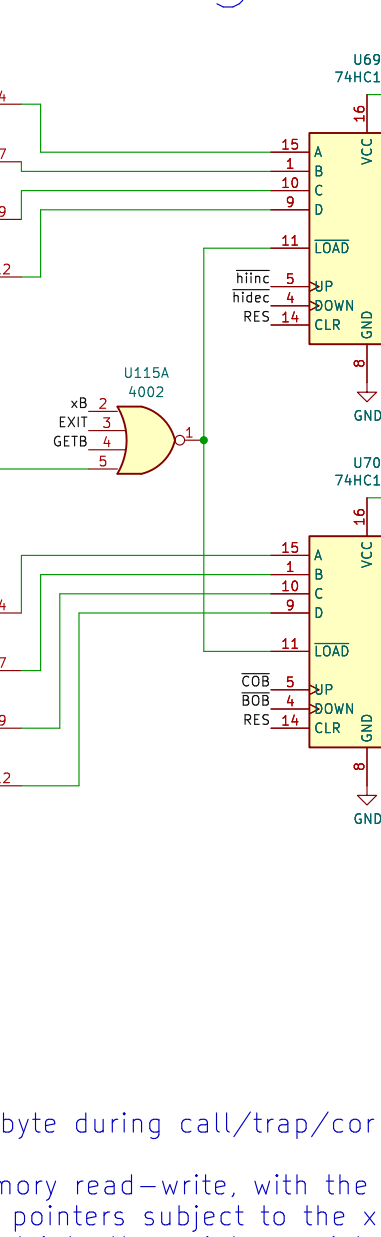
O (Page Offset Register)



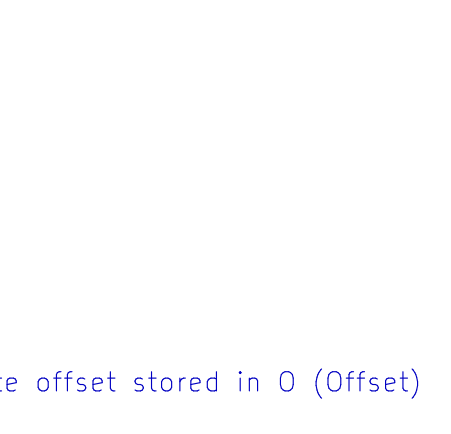
BO Pointer Registers



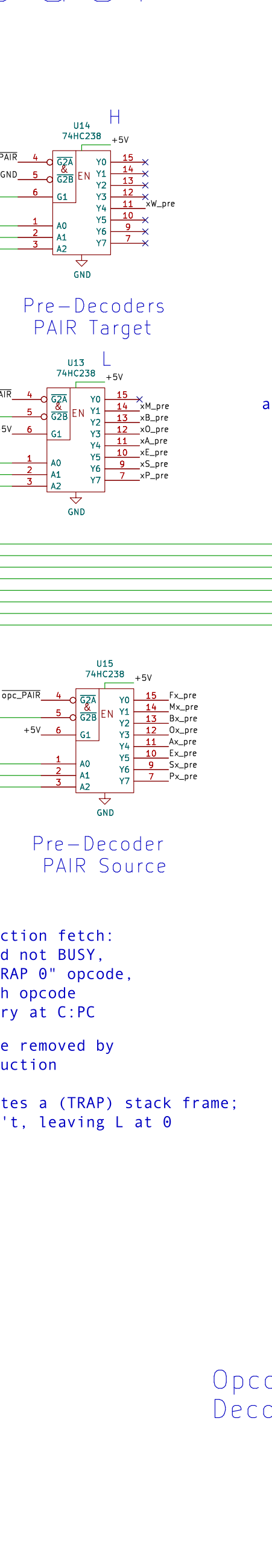
Pointer high



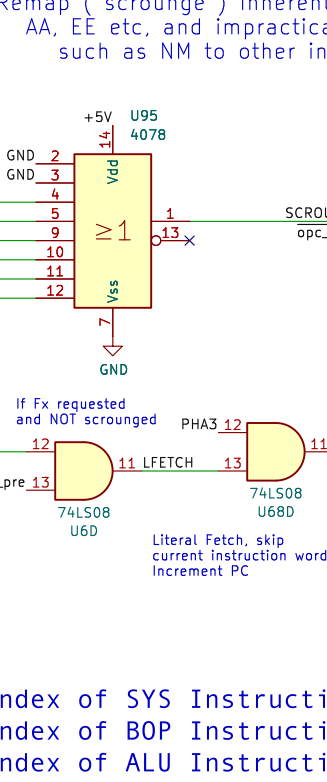
B (Base) Page Index



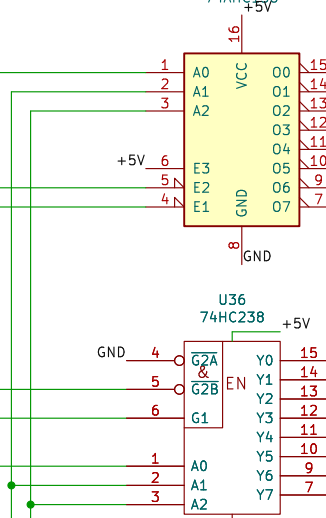
Instruction Decoder



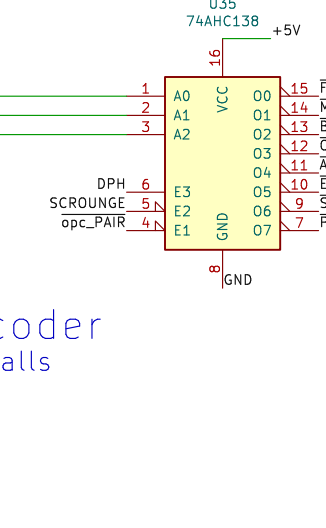
Scrounger



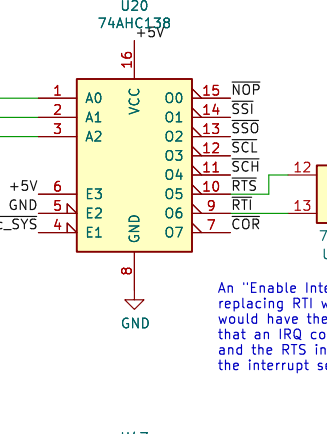
Pair DST Decoder



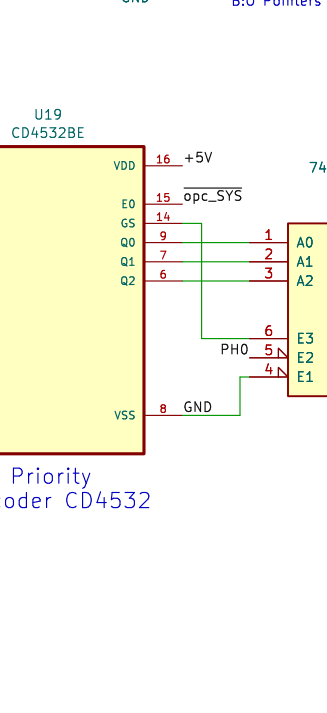
Pair SRC Decoder



SYS Decoder



BOP Decoder



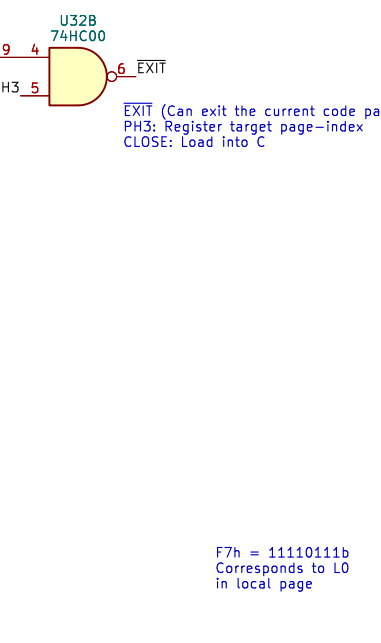
Opcode Type Decoder



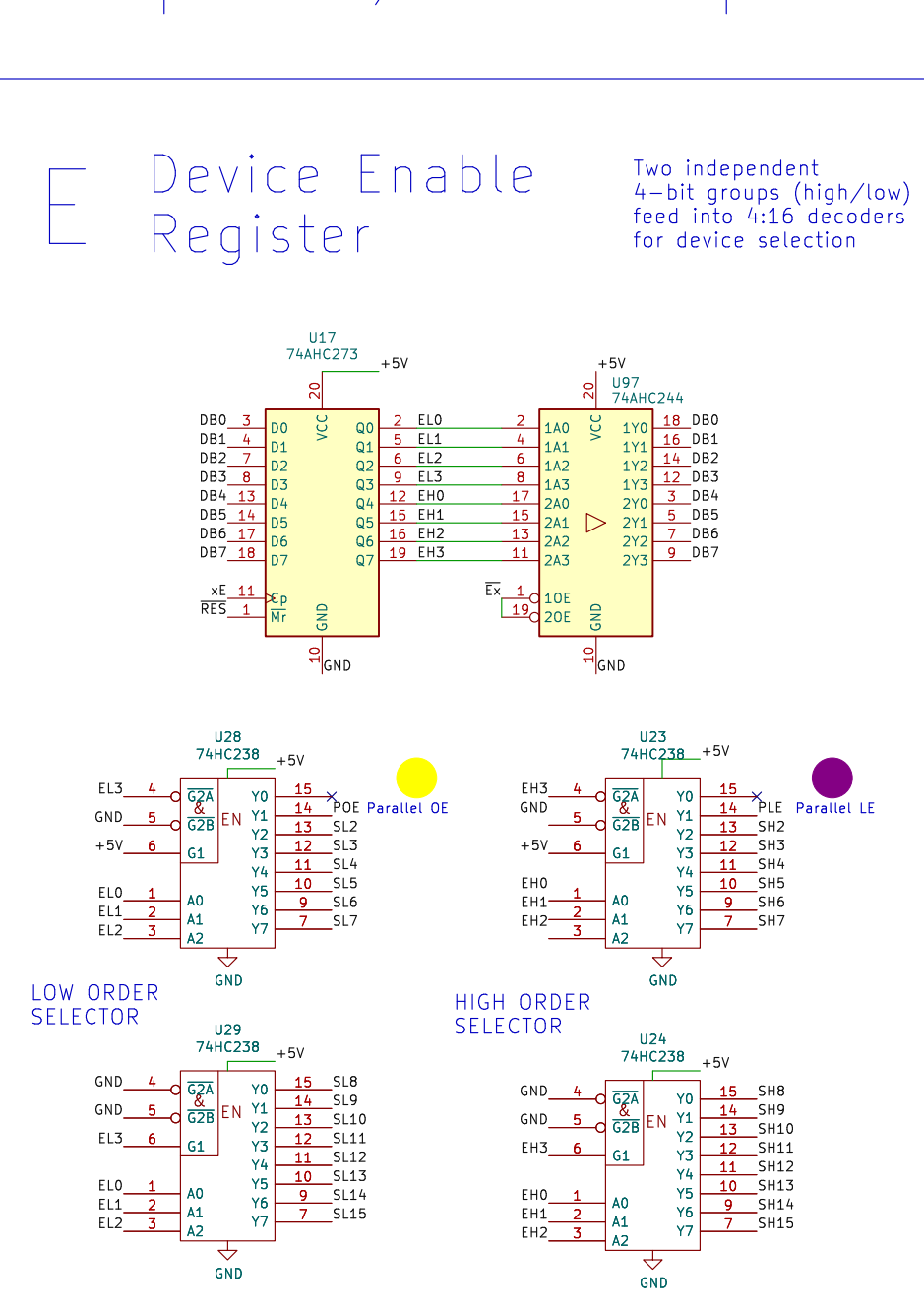
ALU Decoder



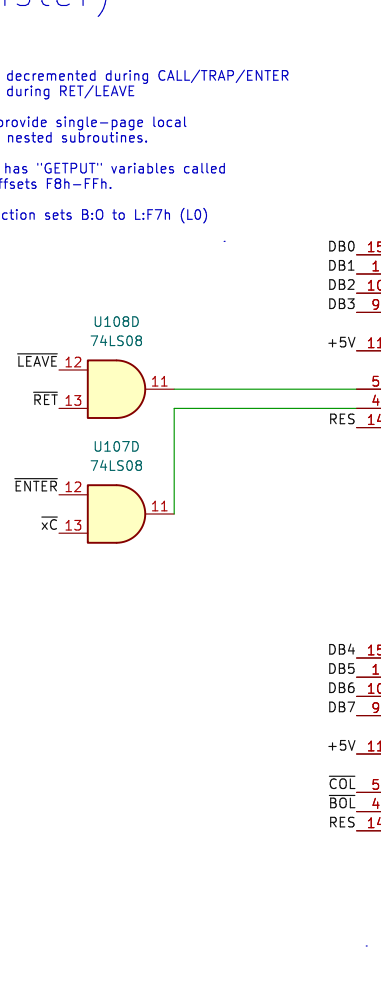
Control Tree



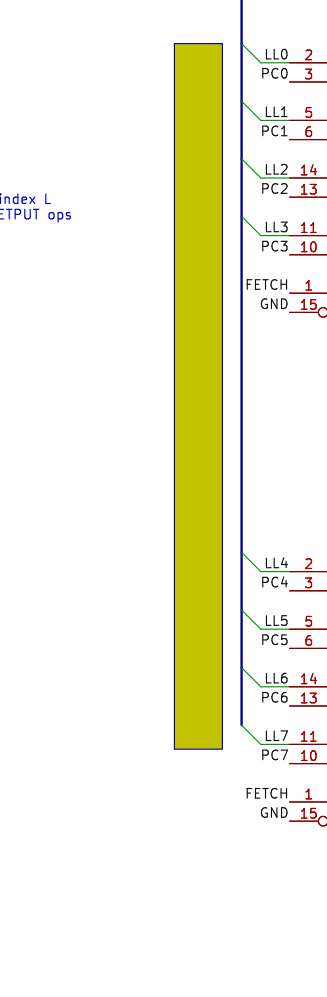
Input/Output



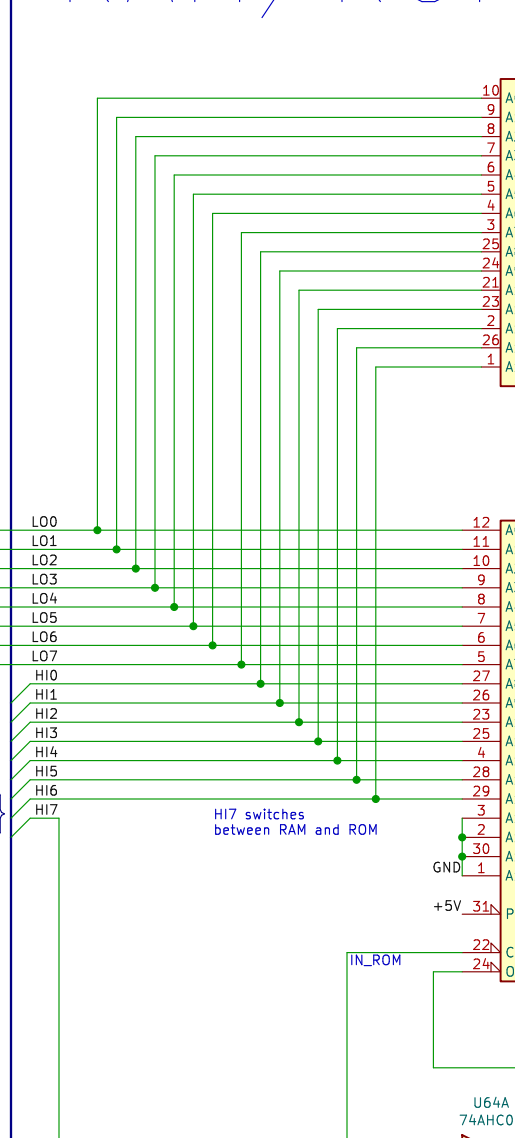
(Local Page Index Register)



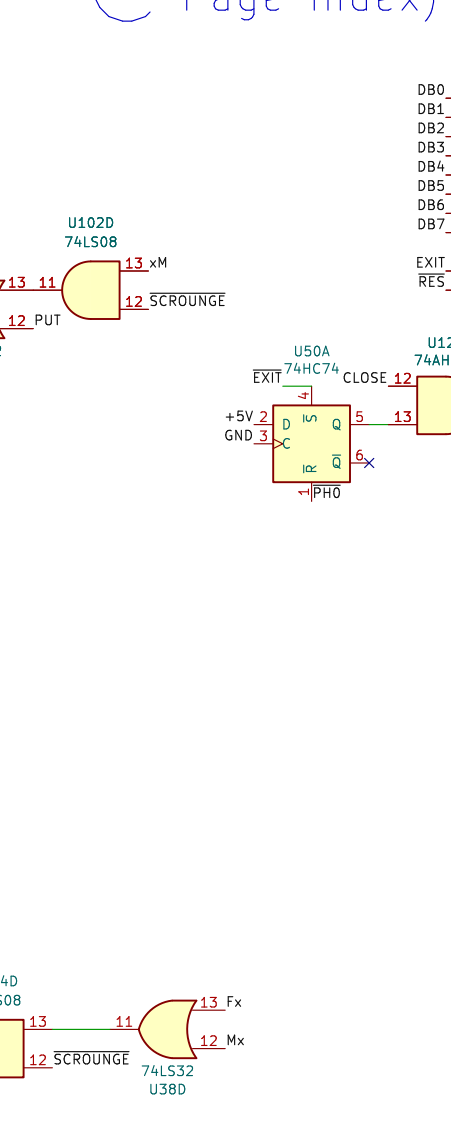
{LL[7..0]}



RAM/ROM



C (Code) Page Index



(Guest-Page Index)

