
Sonne-8 Microcontroller
Reference Schematics
Rev. Myth
Chip "Footprint"
External Connections
Clock
Phases
D(OWN COUNTER)
Inner-loops
AX (Accumulator)
ALU
Branching
Instruction
Decoder
Scrounger
Pair DST Decoder
Pair SRC Decoder
SYS Decoder
TRAP decoder
Immediate calls
GETPUT
Decoder
BOP Decoder
Opcode Type
Decoder
Instruction-
Word Latch
Pre-Decoders
PAIR Target
Pre-Decoder
PAIR Source
Start, latch
instruction or
interrupt vector
(PC Program Counter)
Control Tree
B:O Pointer
low
O (Page Offset
Register)
B:O Pointer
high
B (Base)
Page Index
BO Pointer
Registers
{PTRB[7..0]}
{DB[7..0]}
Input/Output
S Serial IO
E Device Enable
Register
P Parallel IO
L (Local Page Index
Register)
{LL[7..0]}
{LO[7..0]}
{HI[7..0]}
{DB[7..0]}
RAM/ROM
R (Resident
Page Index)
C (Code
Page Index)
Based on working prototype PCB: https://github.com/michaelmangelsdorf/sonne8
Rev. Step "Lox58"
Project homepage: https://github.com/michaelmangelsdorf/myth
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