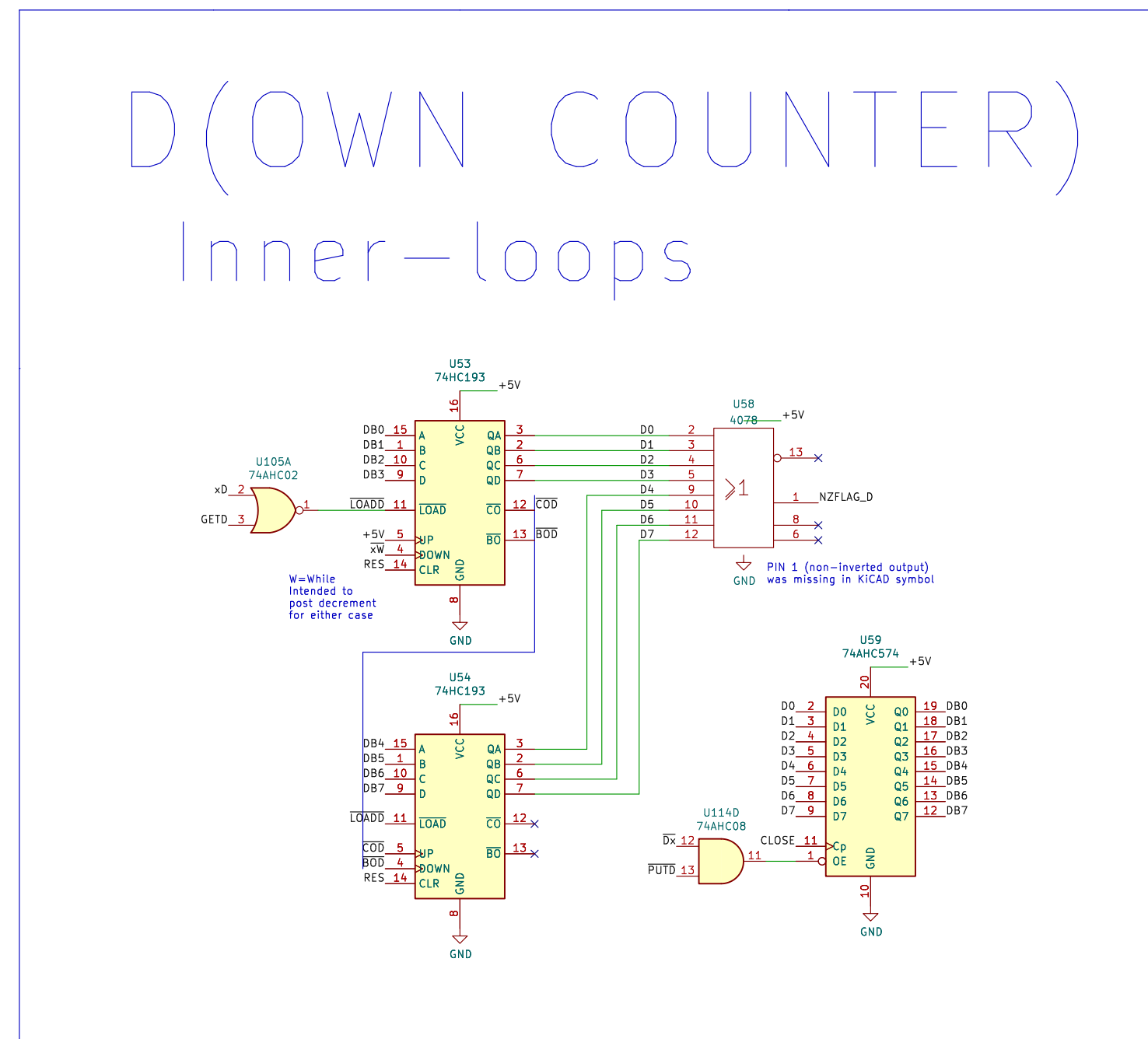
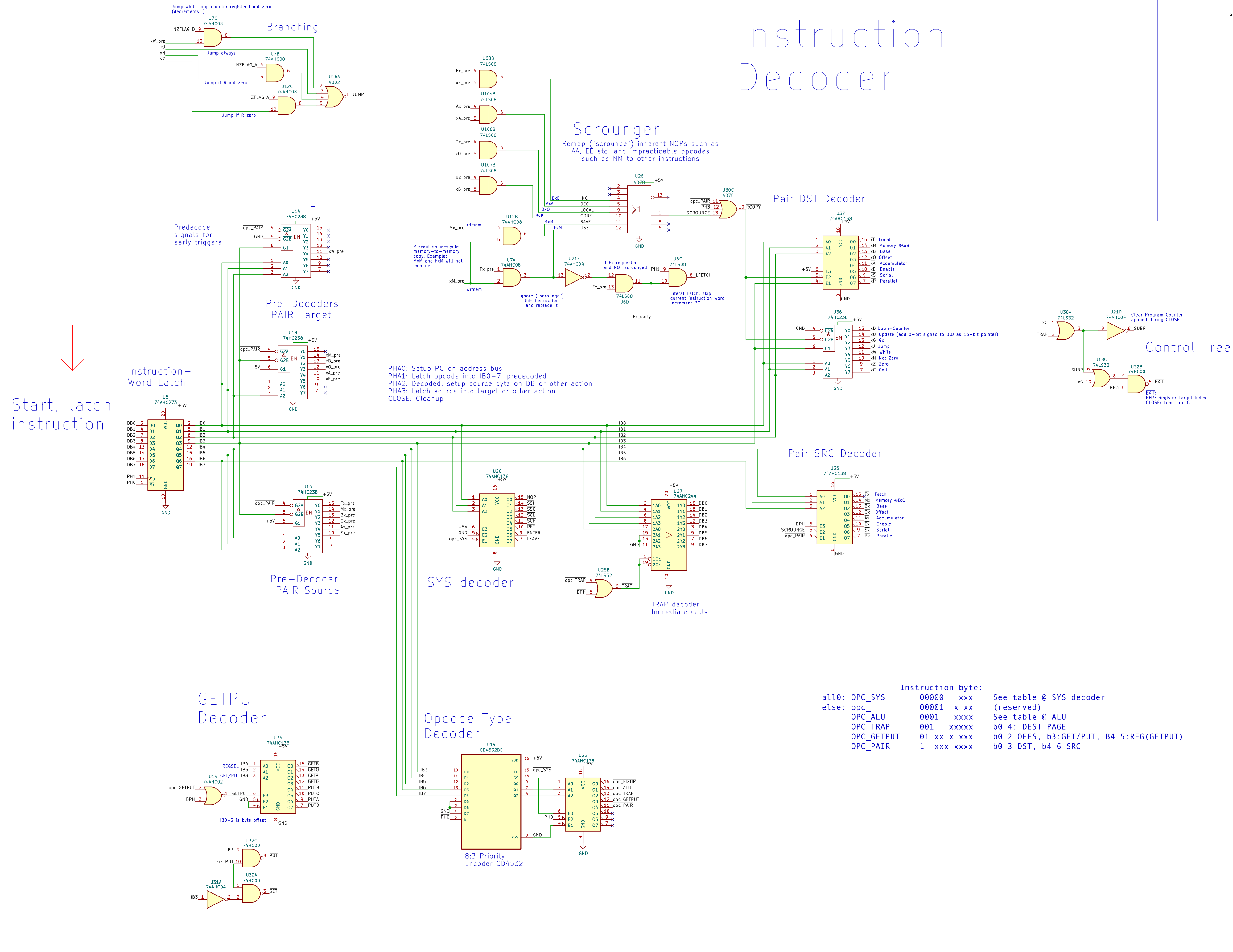


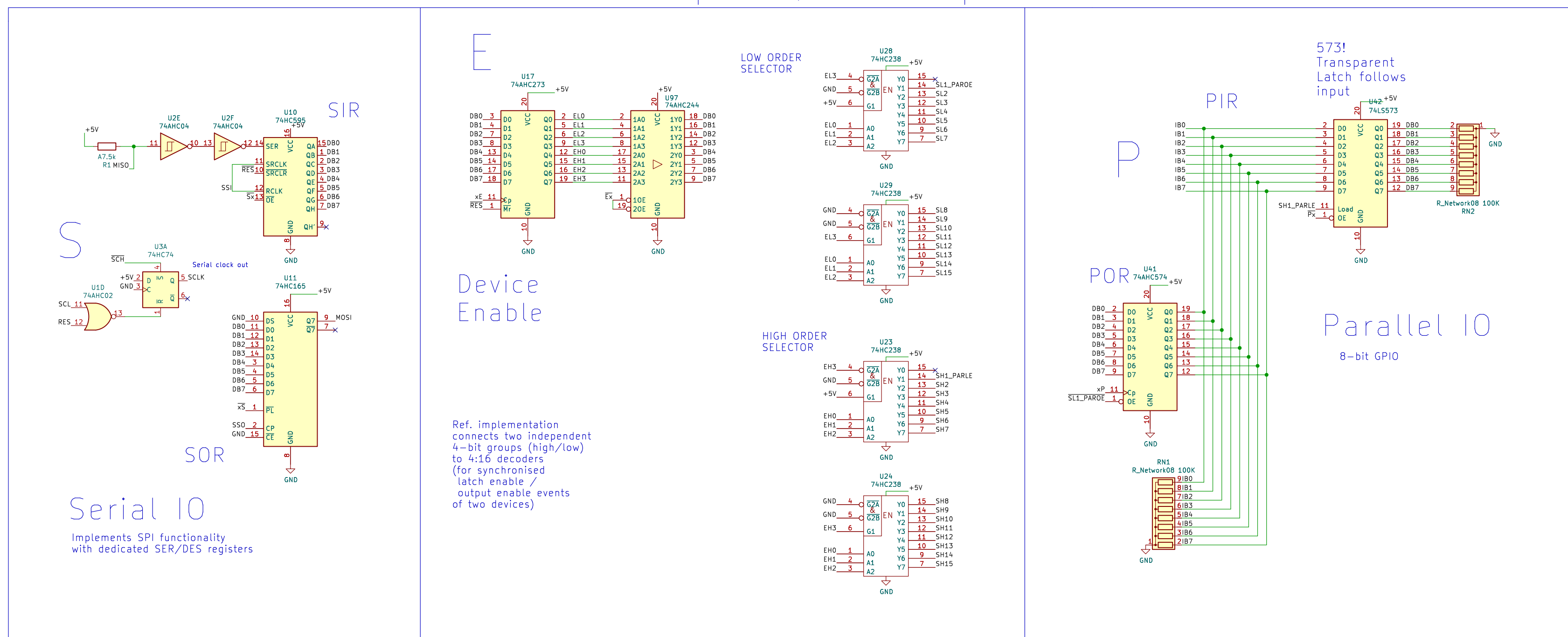
# Sonne-8 Microcontroller Reference Schematics Rev. Myth



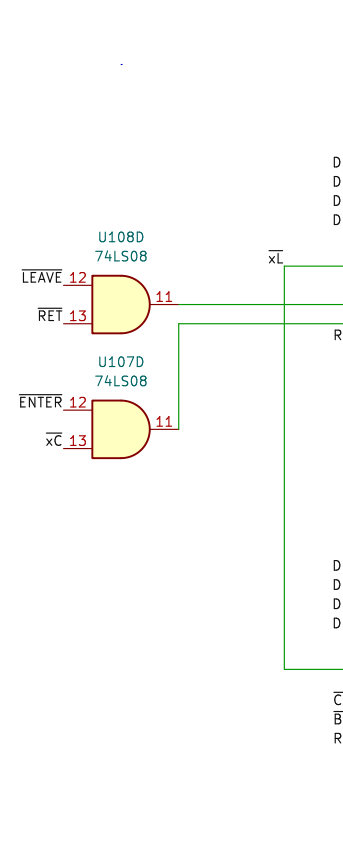
## Instruction Decoder



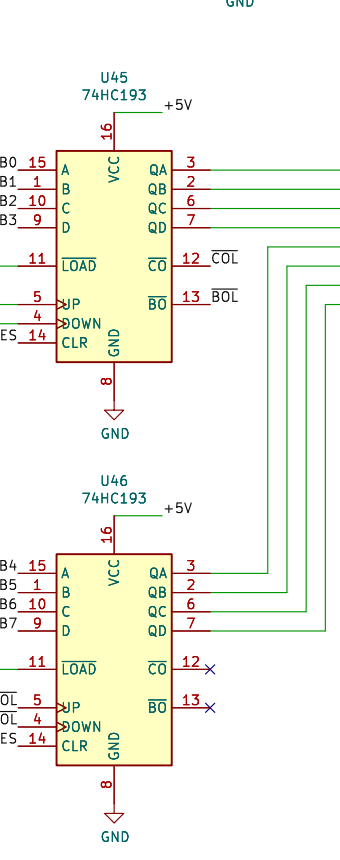
## Input/Output



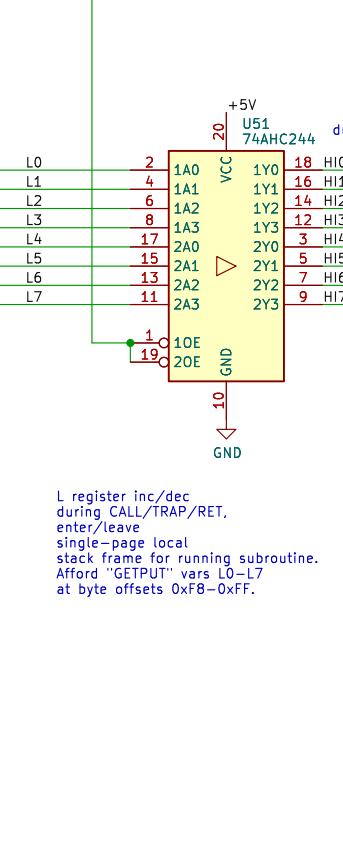
## L (Local Page Index reg)



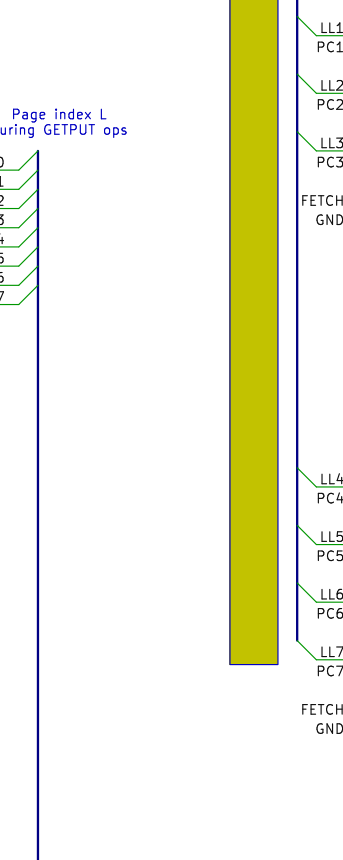
## P (Program Counter)



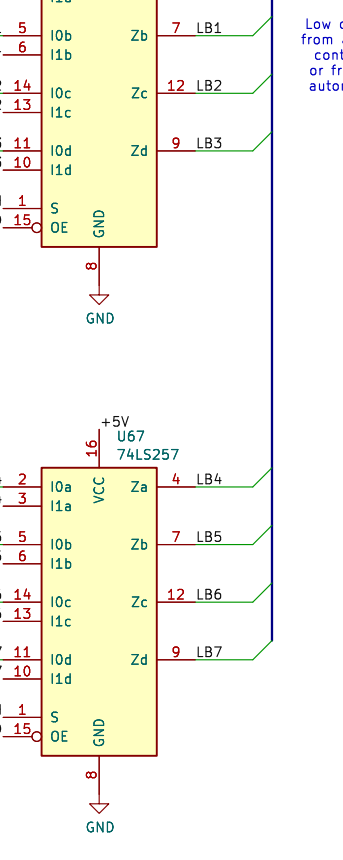
## S (Status Register)



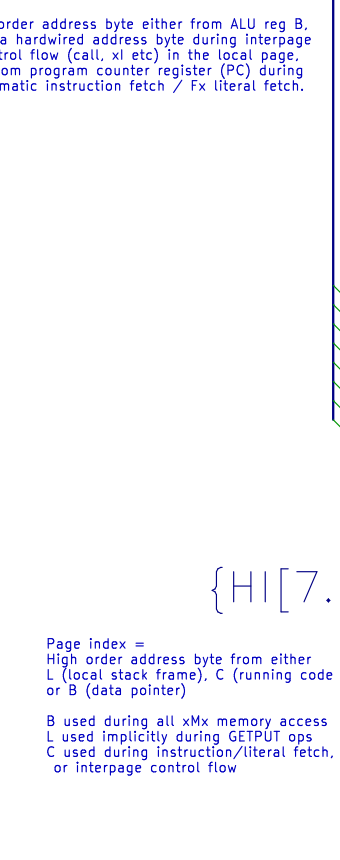
## E (Error Register)



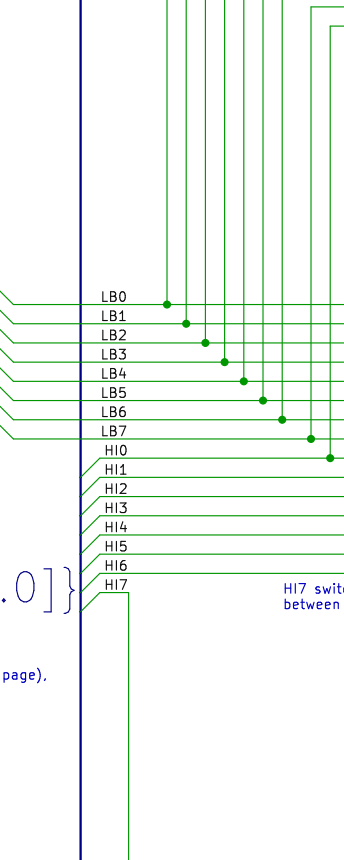
## R (Register File)



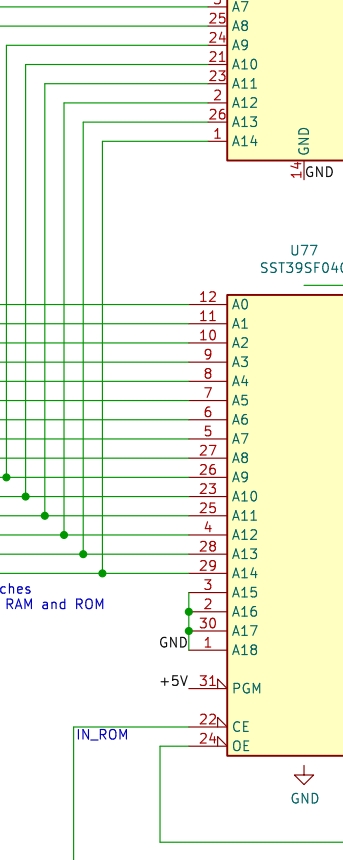
## A (Accumulator) Register



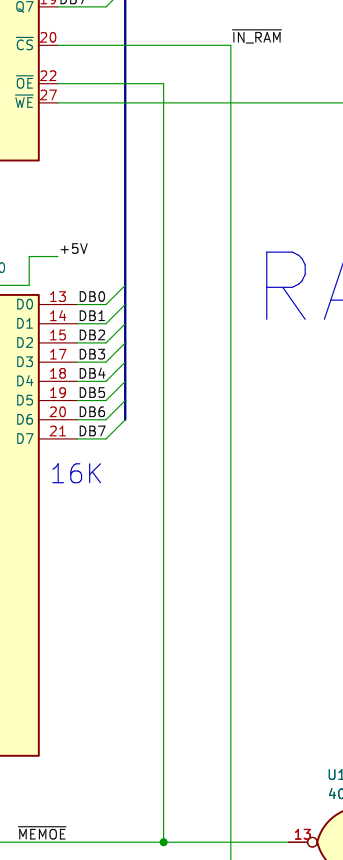
## O (Page Offset) Register



## B (Base) Page Index



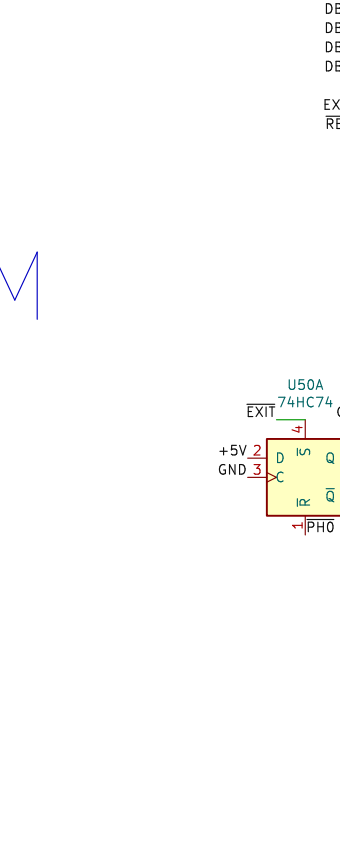
## C (CODE) Page index



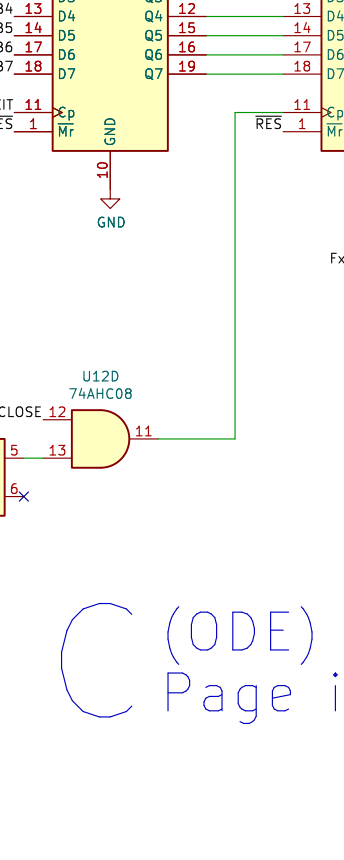
## RAM/ROM



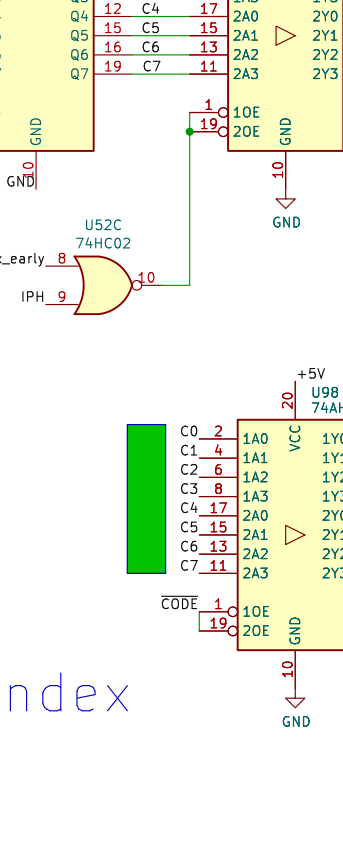
## ALU



## Control Tree



## GETPUT Decoder



## Opcode Type Decoder

