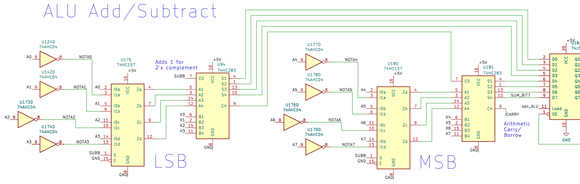
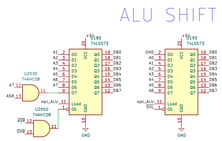
Sonne-8 Microcontroller Reference Schematics Rev. Myth

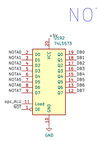
ALU Add/Subtract



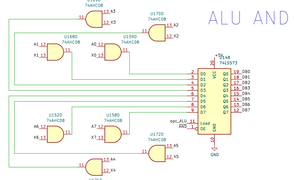
ALU SHIFT



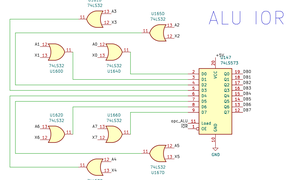
NOT



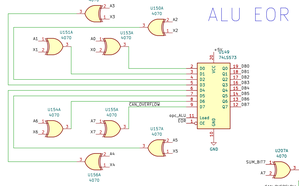
ALU AND



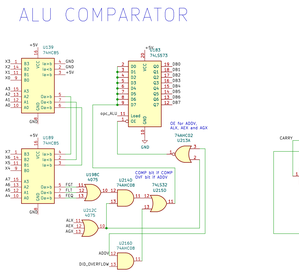
ALU IOR



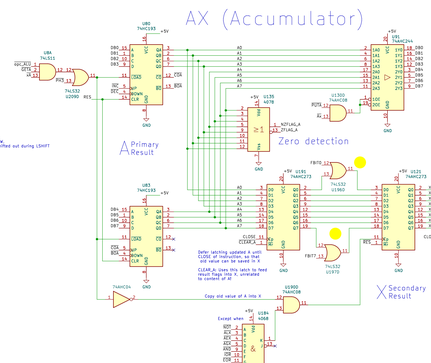
ALU EOR



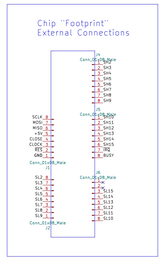
ALU COMPARATOR



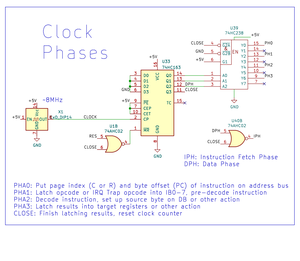
AX (Accumulator)



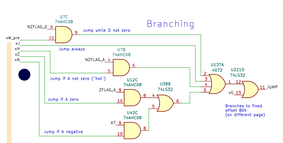
Chip "Footprint" External Connections



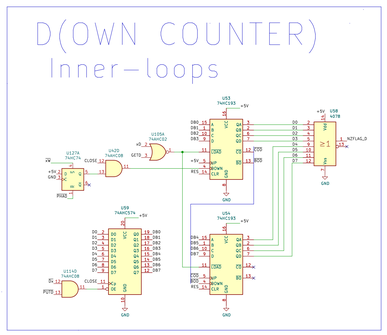
Clock Phases



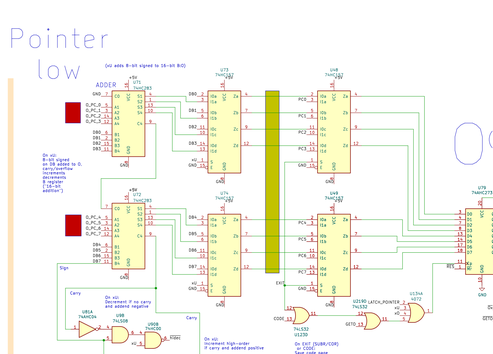
Branching



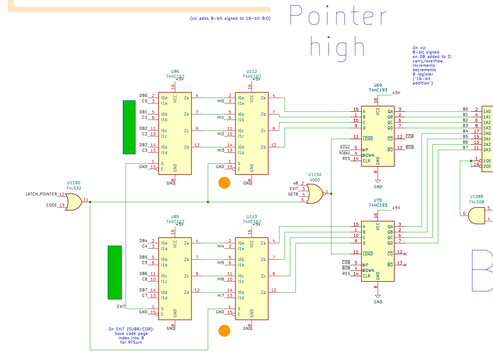
D(OWN COUNTER) Inner-loops



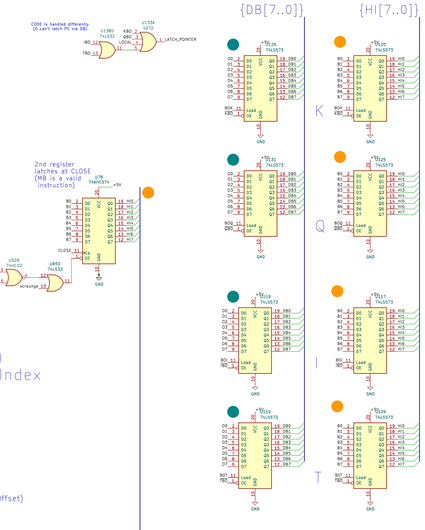
Pointer low



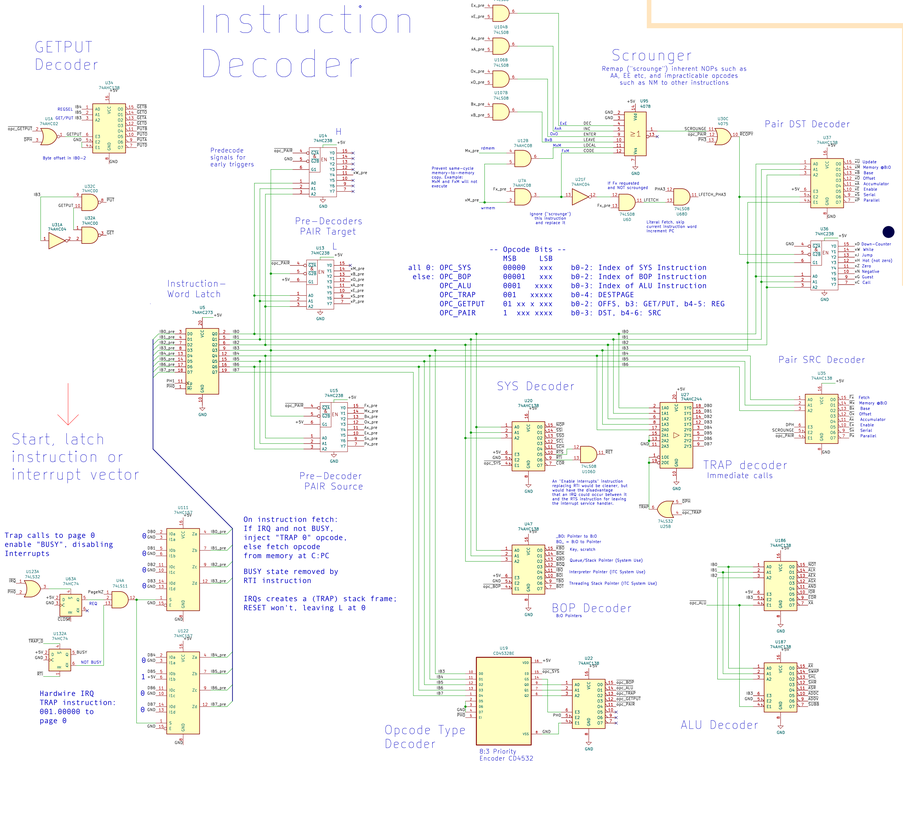
Pointer high



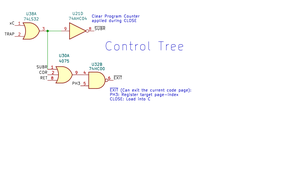
BO Pointer Registers



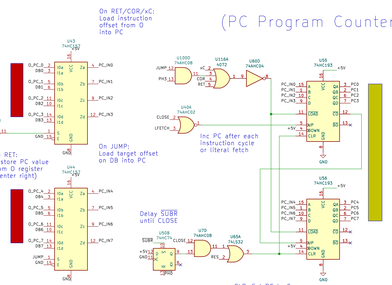
Instruction Decoder



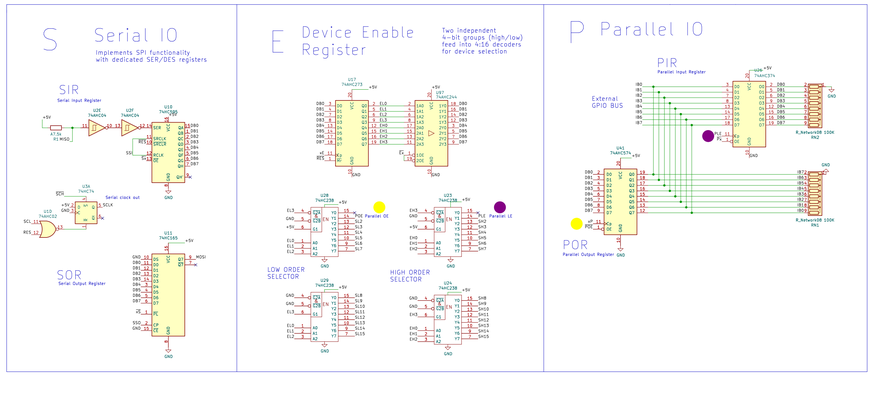
Control Tree



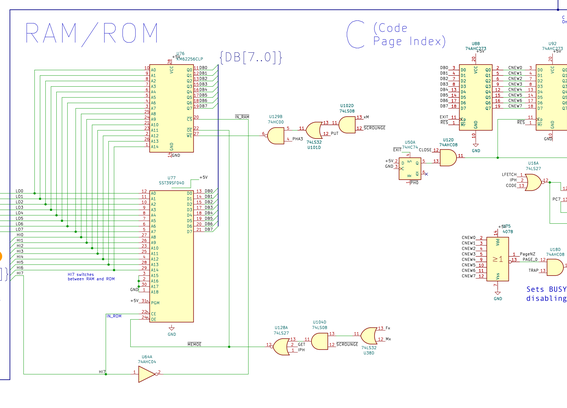
(PC Program Counter)



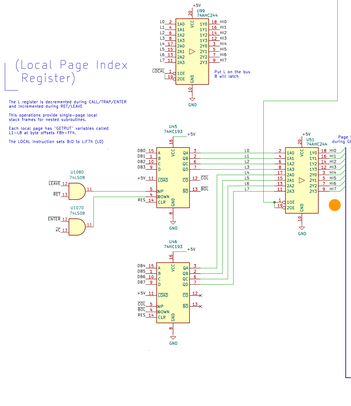
Input/Output



RAM/ROM



(Local Page Index Register)



Based on working prototype PCB: https://github.com/michaelmangelsdorf/sonne8
Rev. 5Dec 08
Project homepage: https://github.com/michaelmangelsdorf/myth
Author: Michael Mangelsdorf
Contact: mim@os1-schalter.de
Sheet: /
File: myth.kicad_sch
Title: Sonne-8 Micro-Controller
Size: User Date: 2025-06-14 **Rev: Myth**
KiCad E.D.A. 9.0.1 Id: 1/1