ECEN 325 – Electronics

Fall 2020

Lab 6: Report



Submitted by:

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I. Objective

The objective of this lab is to introduce us to diodes by examining its basic properties and characteristics.

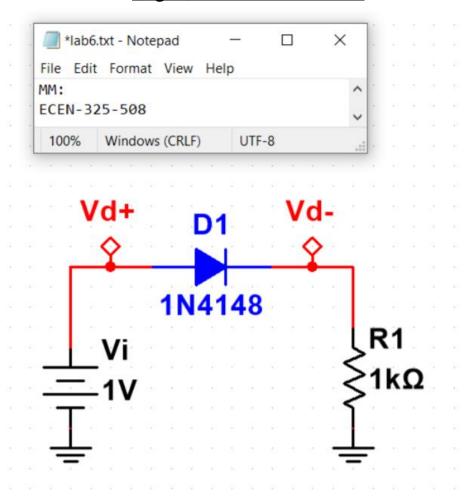
II. Procedure

For the procedure I first had to build a diode test circuit and graph its I-V characteristics. Then I had to calculate the values for figure 5 circuit then build its schematic. After that. I obtained its time domain waveform form the measurements.

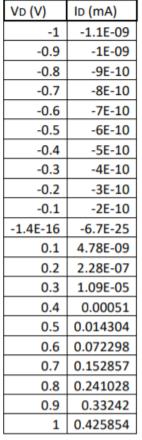
III. Difficulties

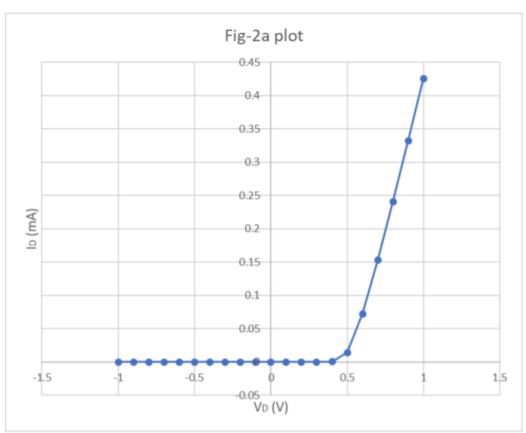
There were no difficulties during the lab.

IV. Results (fig-2) circuit schematic



Plot and table for (fig-2) circuit





Calculations for (fig-5) circuit

$$R_{L} = \frac{V_{o}}{I_{o,max}} \quad C = \frac{1}{2f_{i}R_{L}K_{r}} \quad \hat{V}_{s} \approx V_{o} + 0.7$$

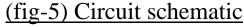
$$I_{o,max} = 3mA \quad V_{o} = 3V \quad f = 2.50 \text{ Hz}$$

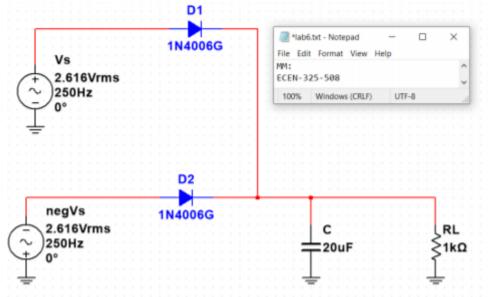
$$R_{L} = \frac{3}{3m} = 1k \text{ L} \quad K_{r} = 0.1 \quad C = \frac{1}{2(3.50)(1k)01} = 20 \text{ MF}$$

$$C = 20 \text{ MF} \quad V_{s} = 3 + 0.7 = 3.7$$

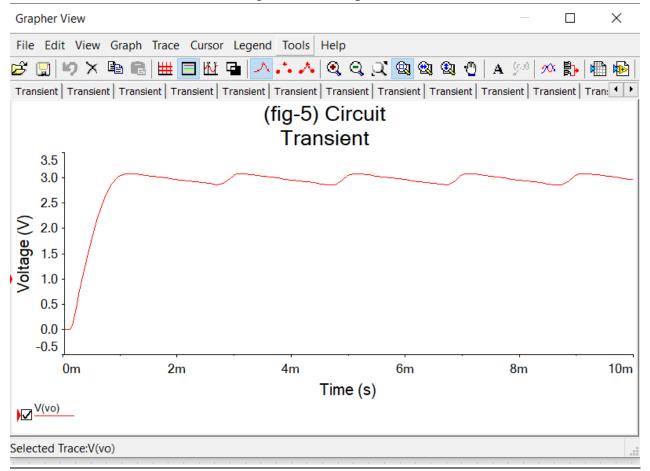
$$V_{s} = 3.7 \sin(500 \text{ Tt}) \text{ V}$$

$$-V_{s} = -3.7 \sin(500 \text{ Tt}) \text{ V}$$





(fig-5) Dc sweep of Vo



Measured and calculated values of (fig-5) circuit

peak Vo (V)	maximum ripple (%)	peak Id1 (A)	peak ID2 (A)	peak I∟ (A)
3.08	10	29.1 m	29.3	0

V. Conclusion

In conclusion, when comparing my calculated with my measured results the avg output voltage is only slightly different between my calculated output voltage. This might be due to averaging output voltage only gives you a estimated look at the voltage that is being out putted.