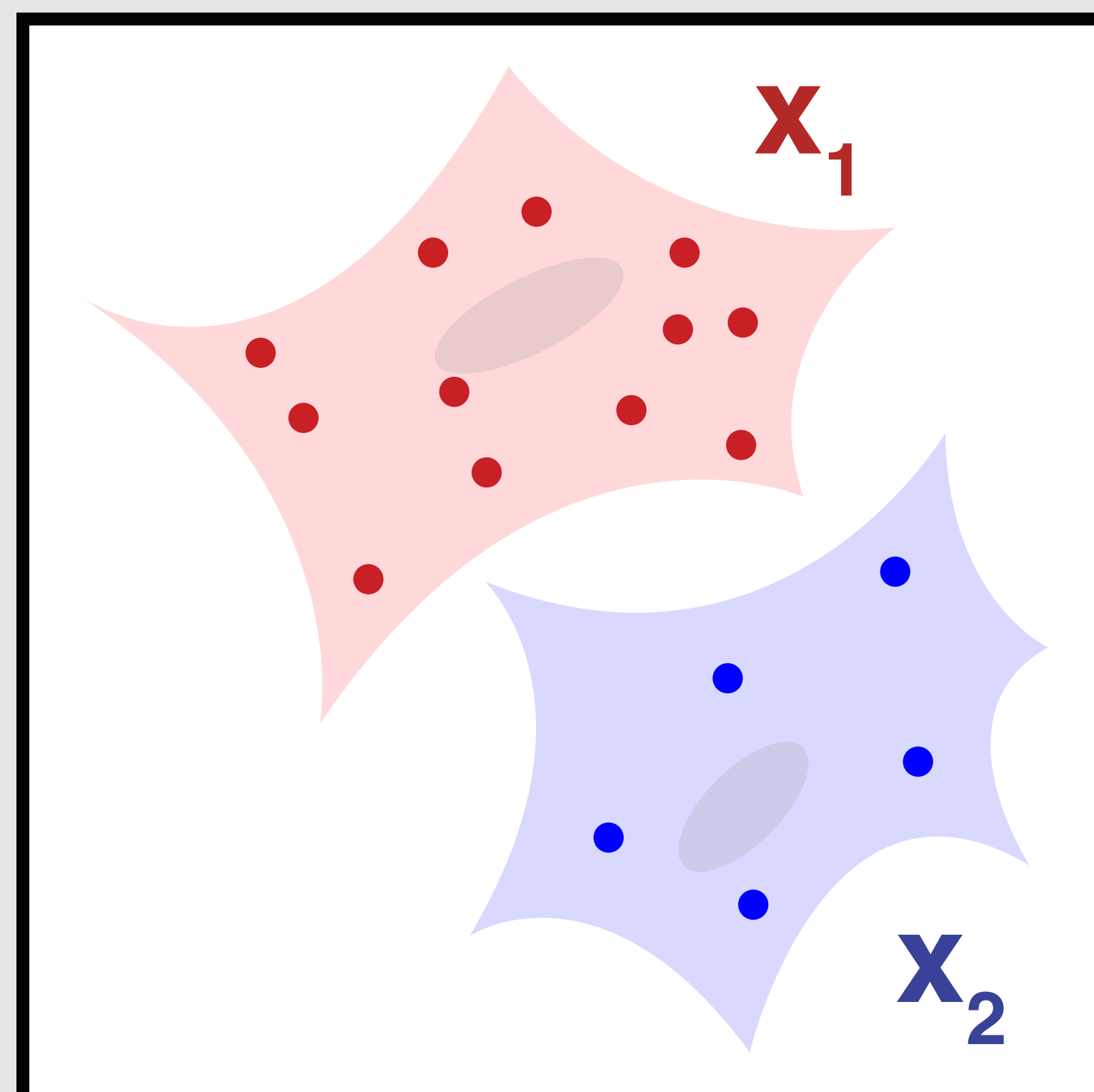
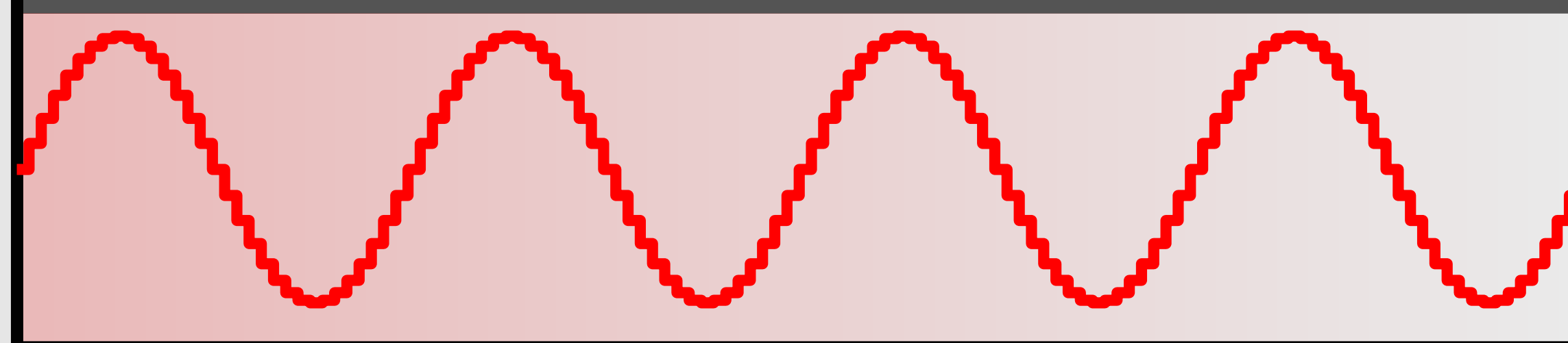


# Piecewise Constant Dynamic Control

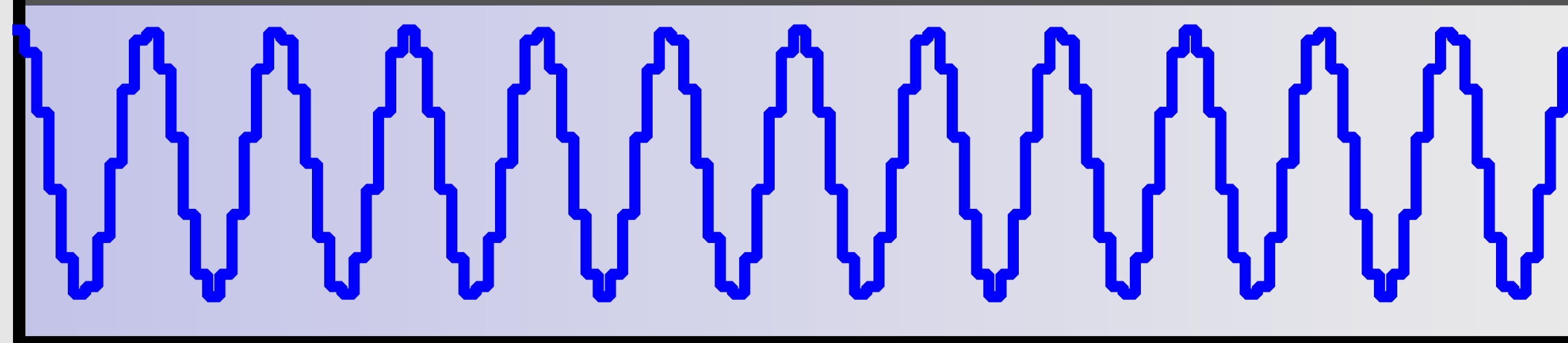
A



Desired Signal  $X_1$



Desired Signal  $X_2$

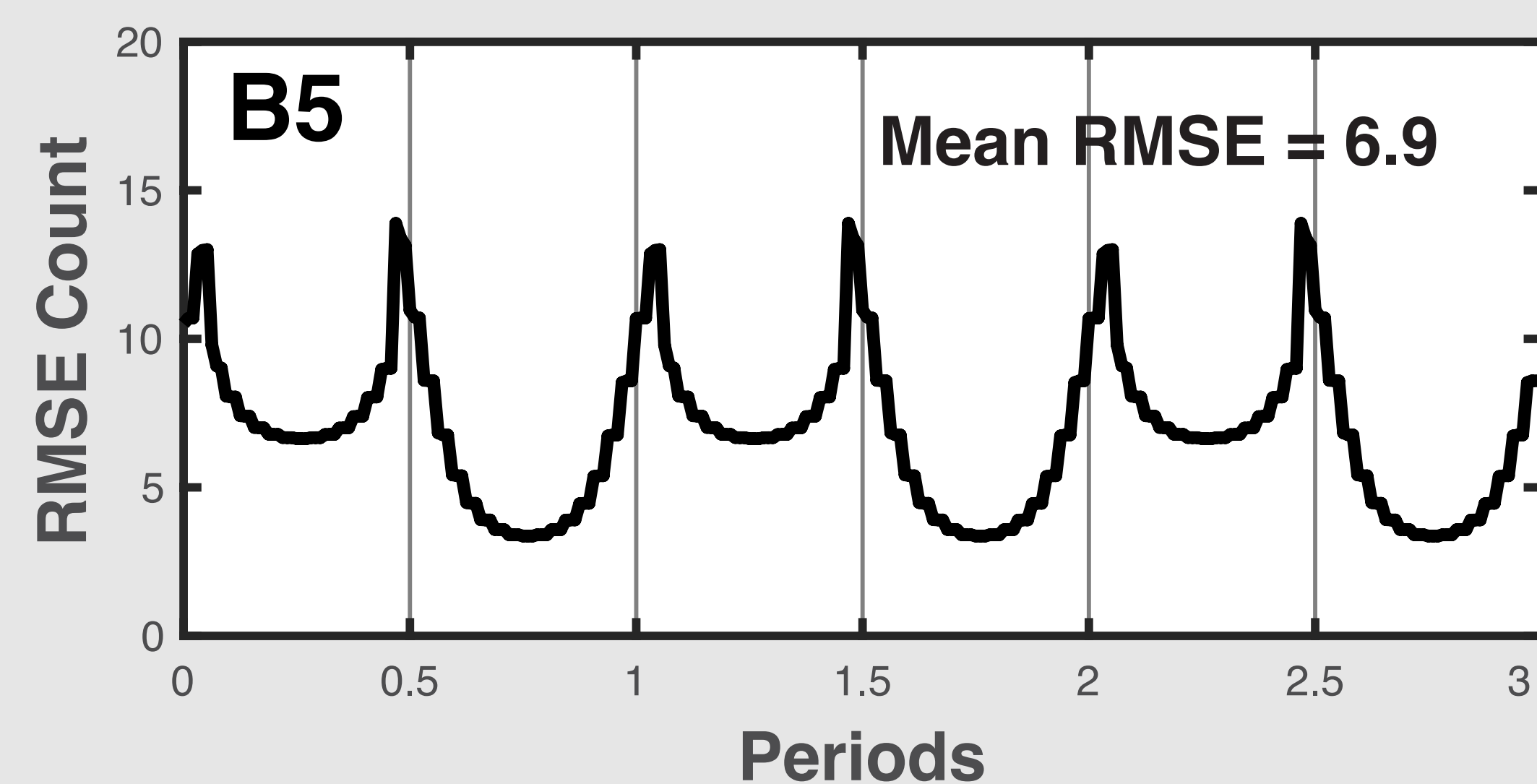
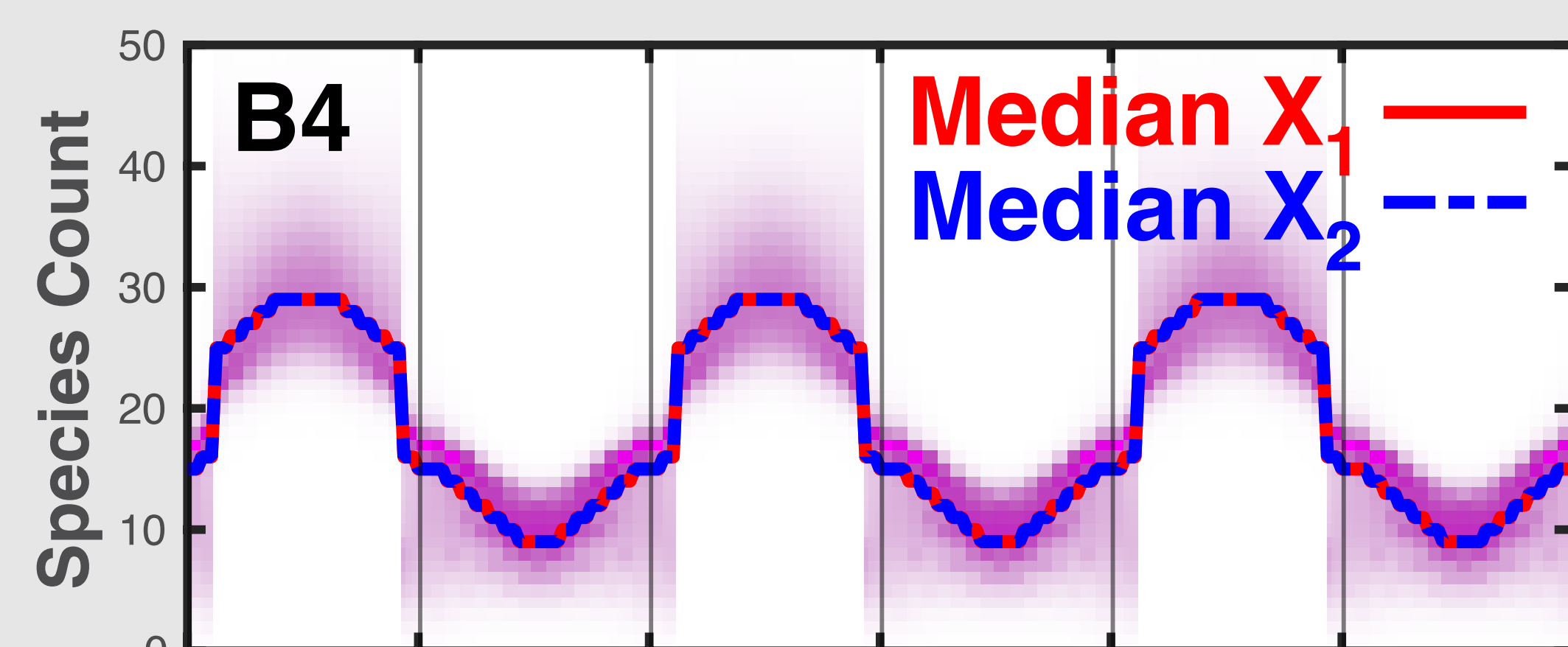
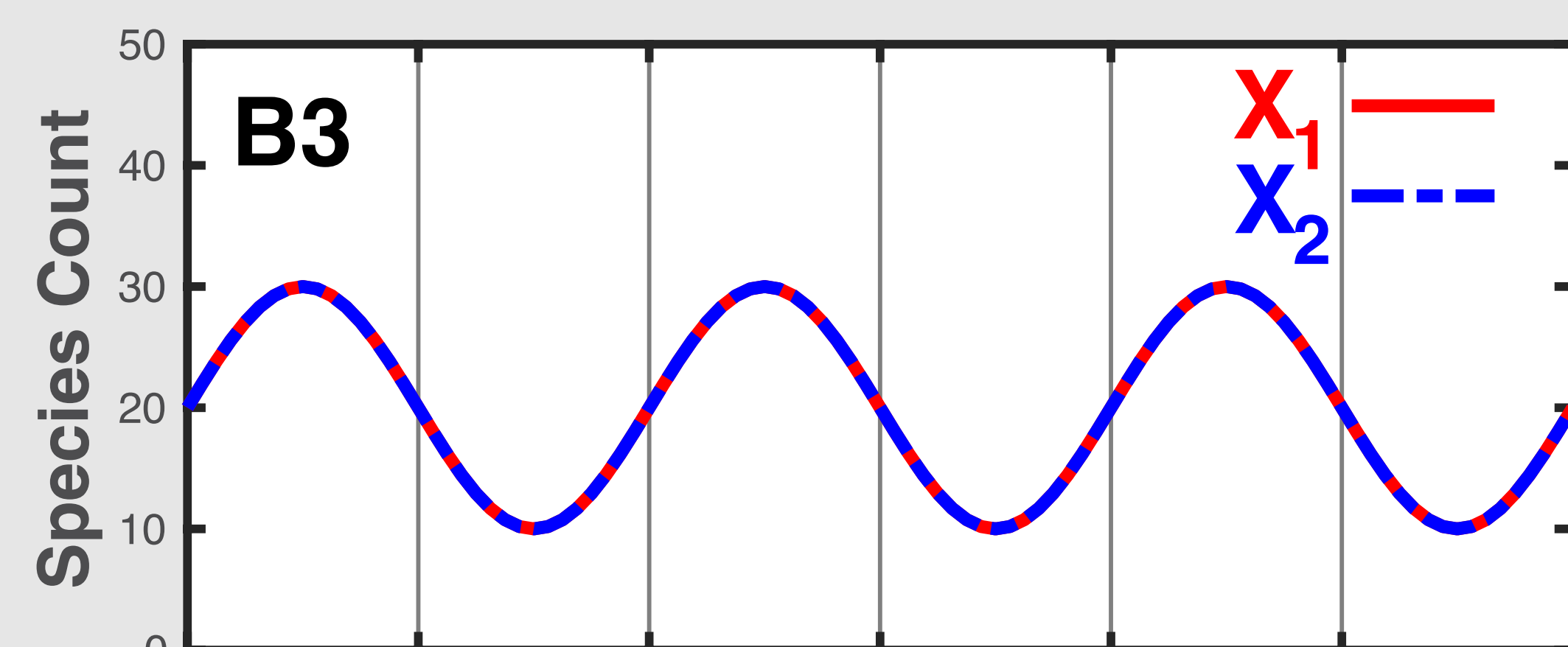
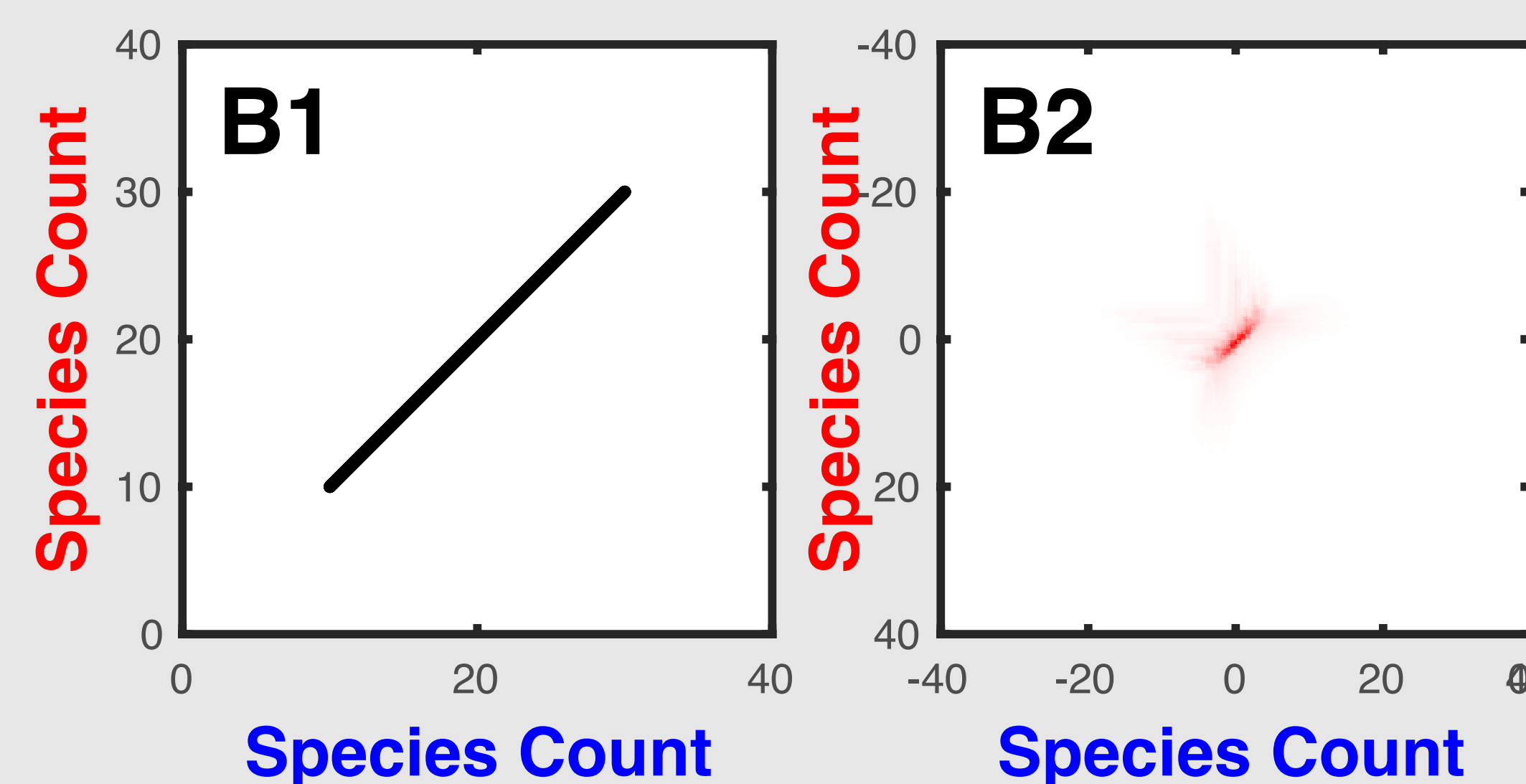


1. Synchronous

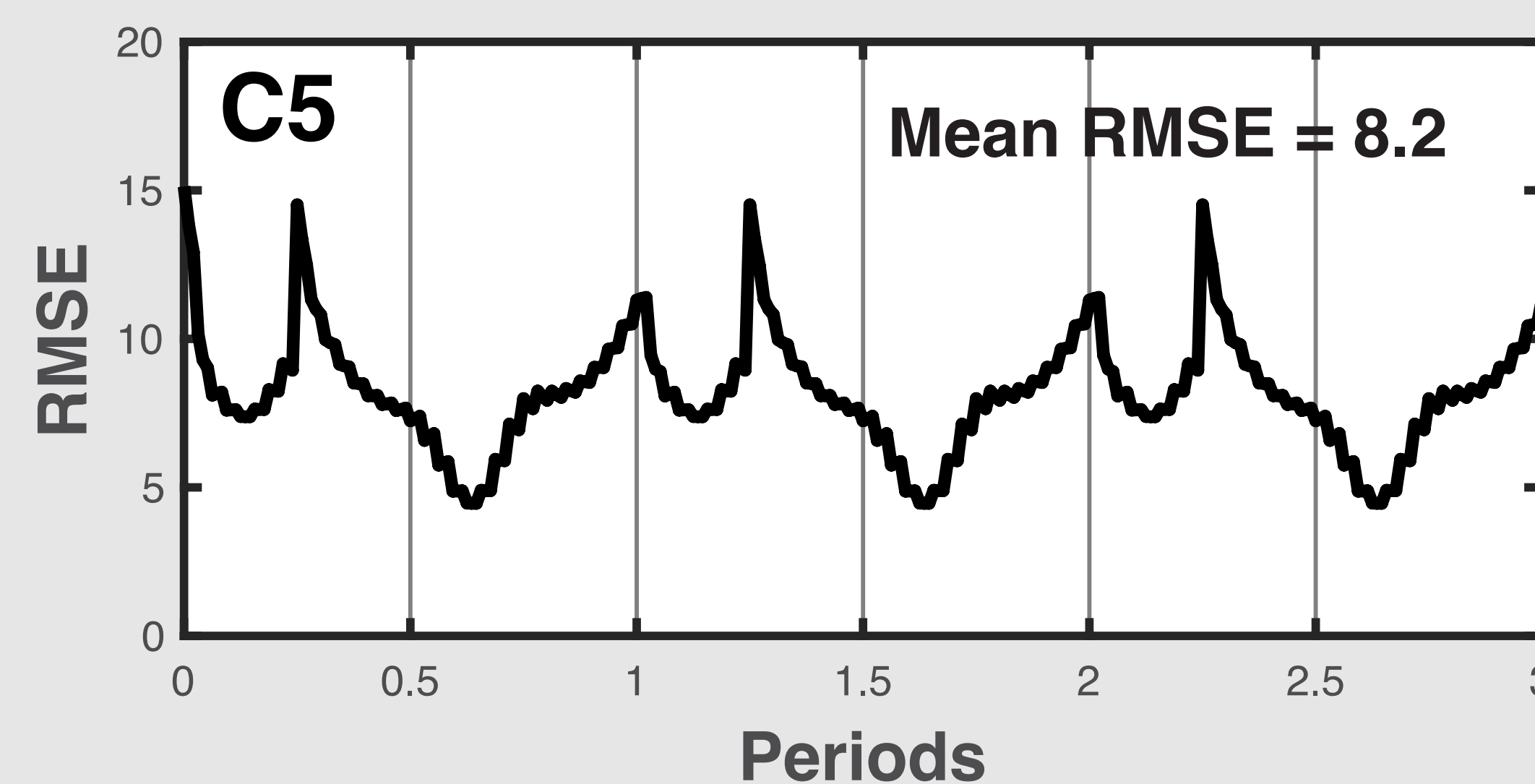
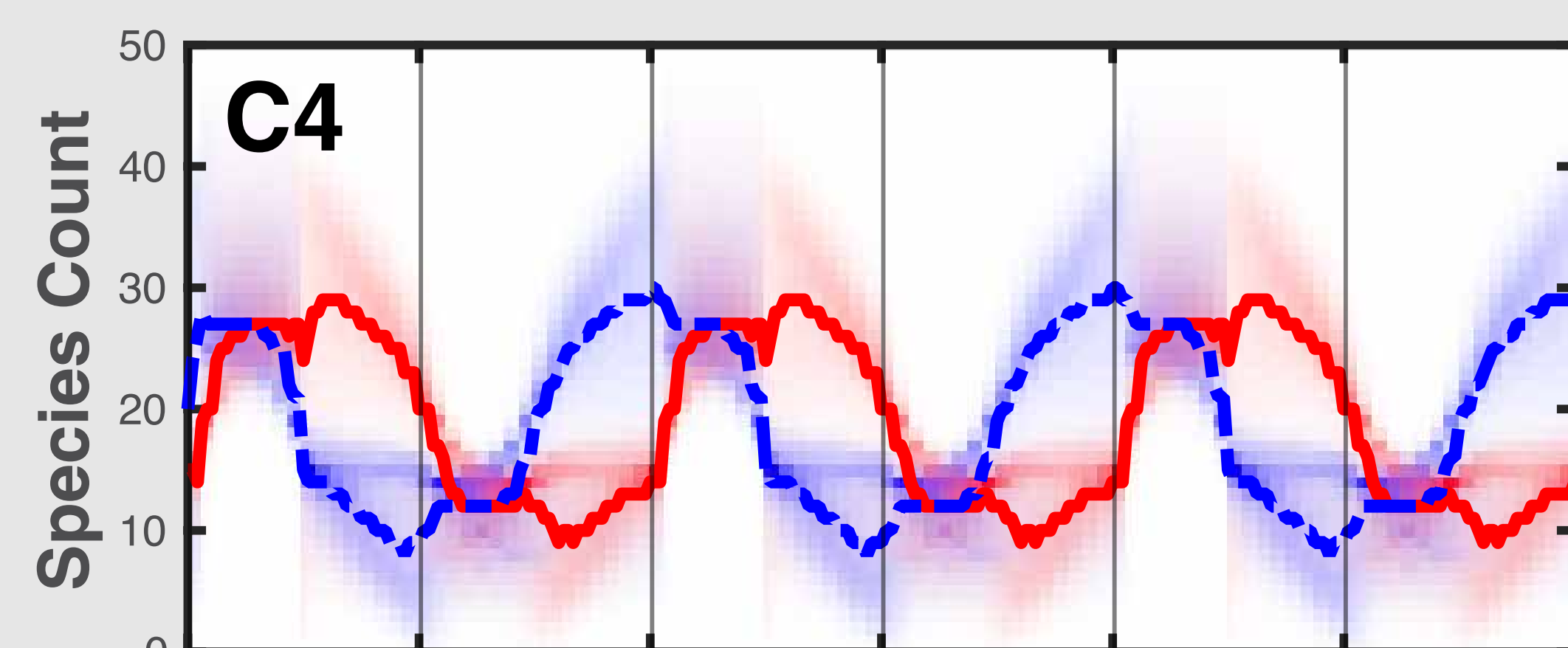
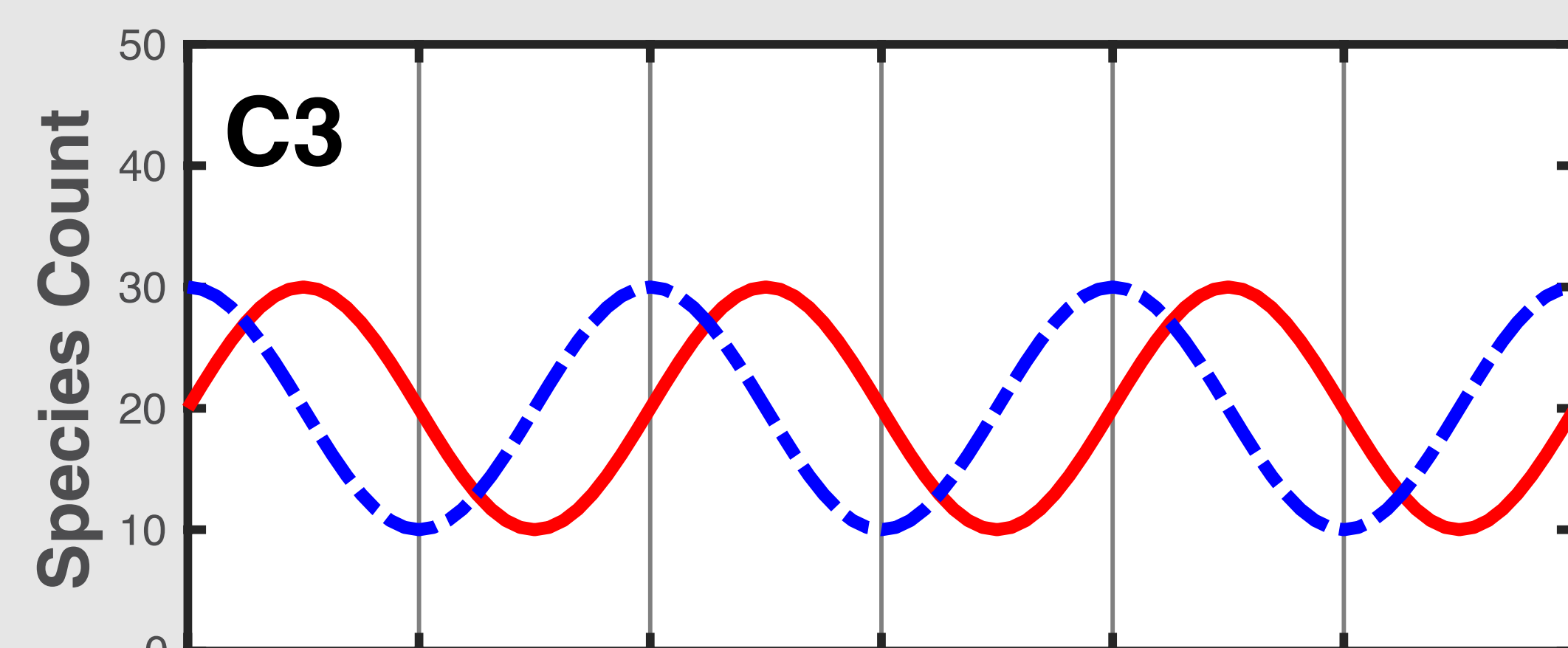
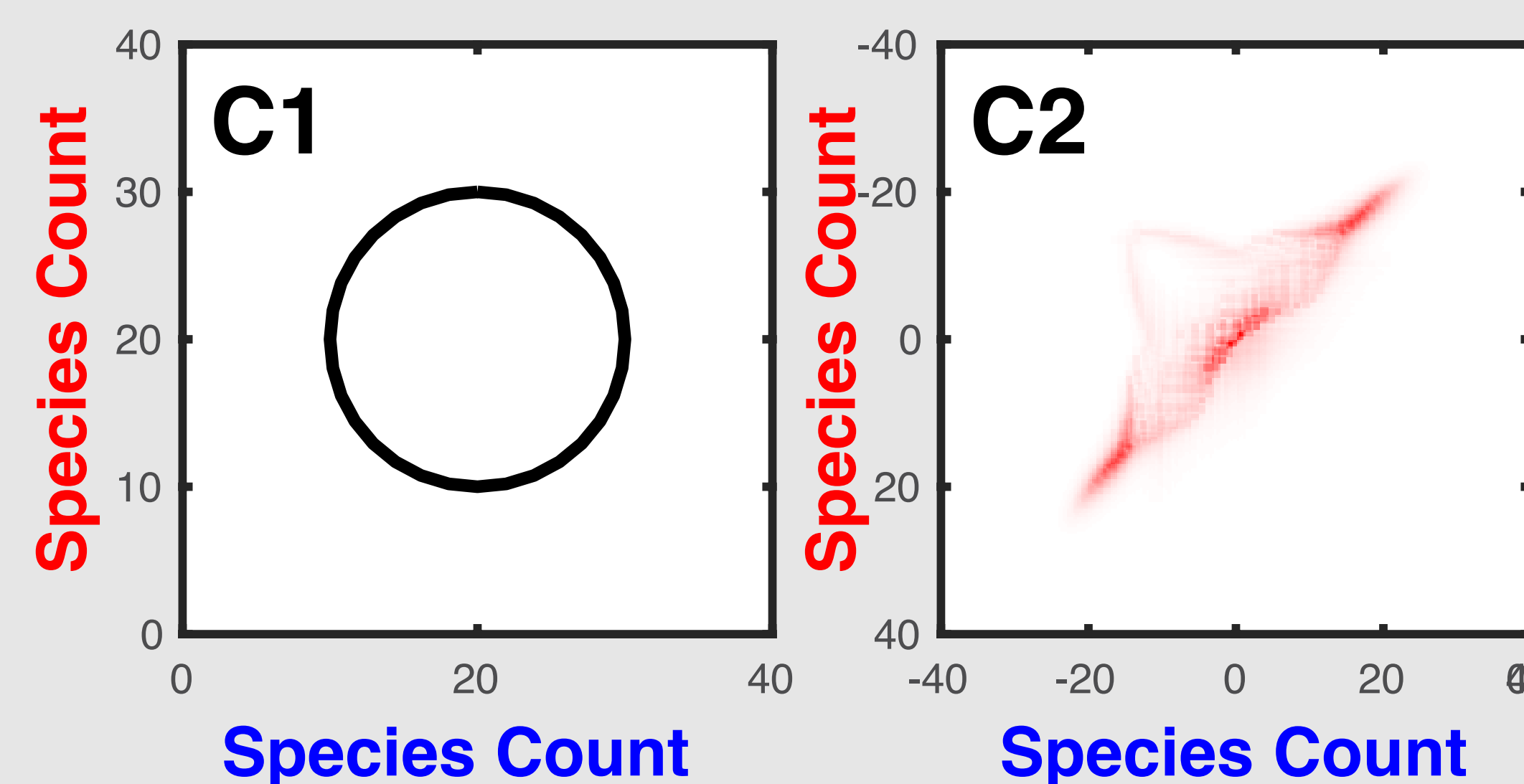
2. Phase Lagged

3. Frequency Changed

Synchronous



Phase Lagged



Frequency Changed

