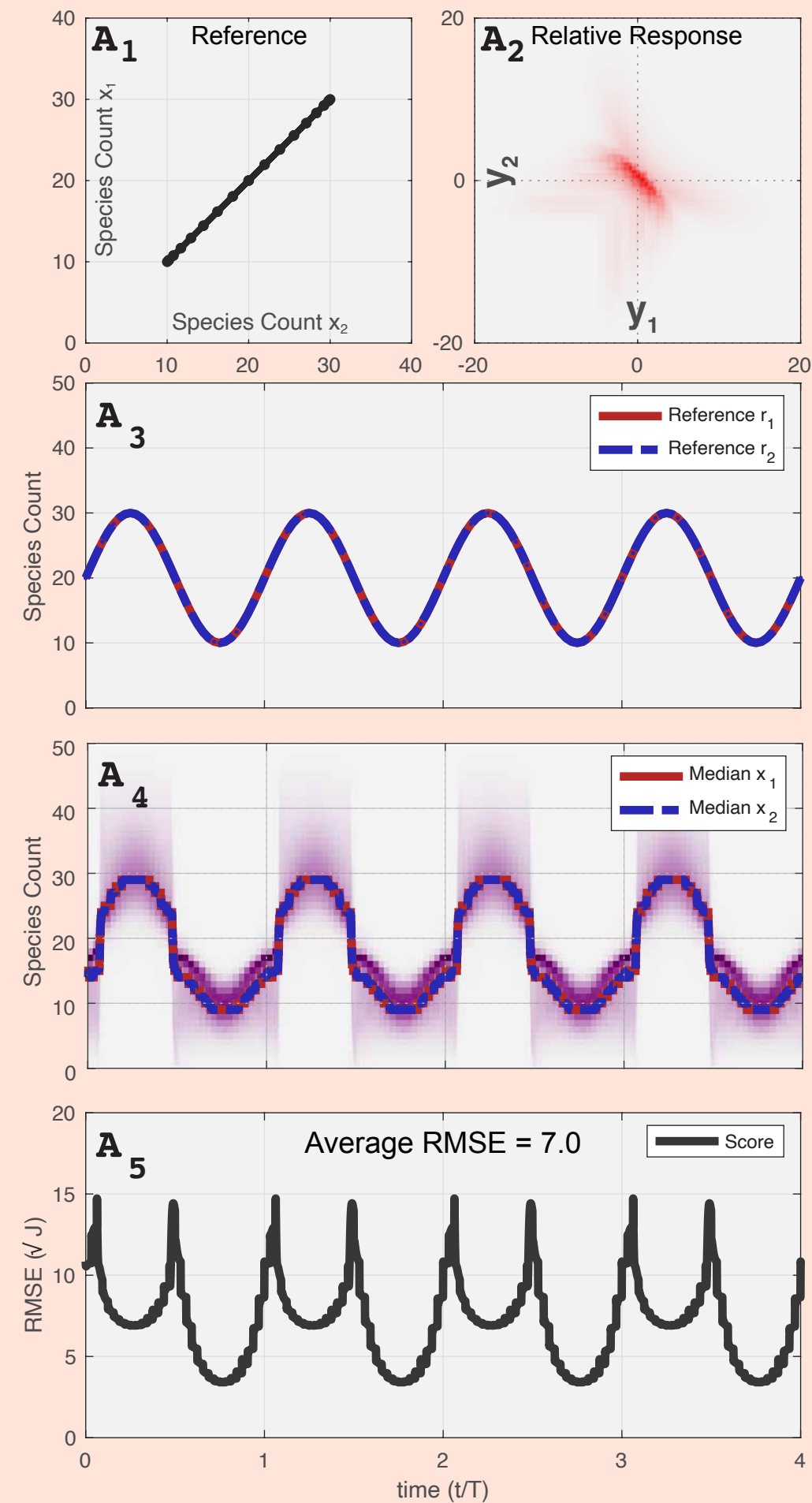


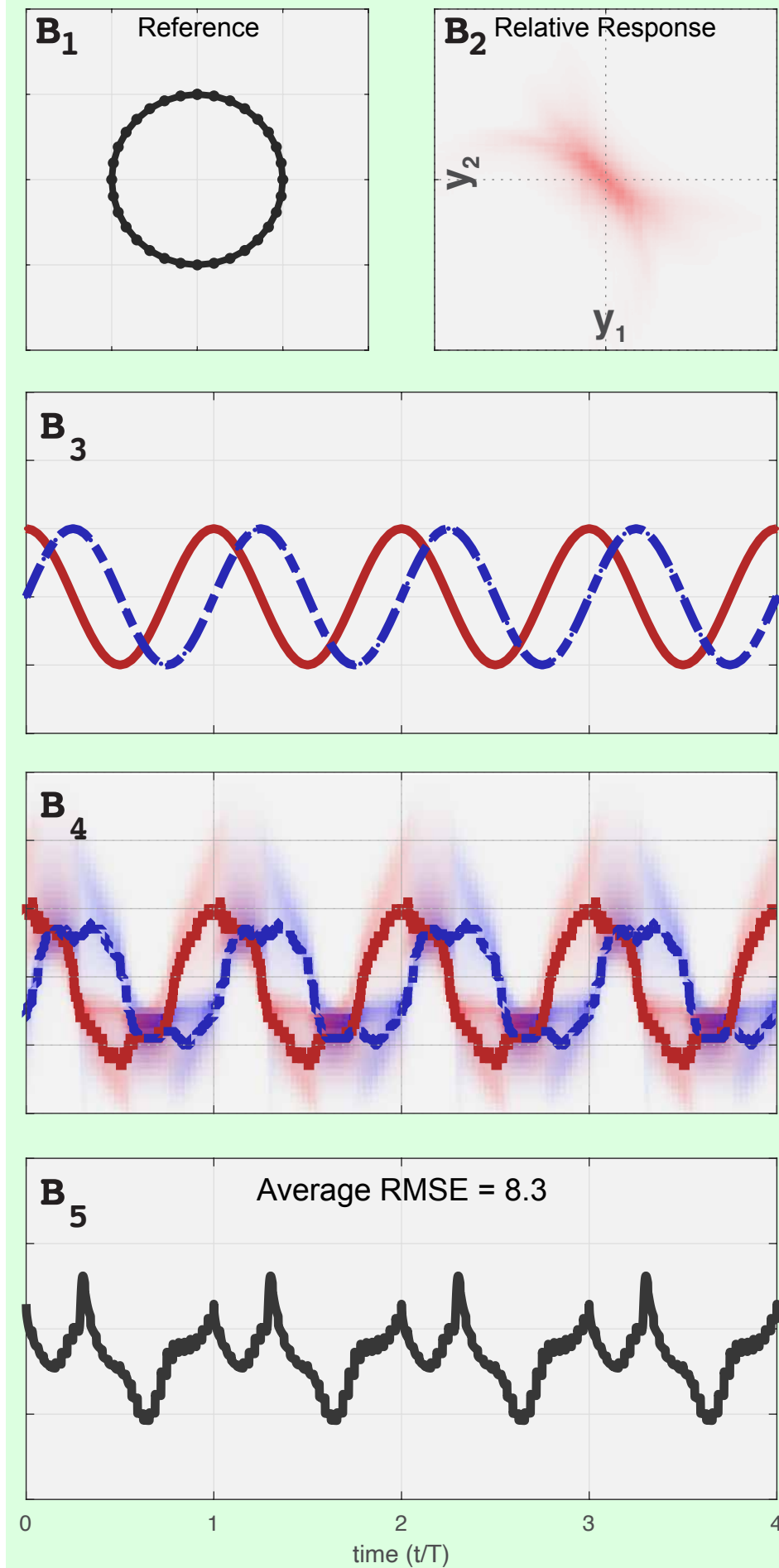
Control Pathway 1

Synchronous



Control Pathway 2

Phase Lagged



Control Pathway 3

Phase Laged + Frequency Difference

