

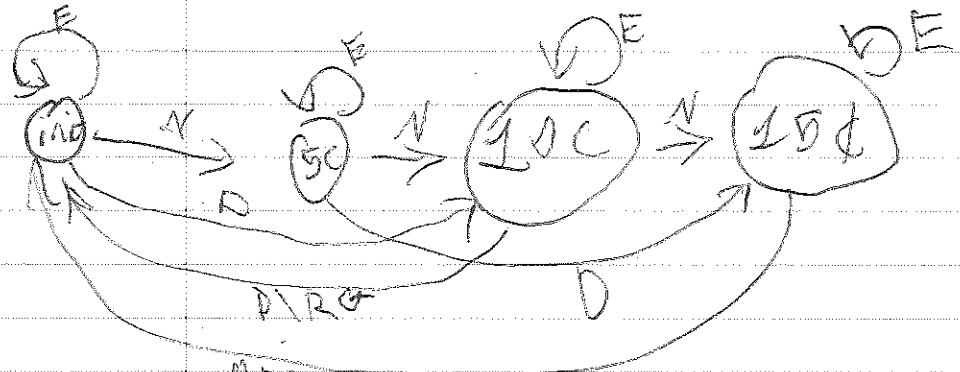
inputs, outputs, and states  
and encoding scheme

INPUTS  
coin  $x_2, x_1, x_0$  PSRE  $r$   
Empty  $00$  False  $0$   
nickel  $01$  true  $1$   
Dime  $11$

OUTPUT  
RG  $z_1$  RN  $z_0$   
false  $0$  false  $0$   
true  $1$  true  $1$

States  
init  $00$   
5C  $01$   
10C  $11$   
15C  $10$   
 $s_2 s_0$

State diagram and table



MRG or DMRG, RN

INPUT				INPUT				INPUT			
PS	E	N	D	PS	E	N	D	PS	E	N	D
init	init	5C	10C	init	0	0	0	init	0	0	0
5C	5C	10C	15C	5C	0	1	0	5C	0	0	0
10C	10C	15C	init	10C	0	0	1	10C	0	0	0
15C	15C	init	init	15C	0	1	1	15C	0	0	1
NS				RG				RN			

# State table in binary

	input				input				input		
PS	00	01	11	PS	00	01	11	PS	00	01	11
00	00	01	11	00	0	0	0	00	0	0	0
01	01	11	10	01	0	0	0	01	0	0	0
11	11	10	00	11	0	0	1	11	0	0	0
10	10	00	00	10	0	1	1	10	0	0	1
	NS				RG				RN		

## Minimization for flip flop inputs

PS	input	$J_1$	$K_1$	$J_0$	$K_0$
00	00	0	-	0	-
00	01	0	-	1	-
00	10	-	-	-	-
00	11	1	-	1	-
01	00	0	-	-	0
01	01	1	-	-	0
01	10	-	-	-	-
01	11	1	-	-	1
10	00	-	0	0	-
10	01	-	1	0	-
10	10	-	-	0	-
10	11	-	1	0	-
11	00	-	0	-	0
11	01	-	0	-	1
11	10	-	-	-	-
11	11	-	1	-	1

$$J_1 = X_1 + S_0 X_0$$

$$K_1 = X_1 + S_0' X_0$$

$$Q = Q'J + K'Q$$

kmaps for the outputs:

RG

		$X_0$			
		0	0	0	-
		0	0	0	-
$S_1$	0	0	0	1	-
	1	0	1	1	-
		$X_1$			$S_0$

$$RG = S_1 S_0' X_0 + S_1 X_1$$

RN

		$X_0$			
		0	0	0	-
		0	0	0	-
$S_1$	0	0	0	0	-
	1	0	0	1	-
		$X_1$			$S_0$

$$RN = S_1 S_0' X_1$$

kmaps for the flip-flop inputs:

$J_0$

		$X_0$			
		0	1	1	-
		-	-	-	-
$S_1$	0	-	-	-	-
	1	0	0	0	-
		$X_1$			$S_0$

$$J_0 = S_1' X_0$$

$K_0$

		$X_0$			
		-	-	1	-
		0	0	1	-
$S_1$	0	0	1	1	-
	1	-	-	-	-
		$X_1$			$S_0$

$$K_0 = S_1 X_0 + X_1$$

# final minimal expressions

$$Z_1 = S_1 S_0' X_0 + S_1 X_2$$

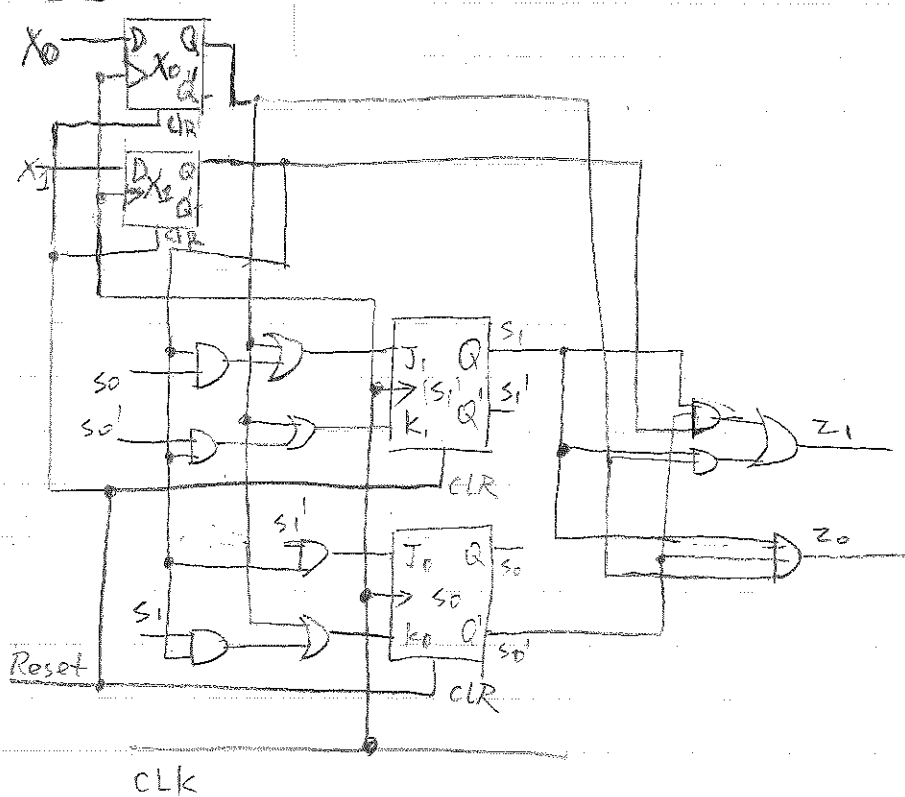
$$Z_0 = S_1 S_0' X_1$$

$$J_1 = X_1 + S_0 X_0$$

$$K_1 = X_1 + S_0' X_0$$

$$J_0 = S_1' X_0$$

$$K_0 = X_1 + S_1 X_0$$



We use a flip flops to synchronize the inputs