

GPIO bit index=3
Select FIFO (to avoid FIFO being filled during transfer to PS which results in pynq error)

EN_cpu_reset_server_response_get all

GPIO bit index=1
Controls EN_cpu_reset_server_request_put (tells CPU to reset itself)

After importing EMIO GPIOs in pynq, it isn't possible to use DMA anymore (because it seems to use these)

