D/P/LMC DSBGA NO. NAME

Α1

B1

C1

C2

C3

B3

А3

A2

NO.

Pin Functions

Output, Channel A

configurations

Positive Supply

Output, Channel B

Inverting Input, Channel A

Non-Inverting Input, Channel A

Inverting Input, Channel B

Non-Inverting Input, Channel B

DESCRIPTION

Ground for Single supply configurations, negative supply for dual supply

TYPE

0

0

Ρ

OUTA

-INA

+INA

GND / V-

+INB

-INB

OUTB

V+