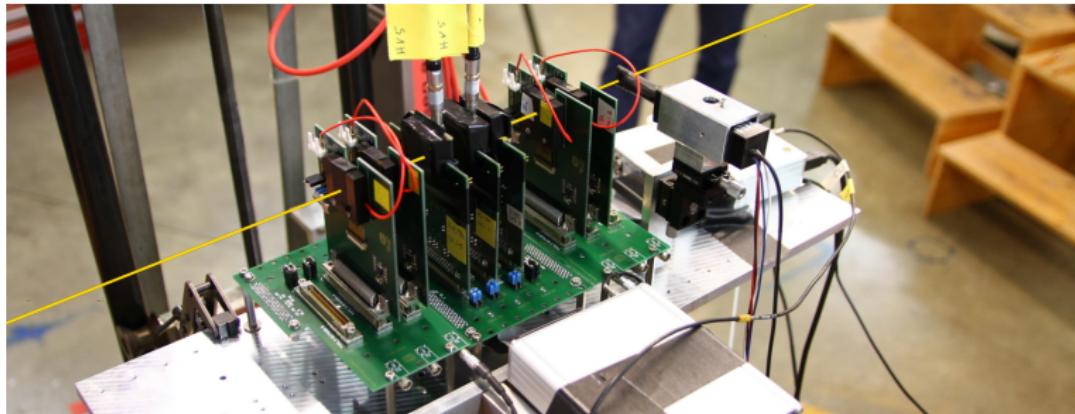


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ETH High Rate Beam Telescope

presented by: Michael Reichmann
coauthors: Felix Bachmair, Dmitry Hits

Motivation	The Telescope	Datataking	Commissioning	Analysis	Conclusion	Outlook	Backup
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Motivation

Motivation	The Telescope	Datataking	Commissioning	Analysis	Conclusion	Outlook	Backup
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Goal:

- testing of different types of diamond sensors for rate dependence (up to fluxes of 10 MHz/cm²)

Conditions:

- beam line PIM1 at PSI (Paul Scherrer institute)
- continuous pion beam with a flux of up to 10 MHz/cm² and momenta of 100-500 MeV/c

Requirements:

- small, flexible and modular system
 - ▶ reduce effects of multiple scattering
 - ▶ fast setup, easy to tear down,
- high rate continuous data taking
- scalable trigger area
 - ▶ high efficiency in the DUT
- precise trigger timing

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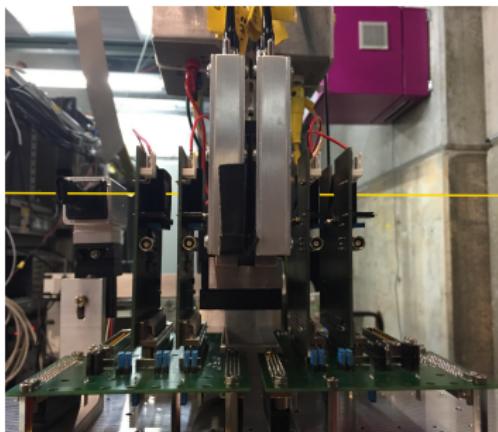
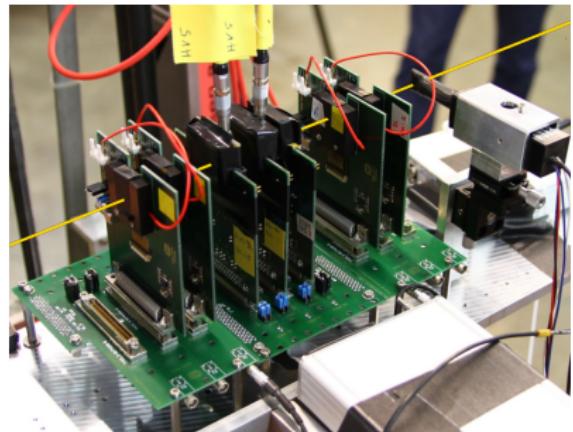
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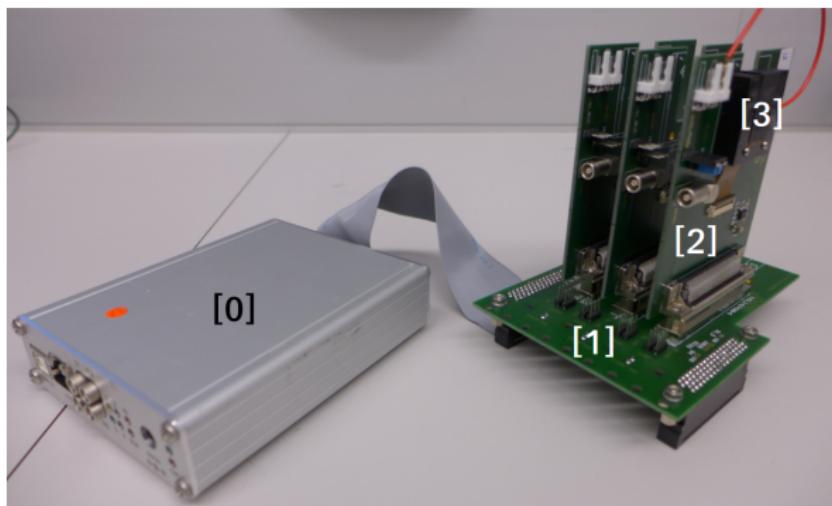
The Telescope





Telescope Module

Telescope Module



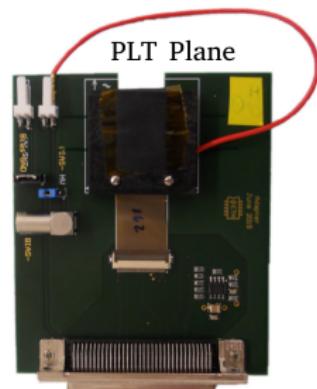
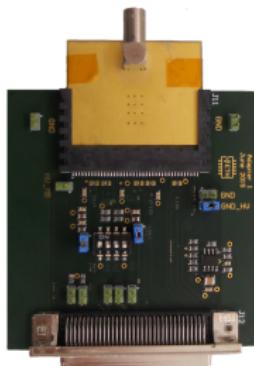
- [0] DTB (Digital Test Board): interface to a computer
- [1] Motherboard: main frame of the telescope
- [2] Adapter Planes: interface to the single pixel chips
- [3] CMS Pixel Chip (analogue or digital)



Parts

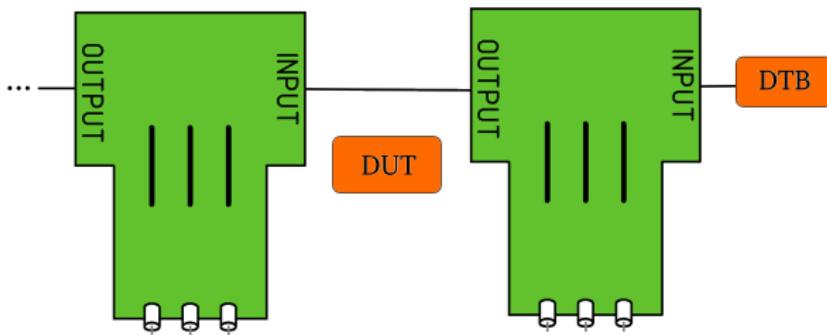
CMS Pixel Chips

	PSI46v2	PSI46dig	PROC600
Chip size		$\approx 8 \times 10 \text{ mm}^2$	
Pixel size		$150 \times 100 \mu\text{m}^2$	
Pixel array		52×80	
Pixel charge readout	analogue	digitised	digitised
Readout	multi level	40 MHz	160 MBit/sec
Hit rate	80 MHz/cm^2	120 MHz/cm^2	600 MHz/cm^2
Radiation Tolerance	200 kGy	1 MGy	6 MGy (exp.)
In-time threshold	3500 e	$\approx 1500 \text{ e}$	$\approx 1500 \text{ e}$
Fast-OR trigger	yes	no	yes

Parts**Motherboard****DTB Planes**



Schematic Setup

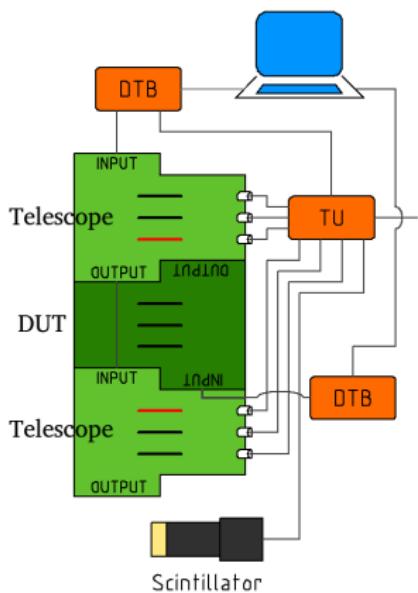


- chain several motherboards together into a single big telescope
- can only chain one chip type (analogue or digital)
- number of planes per motherboard is also variable (1 – 3)



Setups

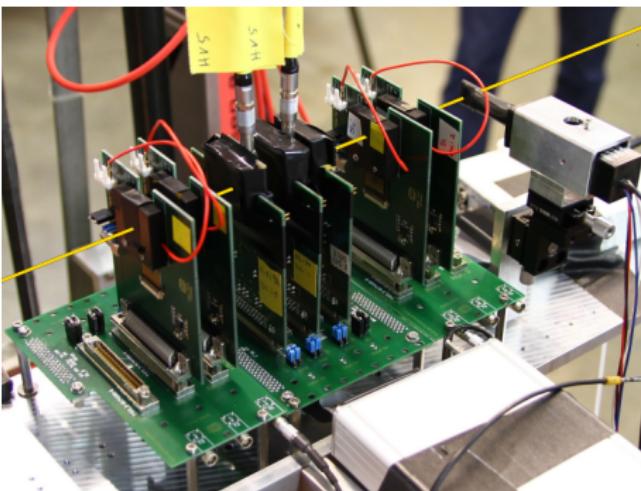
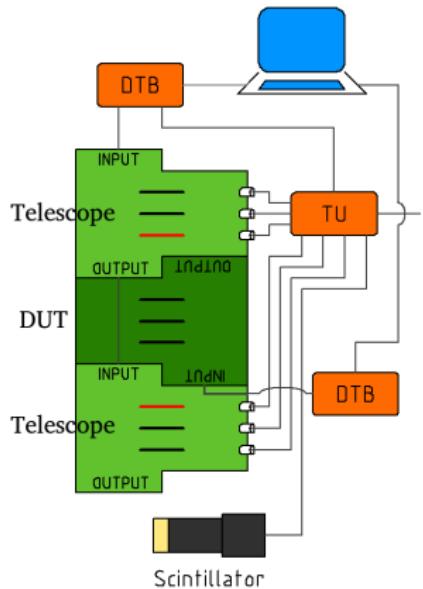
Diamond Pixel Setup



- telescope: two motherboards
 - ▶ analogue chips
- DUT: single motherboard
 - ▶ diamonds sensors on digital chips
- scintillator: precise trigger timing (fast-OR depends on clock, usually 40 MHz)
- trigger: coincidence of the two planes closest to the DUT (red) and the scintillator

Setups

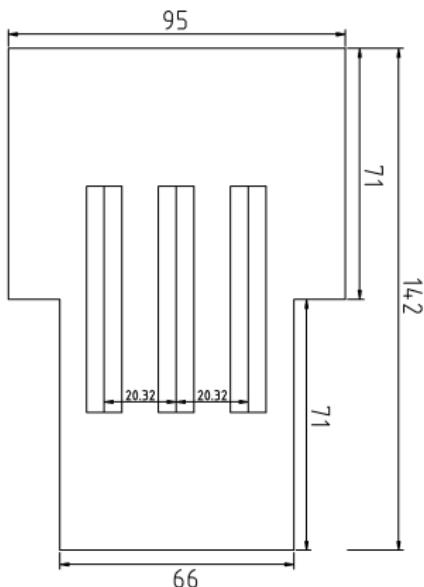
Diamond Pixel Setup





Setups

Specification



Spec	Value
Number of planes	variable
Interplane distance	20.32 mm
Module length	9.5 cm
Height	\approx 12 cm
Width	14.5 cm
Maximum trigger area	$7.8 \times 8 \text{ mm}^2$
Y-Resolution at PSI	$\approx 50 \mu\text{m}$ for pads $\approx 100 \mu\text{m}$ for pixel



Datataking

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EUDAQ

EUDAQ

Software:

- portable, modular and cross-platform DAQ framework
- developed for the EUDET Telescope
- can combine data streams from several different devices into an event based data stream
- utilises pXar to communicate with the telescope
 - ▶ pXar-core libraries: programming and readout of the CMS pixel chips

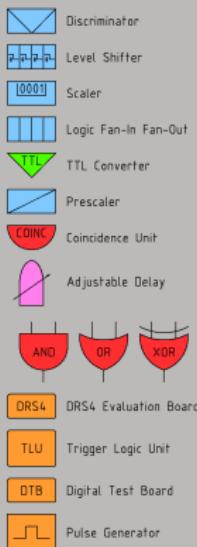
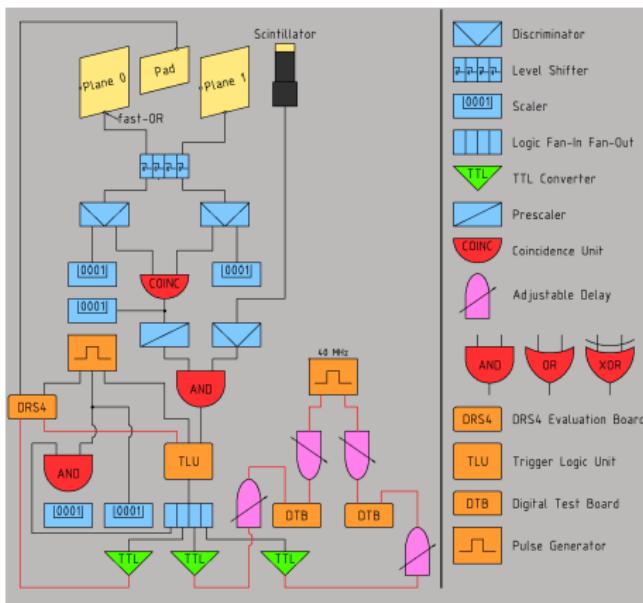
Extension: (with guidance from DESY)

- readout of the analogue chip with pXar and the DTB (thanks to Simon Spannagel!)
- readout of diamond pad sensors with DRS4 Evaluation Board
- adding new class to save whole waveforms



Trigger logic

Trigger logic



- coincidence between a fast-OR of planes before and after the DUT
- AND of scintillator and fast-OR coincidence as trigger
- OR with a low frequency pulser
- global external clock with adjustable delays
- busy signal after each trigger to avoid event misalignment
 - useful for events with many pixels hit



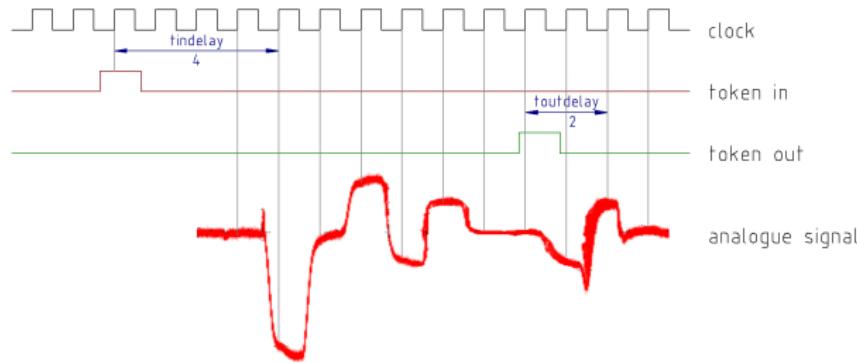
Commissioning



Inclusion of the analogue pixel chip

Inclusion of the analogue pixel chip

- analogue chips were read out with an Analogue Test Board (ATB)
 - ▶ limited buffer size (\rightarrow limited run time)
- adapting pXar to use the DTB for the readout (thanks to Simon Spannagel)
- need to adjust DTB timings:
 - ▶ token delays to find the begin and the end of the waveform
 - ▶ clock offset to sample at the center of each peak of the waveform





WBC scan

```

pxarCore >>> wbcScan
wbc      yield
090      0%
091      0%
092      0%
093      0%
094      0%
095      0%
096      0%
097      0%
098      0%
099      0%
100      2%
101      20%
102      98%
103      8%
104      0%
105      0%

ROC STATISTICS:
wbc    roc0    roc1    roc2    roc3
100    0       2       0       0
101    6       12      10      4
102    60      92      96      56
103    2       6       6       0
104    0       0       0       0

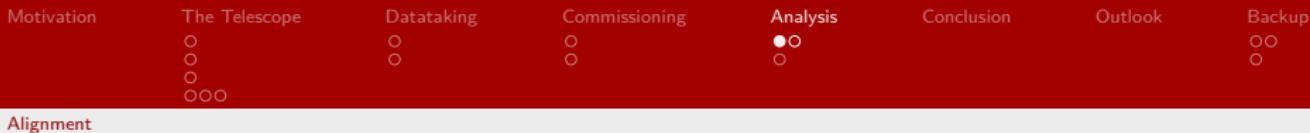
TRIGGER PHASE:
3      ||||| 8.9%
4      |::::::::::::::::::: 89.0%
5      | 2.1%

```

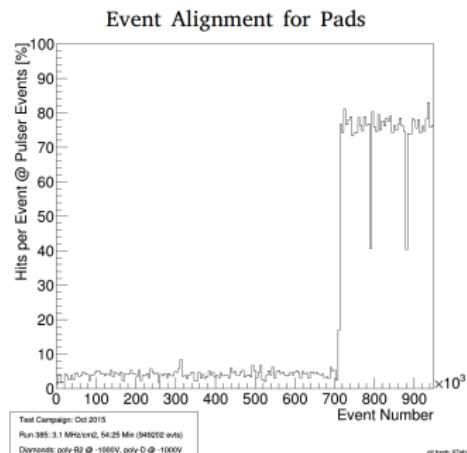
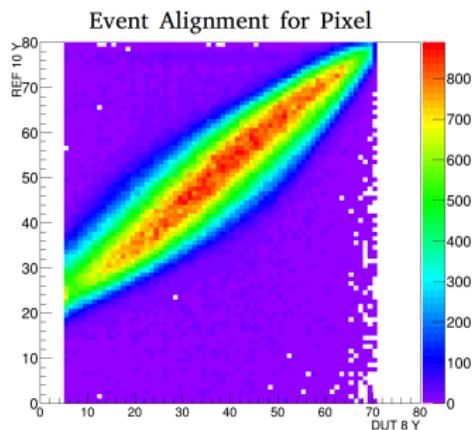
- ROC saves bunch crossing when particle hits the sensor
- programmable setting called wbc (wait bunch crossing)
- trigger only validates if time the trigger takes back to the ROC matches the wbc setting
- automated wbc scan using the pXar CLI
- detailed information about the hit yield (event has at least one hit) for every connected ROC
- information of the trigger phase (relative timing of the trigger compared to the clock)



Analysis



Event alignment



- compare x and y position of telescope and DUT

- use constant frequency pulser signal as reference
- expect less pixel hits at pulser events

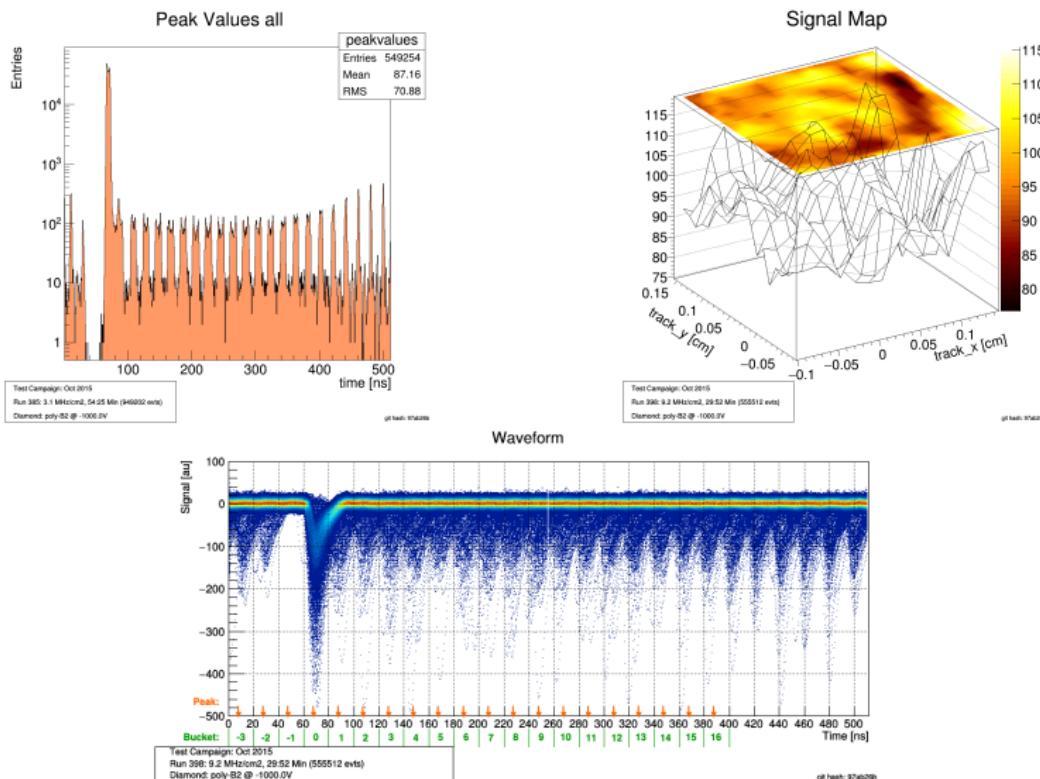
Motivation	The Telescope	Datataking	Commissioning	Analysis	Conclusion	Outlook	Backup
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	○	○	○				○
	○						
Alignment	○○○						

Plane alignment

- iterative procedure from the PLT
- moving track residuals to zero
- first rotation around beam axis
- translation in x-y plane perpendicular to the beam axis



Miscellaneous



Motivation	The Telescope	Datataking	Commissioning	Analysis	Conclusion	Outlook	Backup
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Conclusion

Motivation	The Telescope	Datataking	Commissioning	Analysis	Conclusion	Outlook	Backup
	○	○	○	○○			
	○	○	○	○			
	○						
	○○○						

- overcoming problems in the beginning
 - ▶ finding a good design and testing the single components
 - ▶ readout of the analogue chip by the DTB
 - ▶ adapting the softwares pXar and EUDAQ
- great working telescope for our purposes
 - ▶ reliable tracking and alignment
 - ▶ not fully working event alignment (can be fixed afterwards)
- setup time atm about half a day
- still room for improvement



Outlook

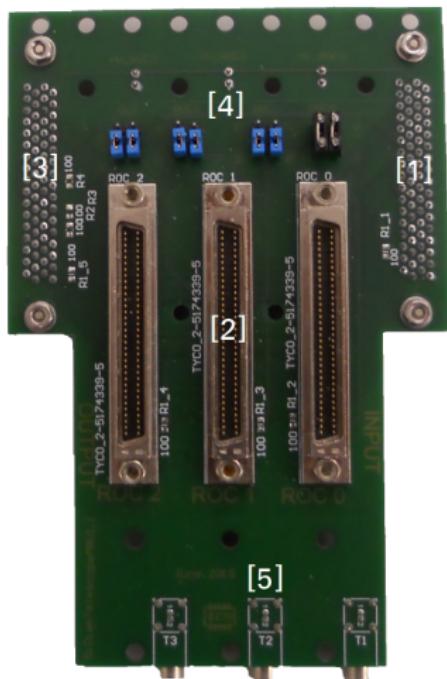
Motivation	The Telescope	Datataking	Commissioning	Analysis	Conclusion	Outlook	Backup
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	○○○						

- use the OSU TU (Trigger Unit) as single device to process all triggers
- two preinstalled setups for pad and pixel tests
- synchronise DTB clock with the beam clock at PSI ($40 \rightarrow 50$ MHz)
- save scintillator signal with the DRS4
 - ▶ particle identification by time of flight
- increase resolution by tilting the telescope planes
- using PROC600 as chip for the telescope
- test PSI-ROC4SENS - a ROC without threshold



Telescope Parts

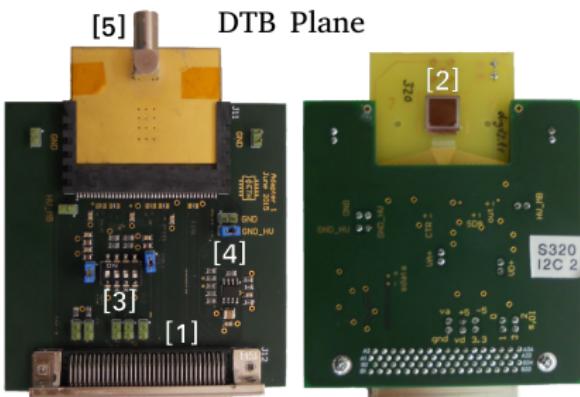
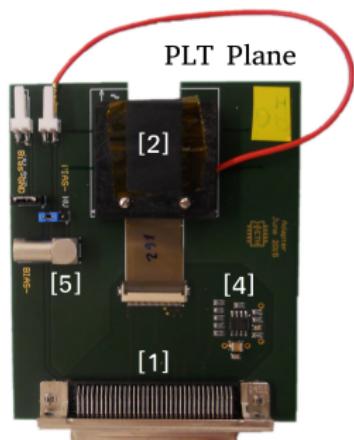
Motherboard



- [1] input: SCSI connector to the DTB
- [2] sockets for the adapter planes
- [3] output (optional): SCSI connector to another motherboard
 - ▶ daisy-chainable
- [4] token jumpers:
 - ▶ blue = plane used
 - ▶ black = plane skipped
- [5] output of the fast-OR trigger signal

Telescope Parts

Adapter Planes



- [1] SCSI connector to MB
- [2] CMS pixel chip
- [3] bit switch for I²C address

- [4] fast-OR amplifying circuit
- [5] sensor bias input



The DTB

The Digital Test-Board

- FPGA including soft Token Bit Manager (TBM) emulator
- clock and external trigger inputs
- connectors: USB, low voltage and scsi
- LEMO high voltage input for biasing the sensors
- internal ADC

Figure : DTB inside



Figure : DTB front and back

