

A102x251-3.xml

WT

15

10

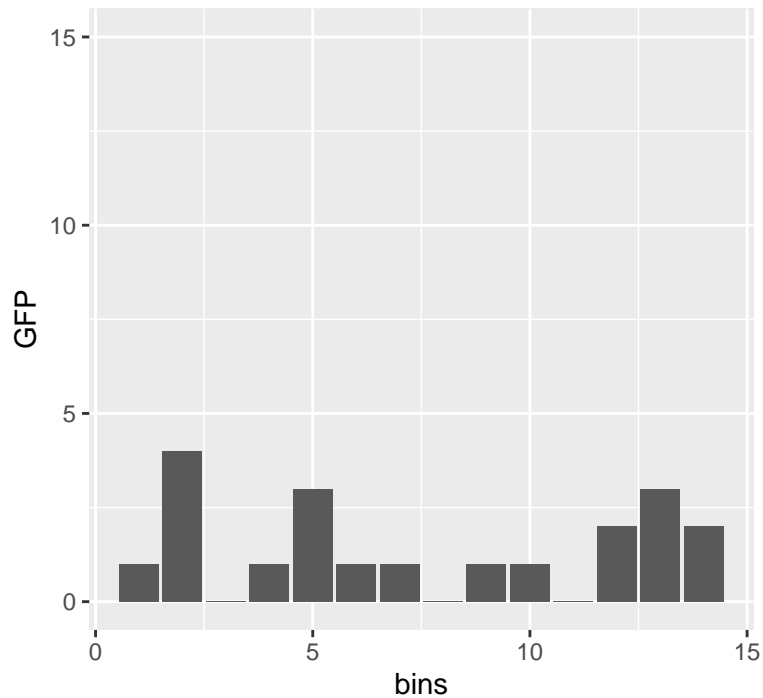
5

0

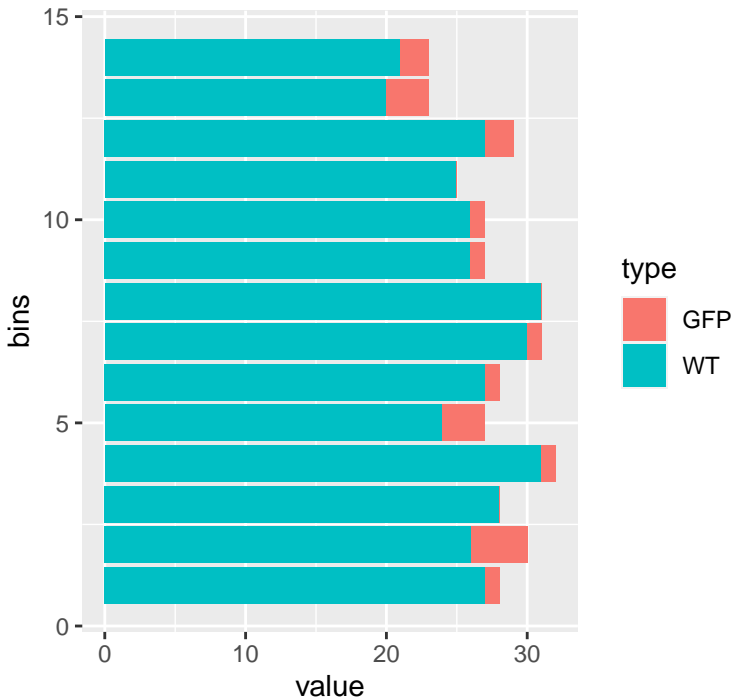
bins



A102x251-3.xml



A102x251-3.xml



A102x251-4.xml

WT

15

10

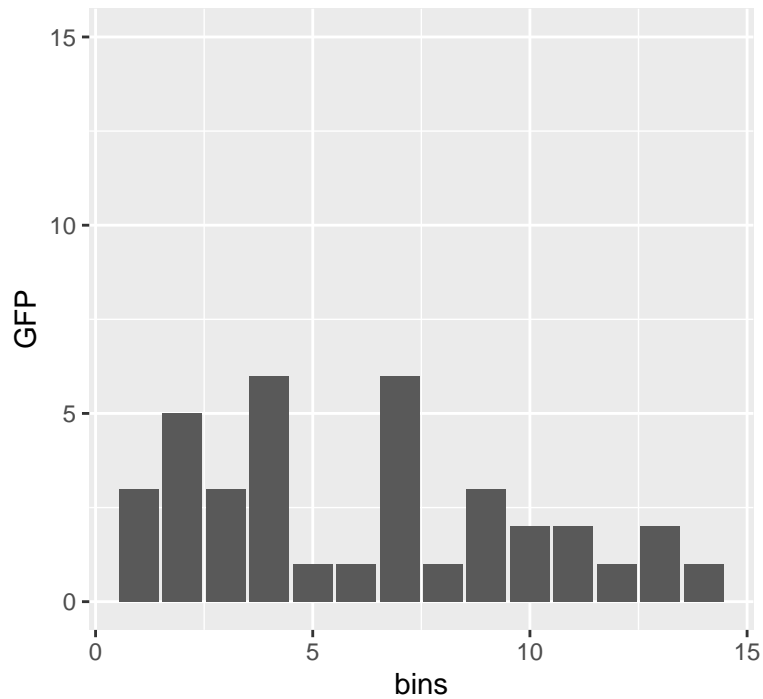
5

0

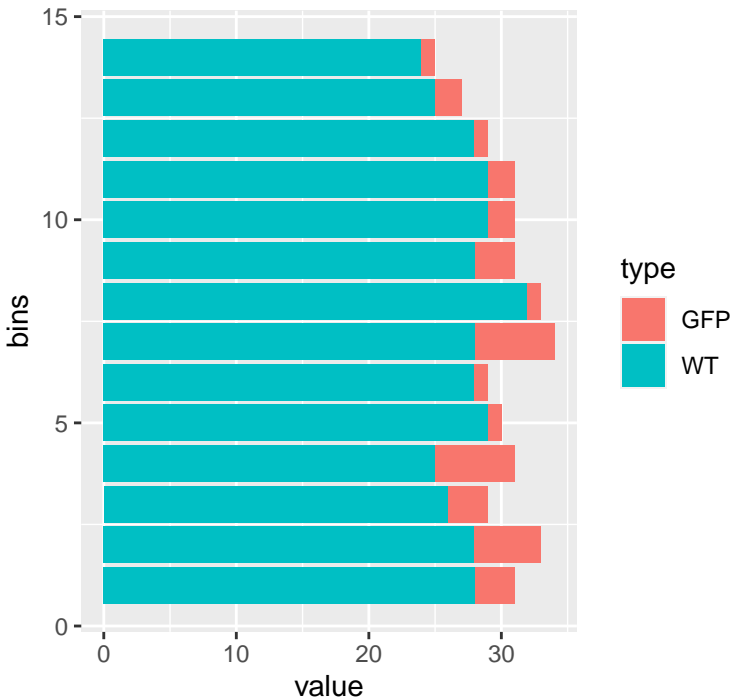
bins



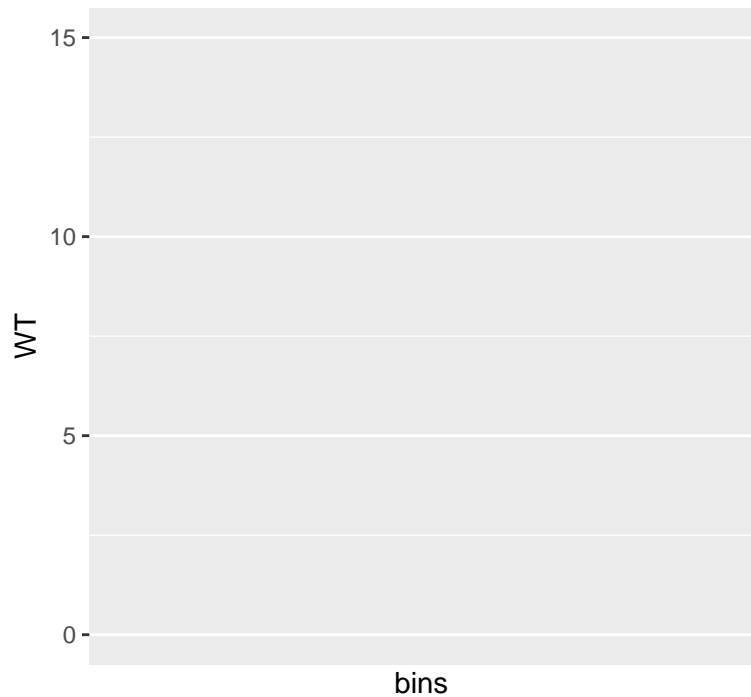
A102x251-4.xml



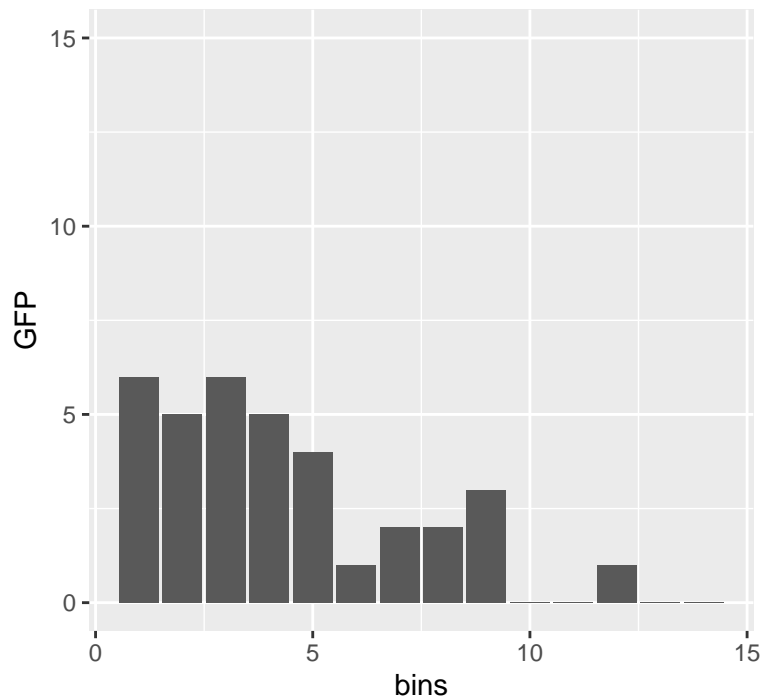
A102x251-4.xml



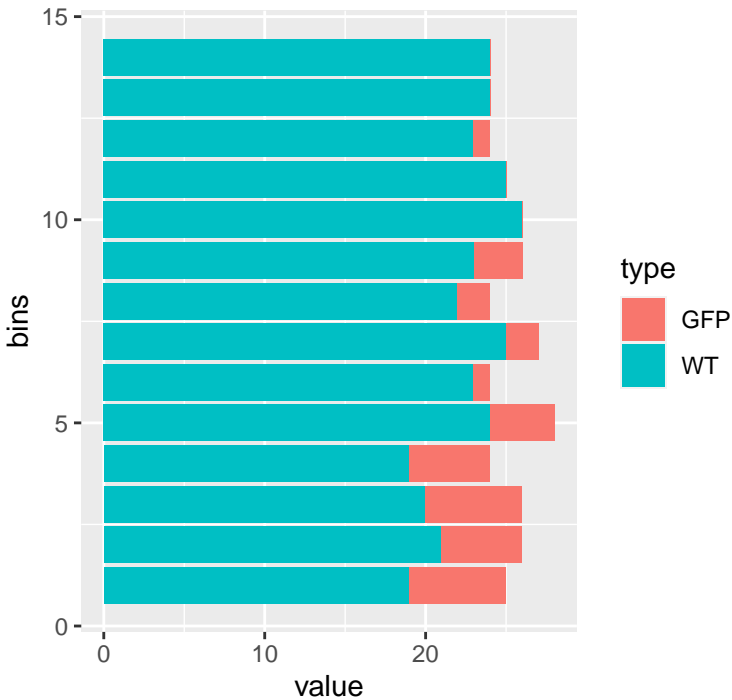
B2x571-1_inference.xml



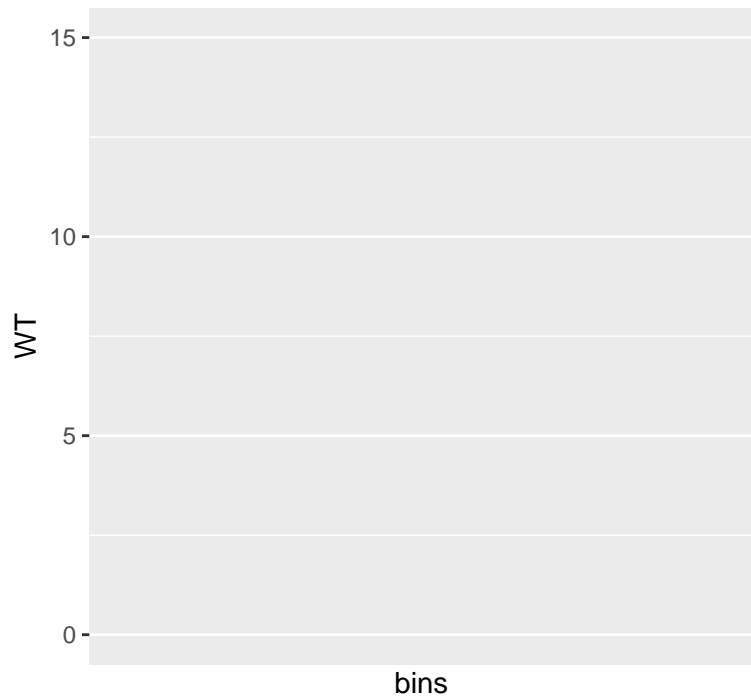
B2x571-1_inference.xml



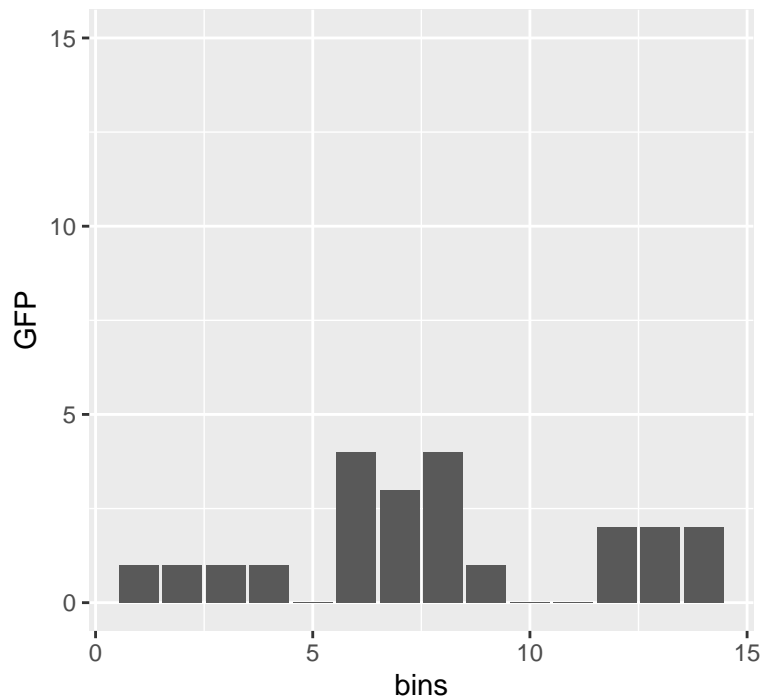
B2x571-1_inference.xml



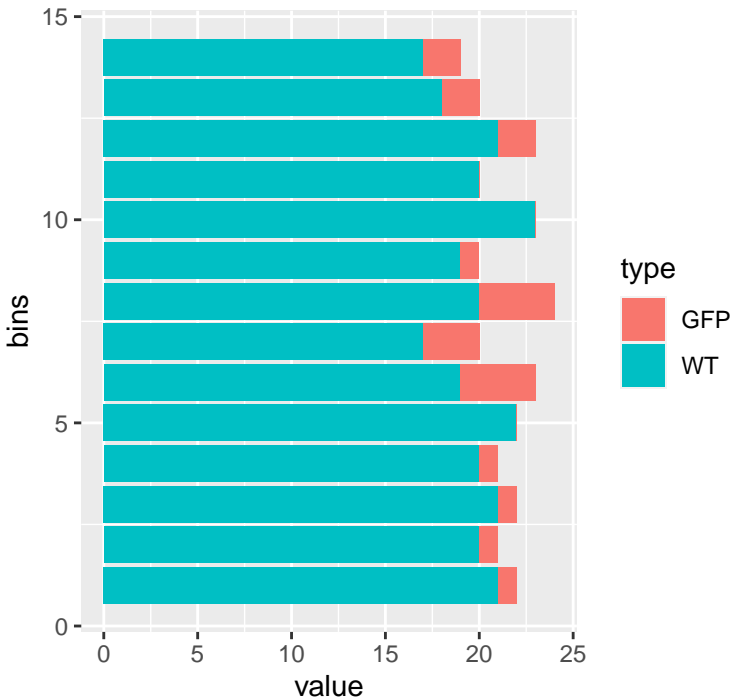
B2x571-2_inference.xml



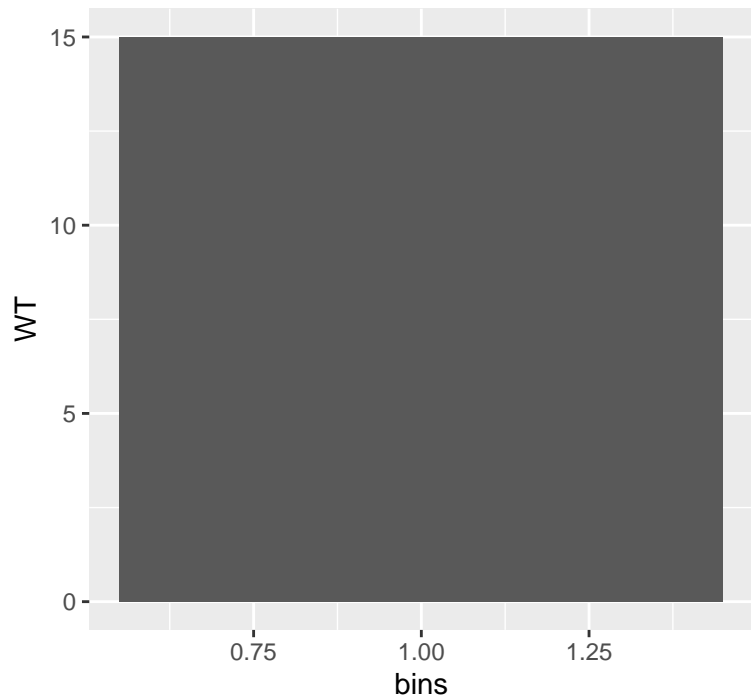
B2x571-2_inference.xml



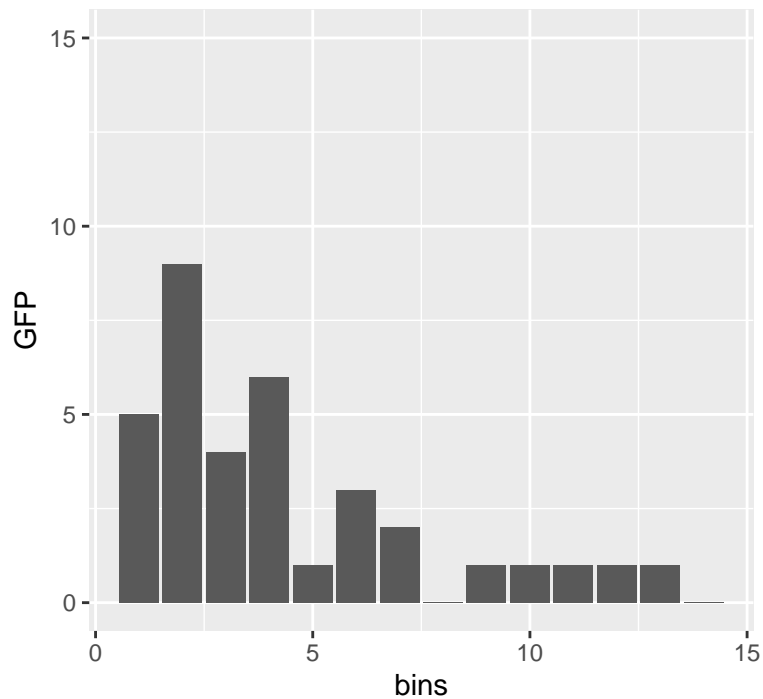
B2x571-2_inference.xml



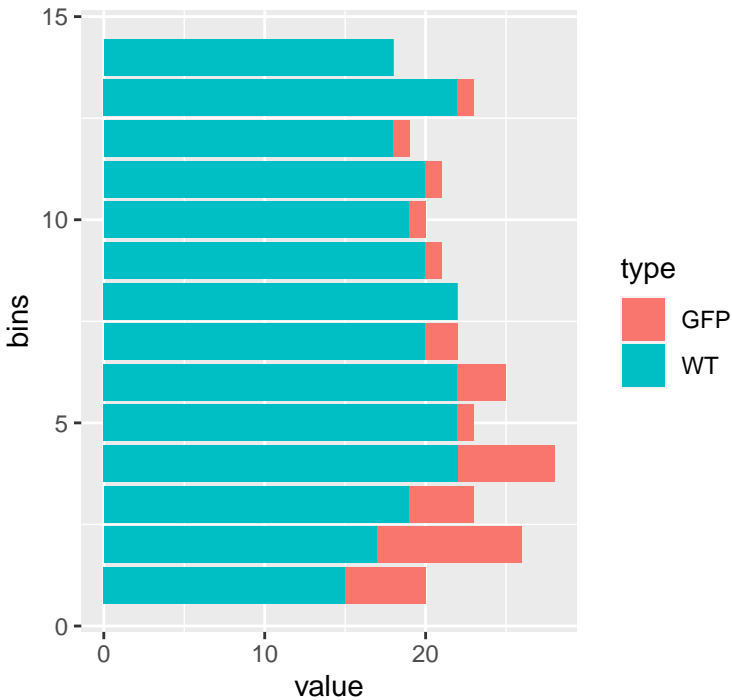
B2x571-3_inference.xml



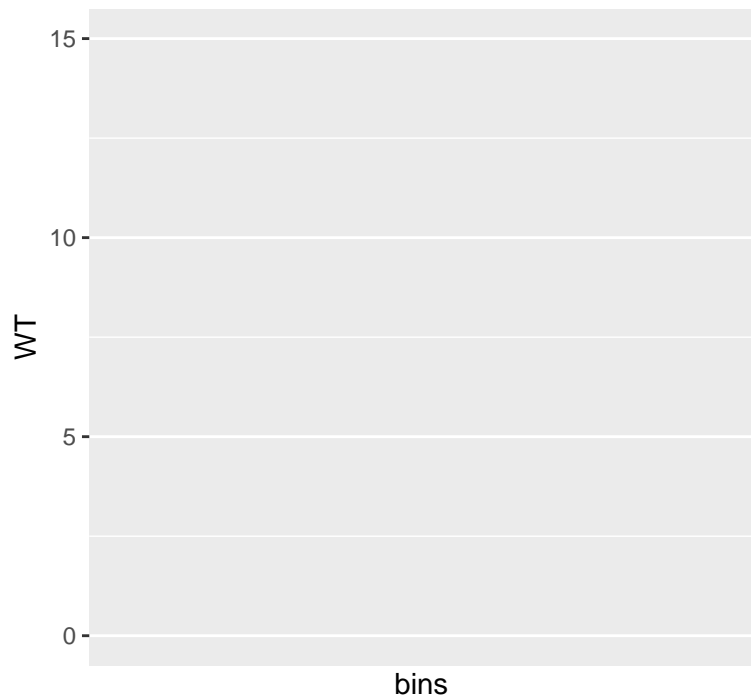
B2x571-3_inference.xml



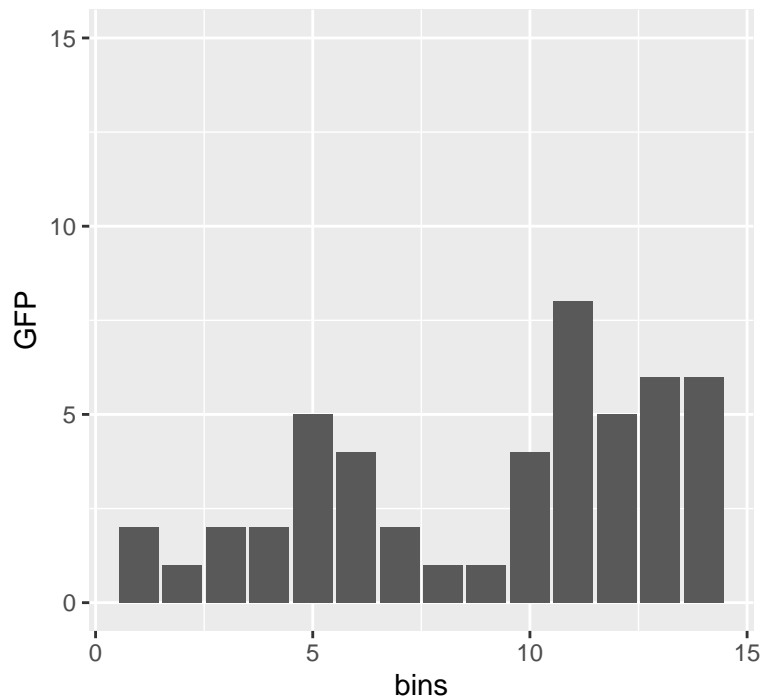
B2x571-3_inference.xml



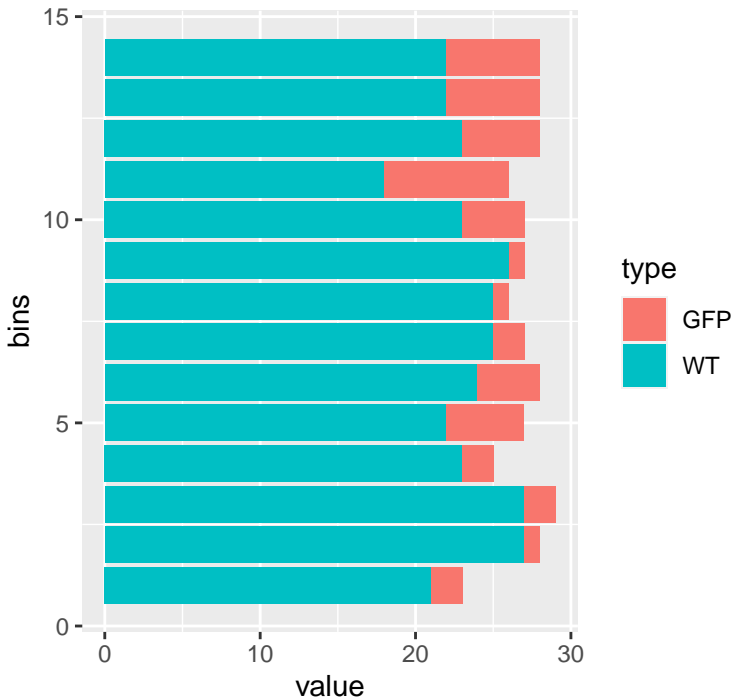
Y1x331AL-11.xml



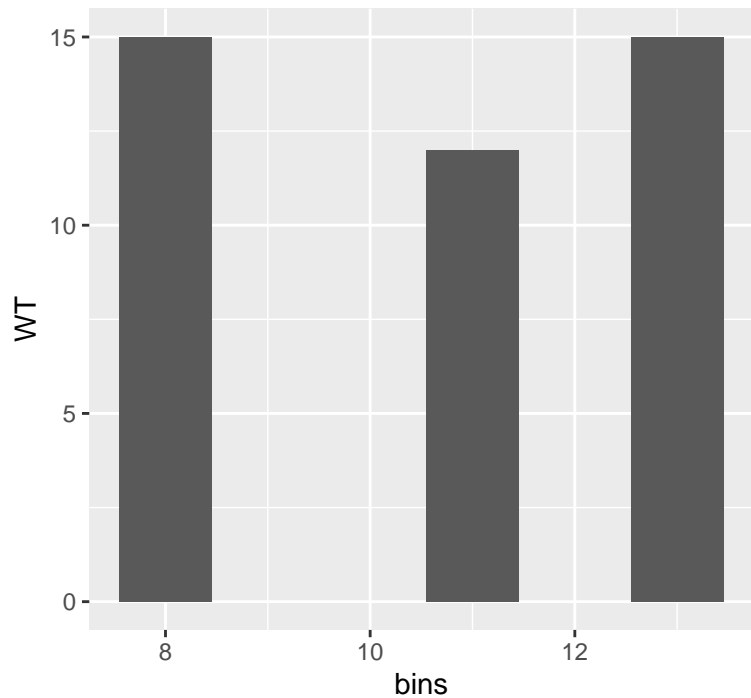
Y1x331AL-11.xml



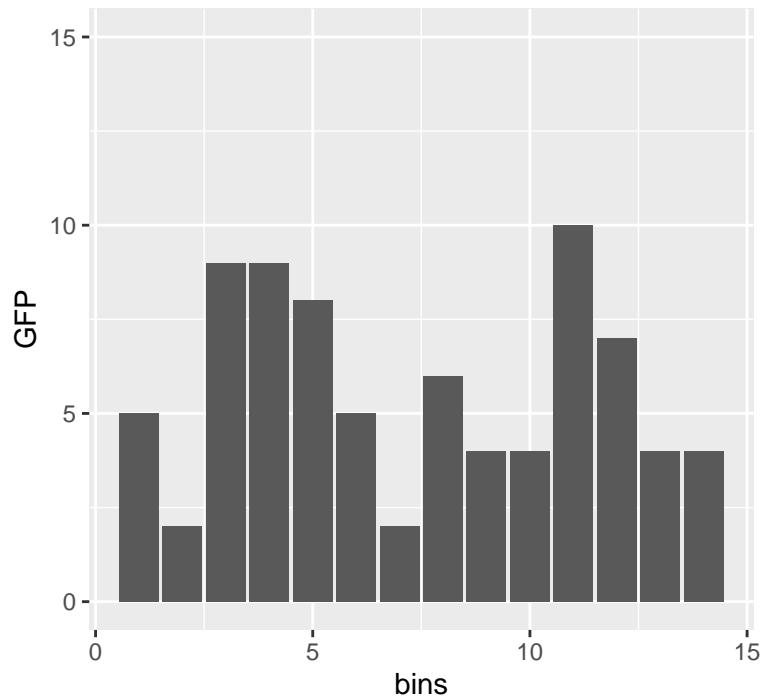
Y1x331AL-11.xml



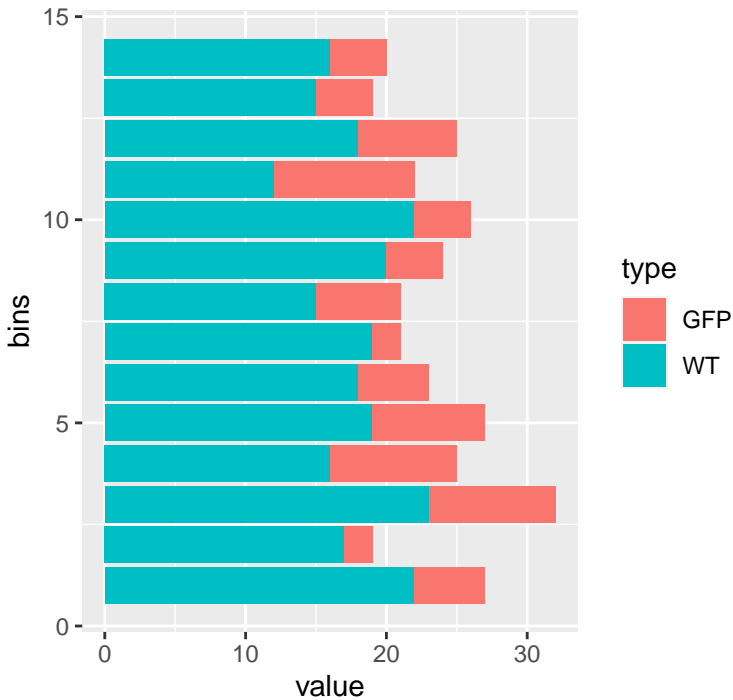
Y1x331AL-2.xml



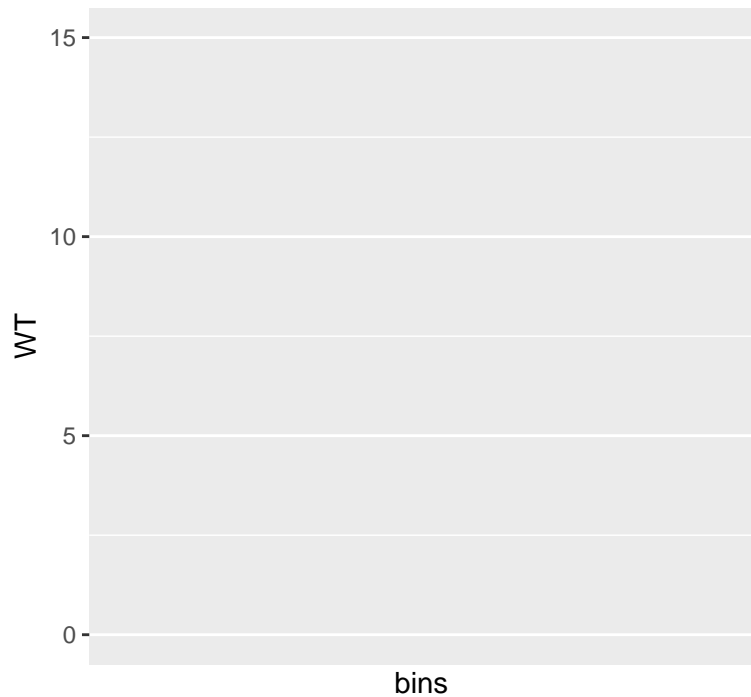
Y1x331AL-2.xml



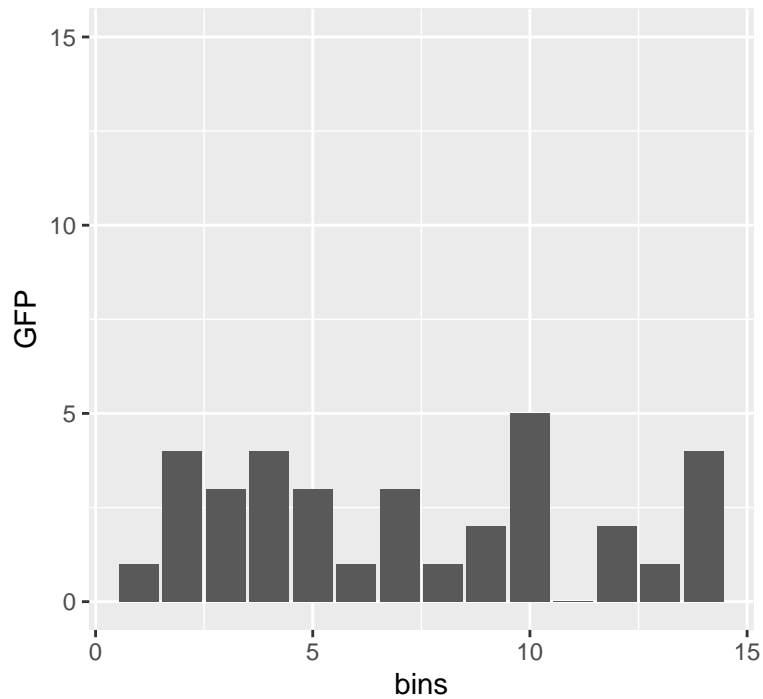
Y1x331AL-2.xml



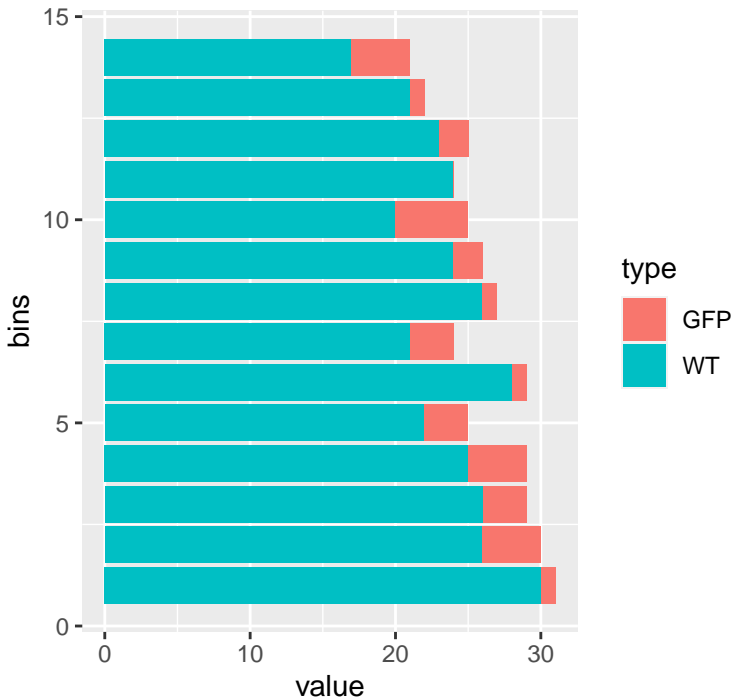
Y1x331AL-7.xml



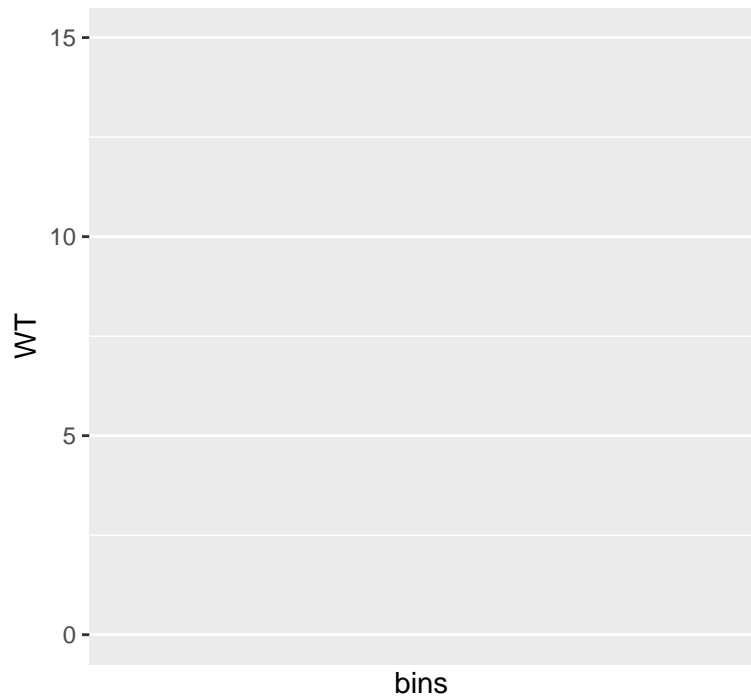
Y1x331AL-7.xml



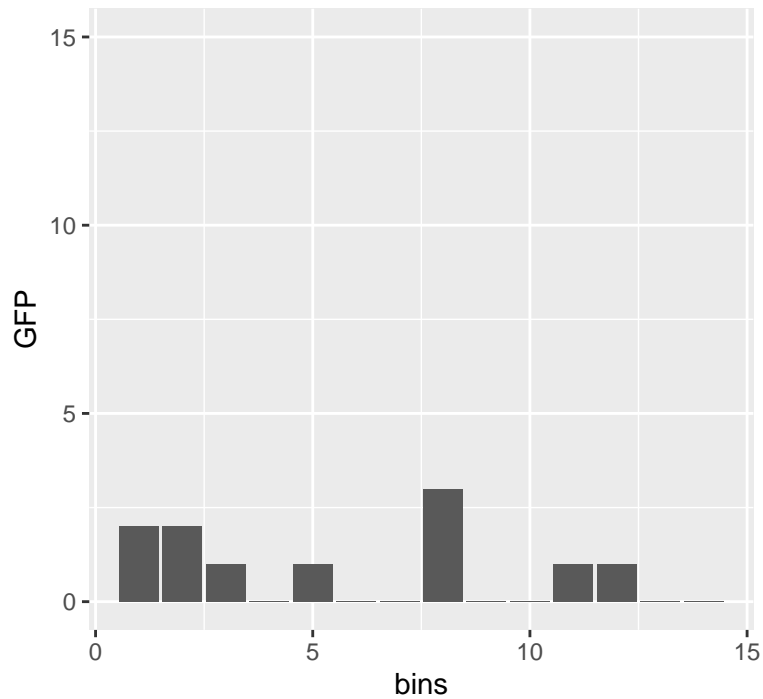
Y1x331AL-7.xml



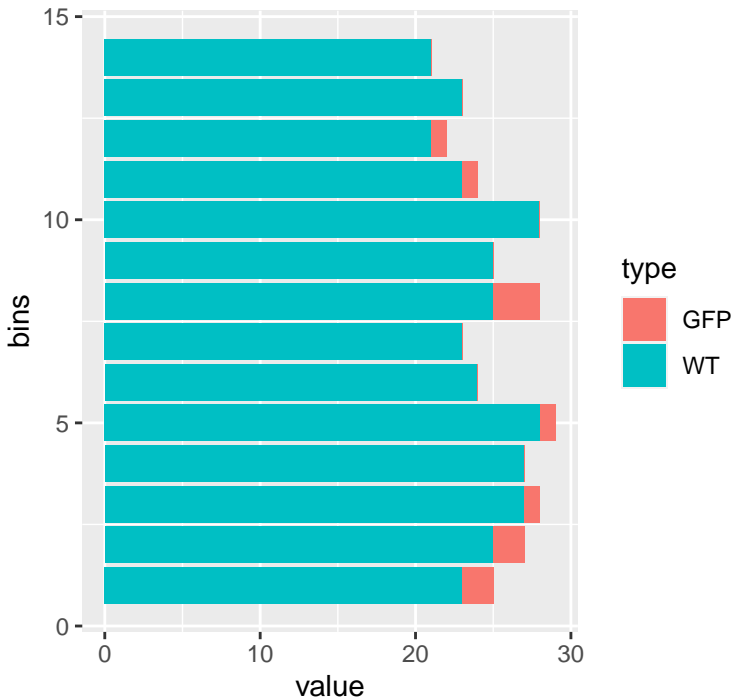
Y1x331AL-8.xml



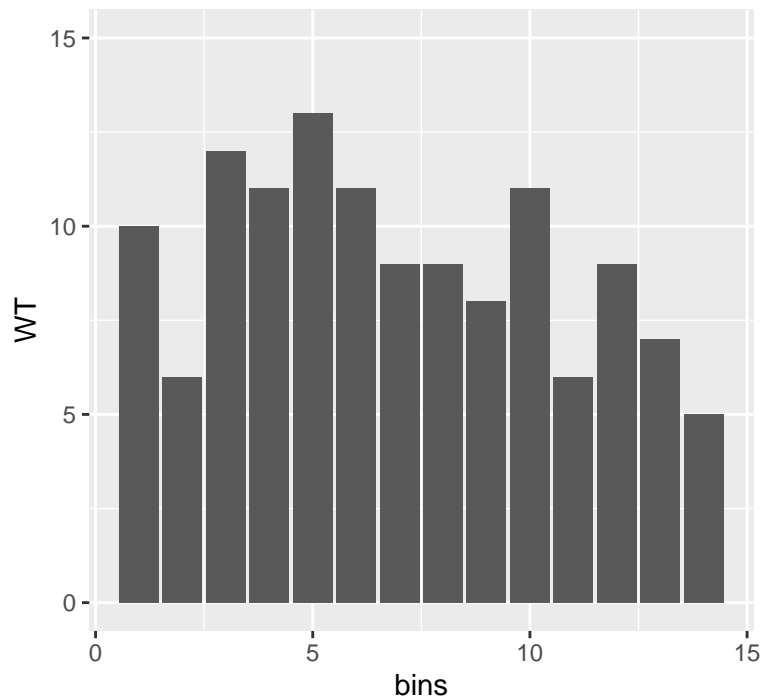
Y1x331AL-8.xml



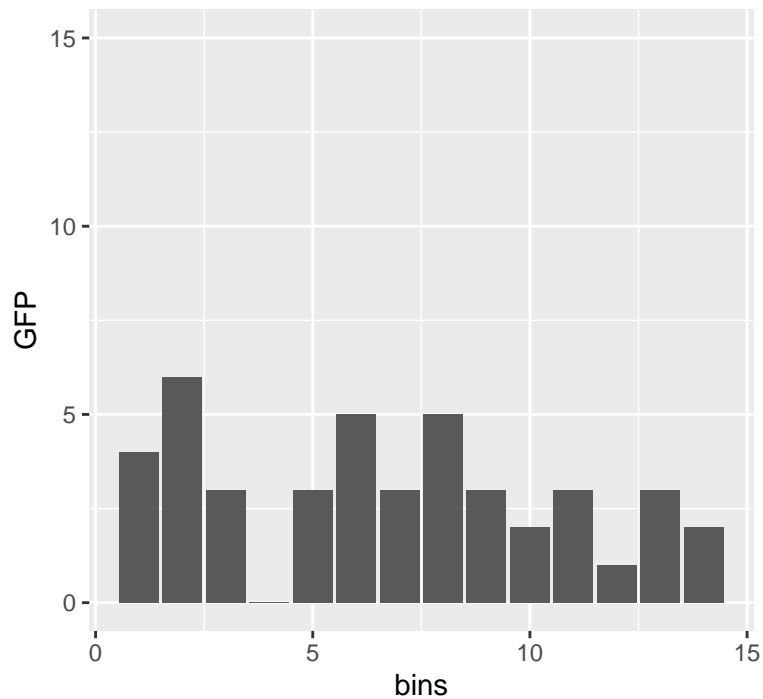
Y1x331AL-8.xml



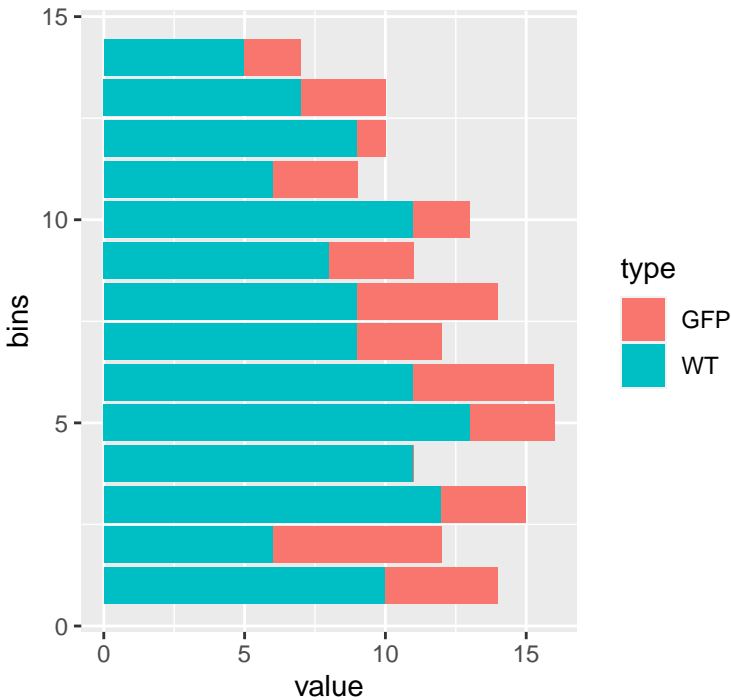
Y2x220-1_inference.xml



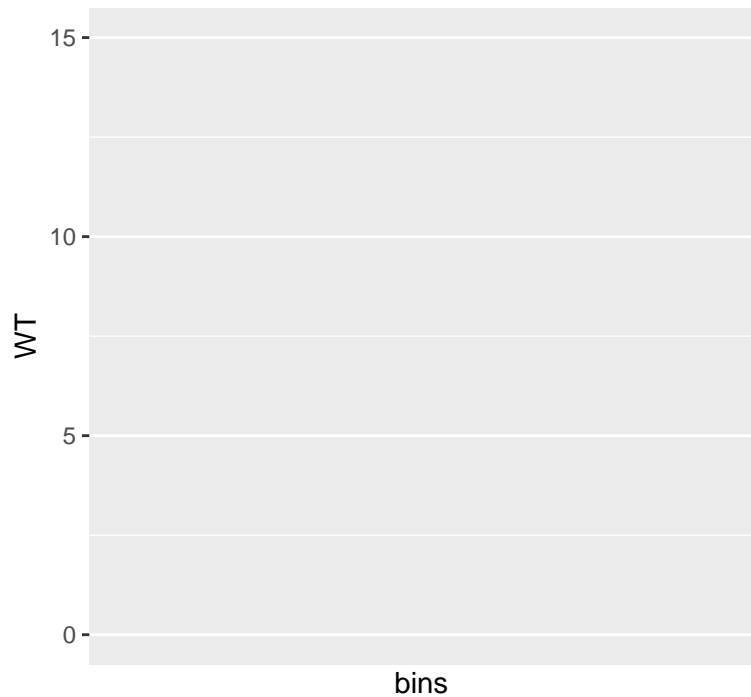
Y2x220-1_inference.xml



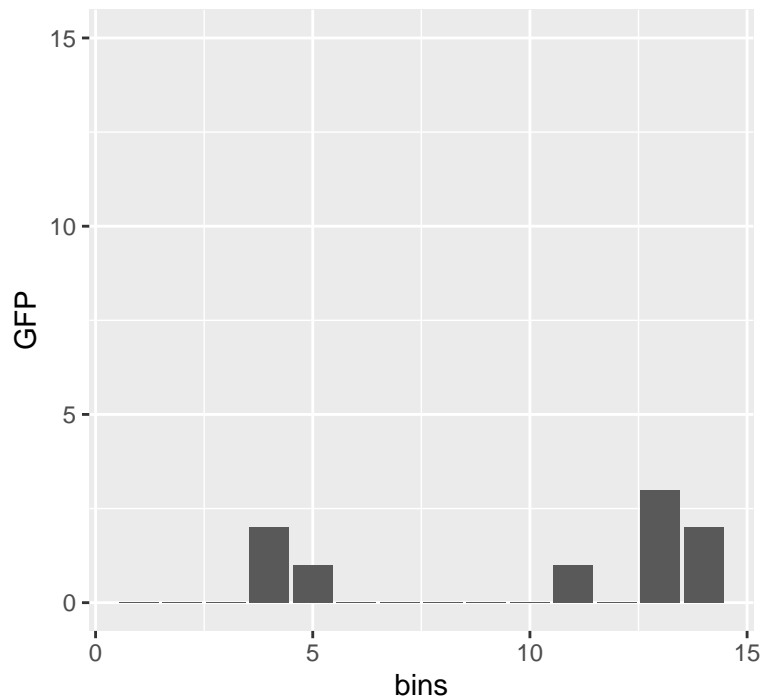
Y2x220-1_inference.xml



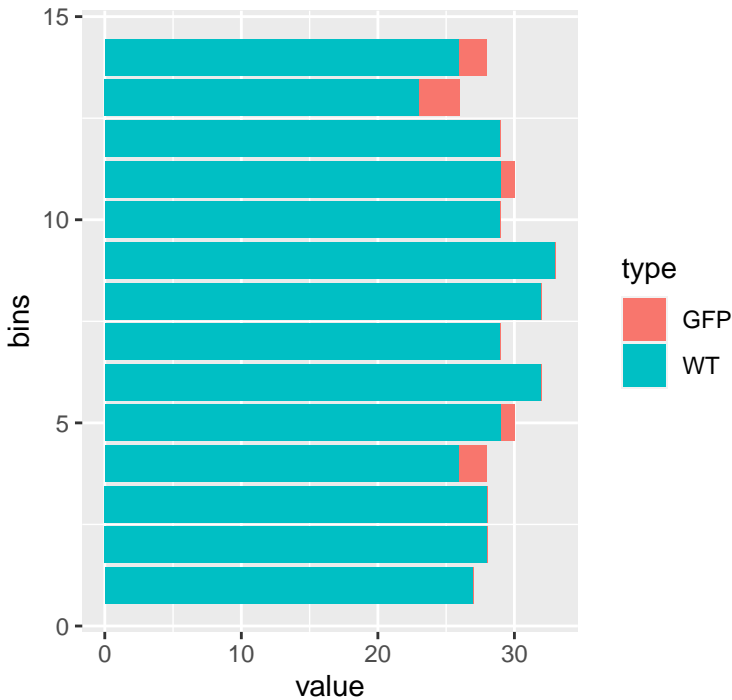
Y2x220-3.xml



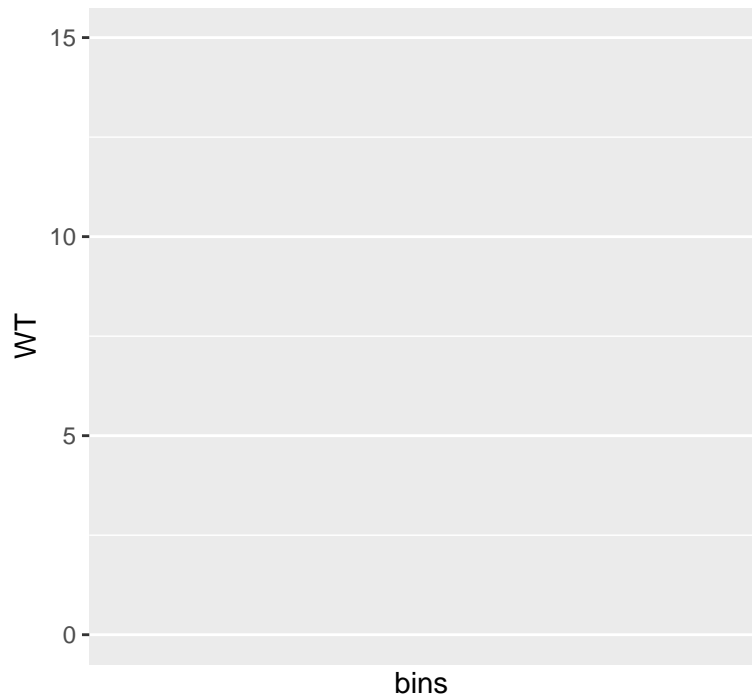
Y2x220-3.xml



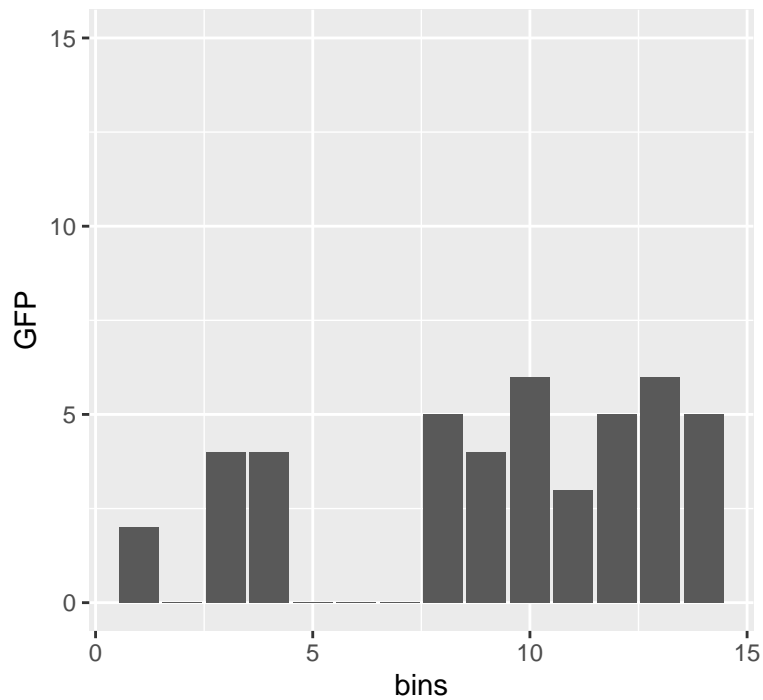
Y2x220-3.xml



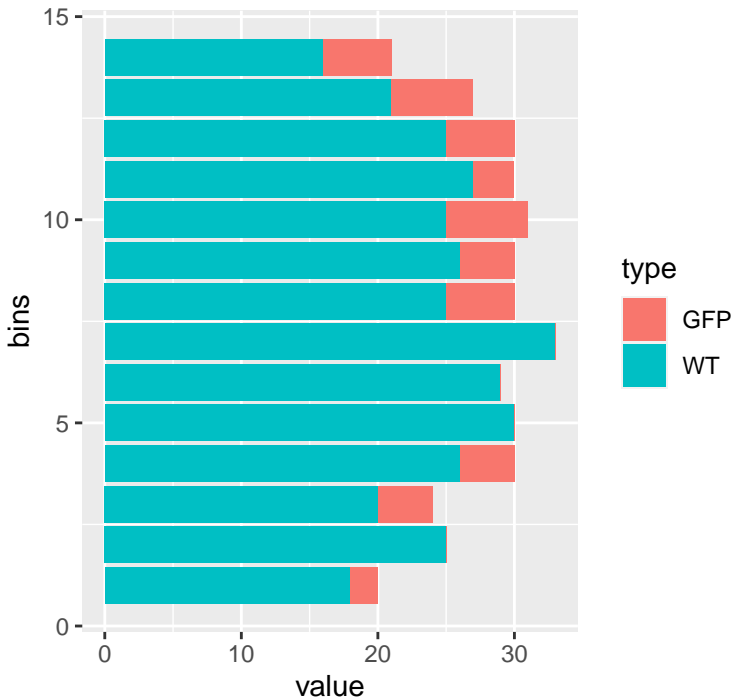
Y2x220-5.xml



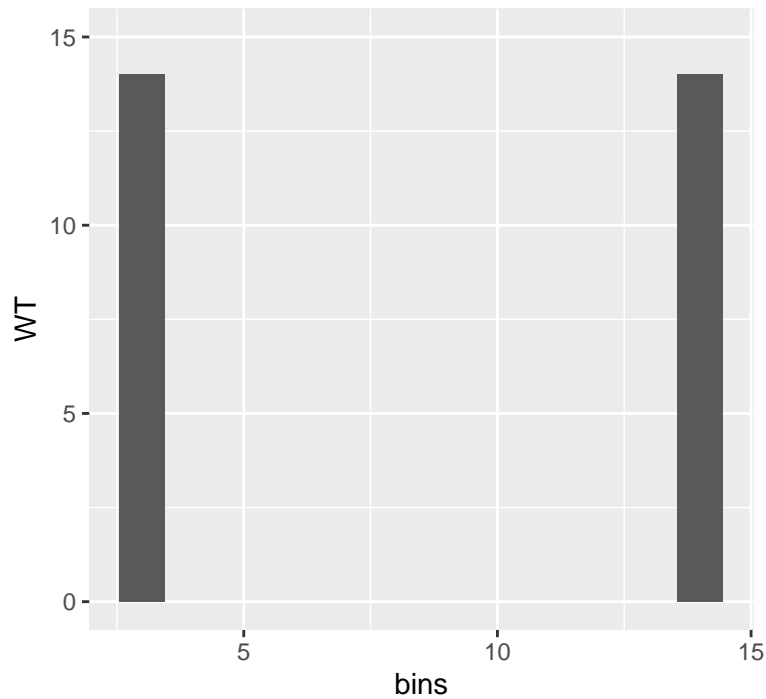
Y2x220-5.xml



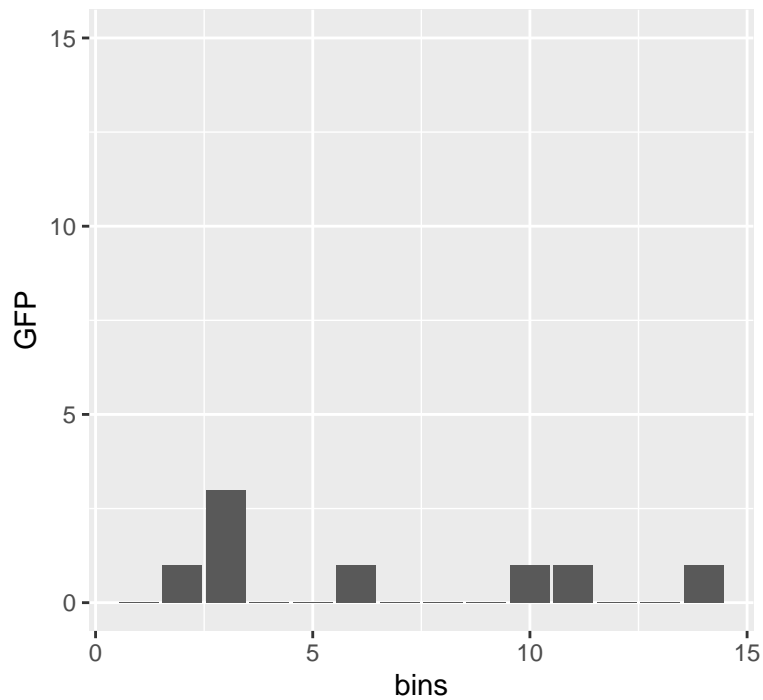
Y2x220-5.xml



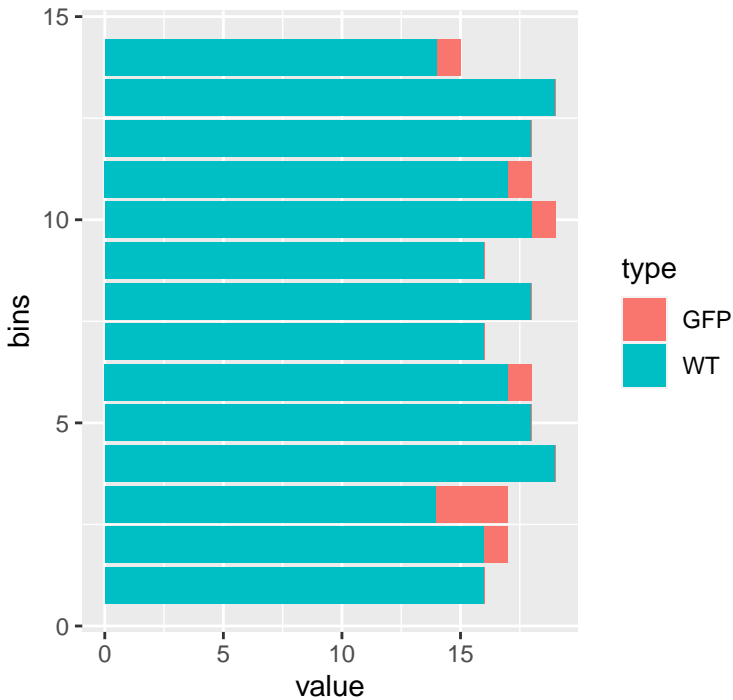
Y2x220-6.xml



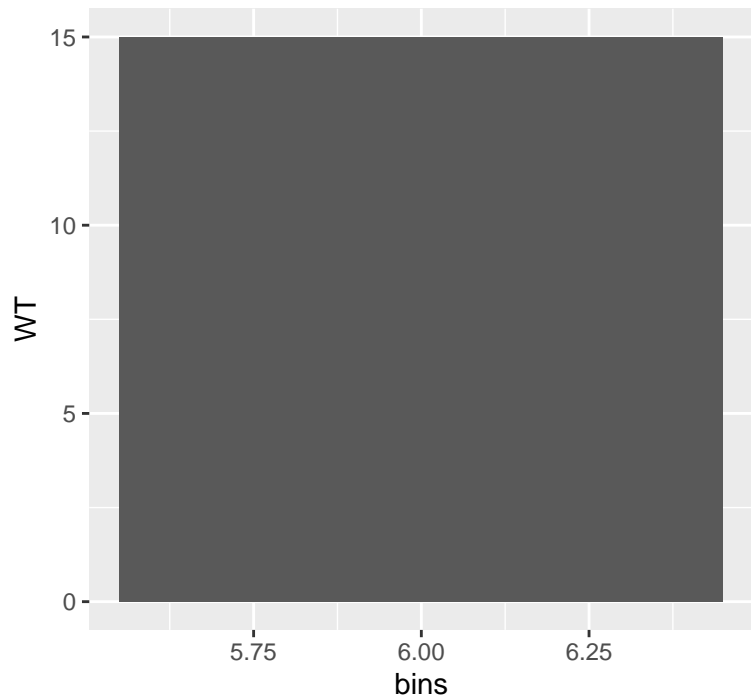
Y2x220-6.xml



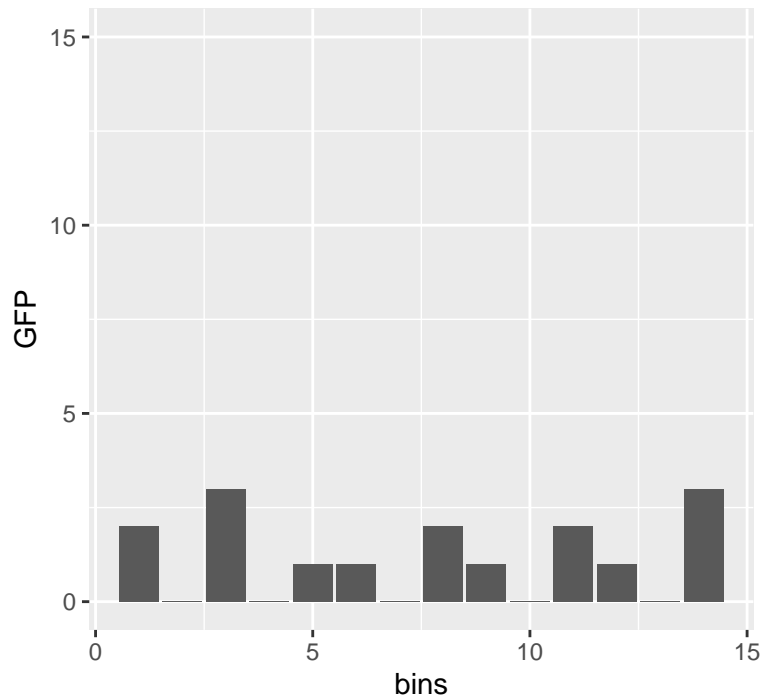
Y2x220-6.xml



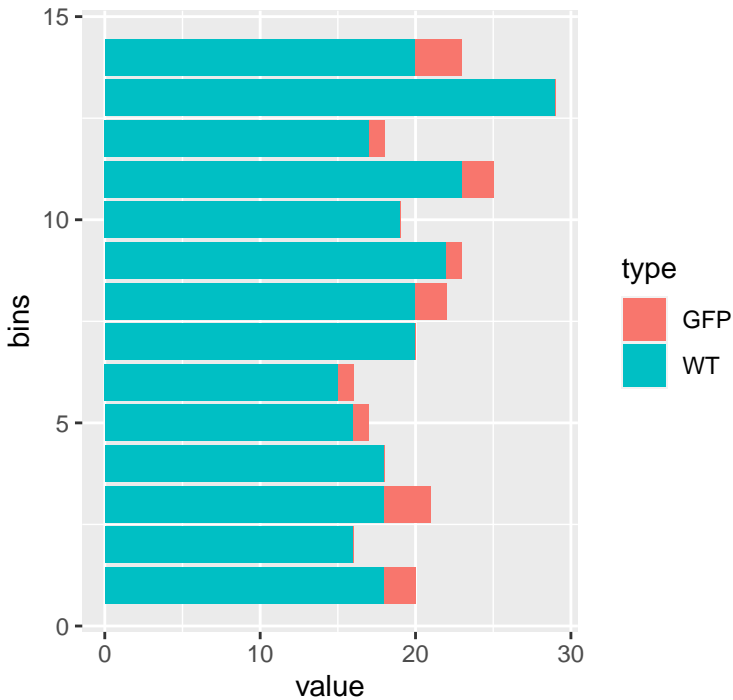
Z1x230-6.xml



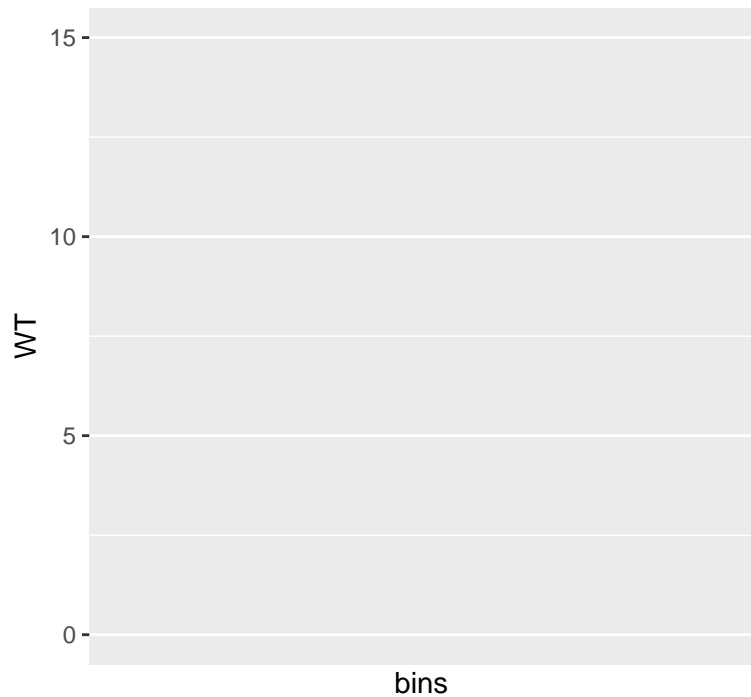
Z1x230-6.xml



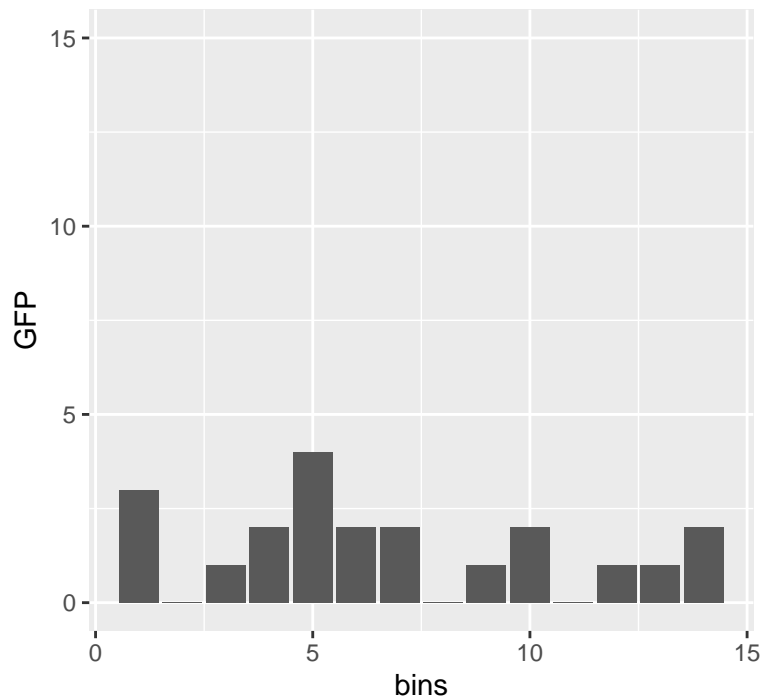
Z1x230-6.xml



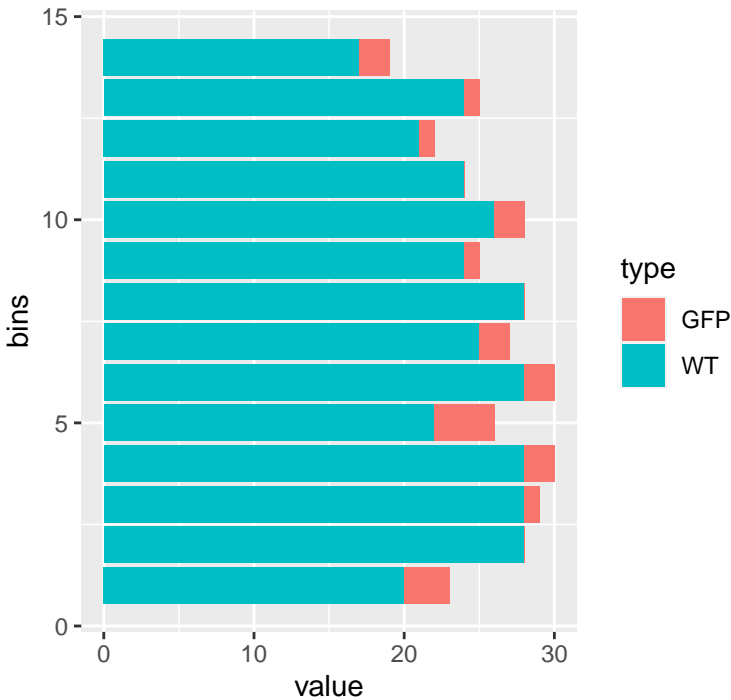
Z1x230-7_inference.xml



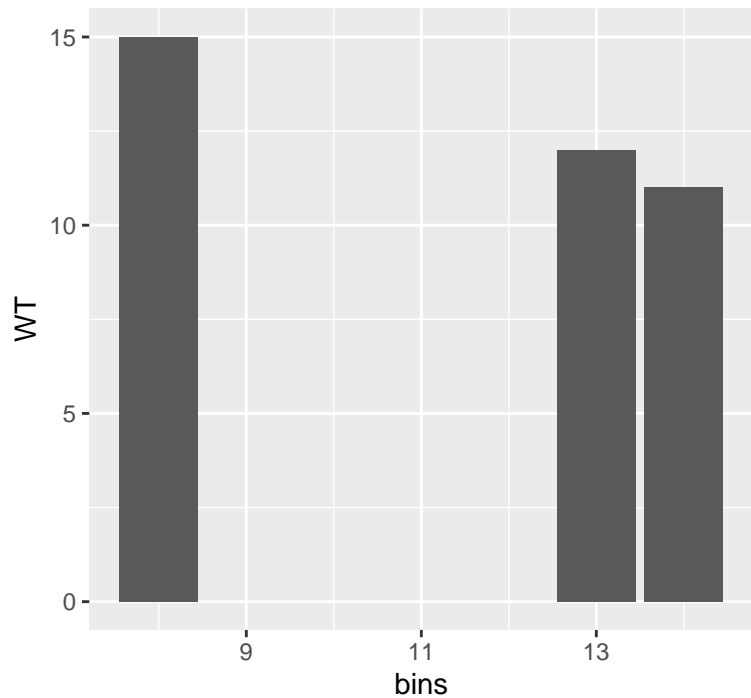
Z1x230-7_inference.xml



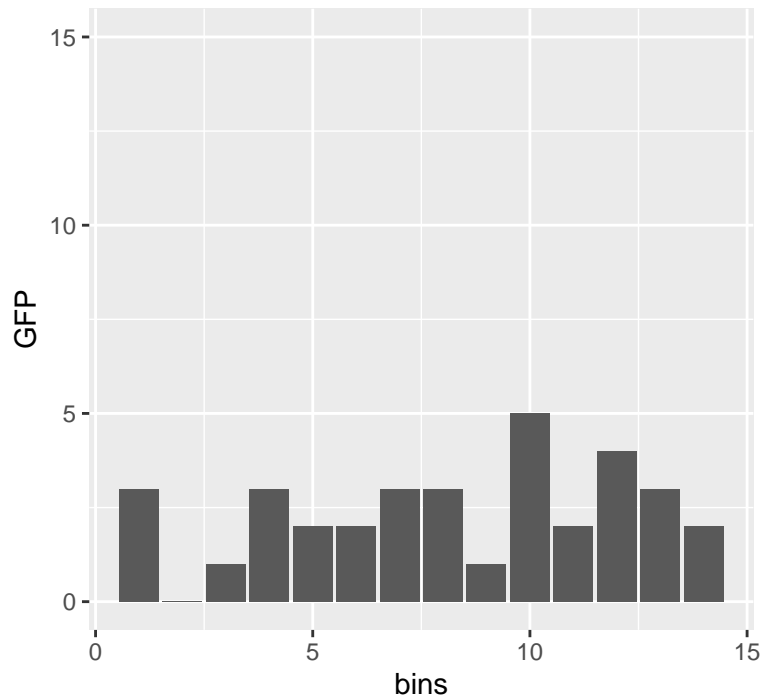
Z1x230-7_inference.xml



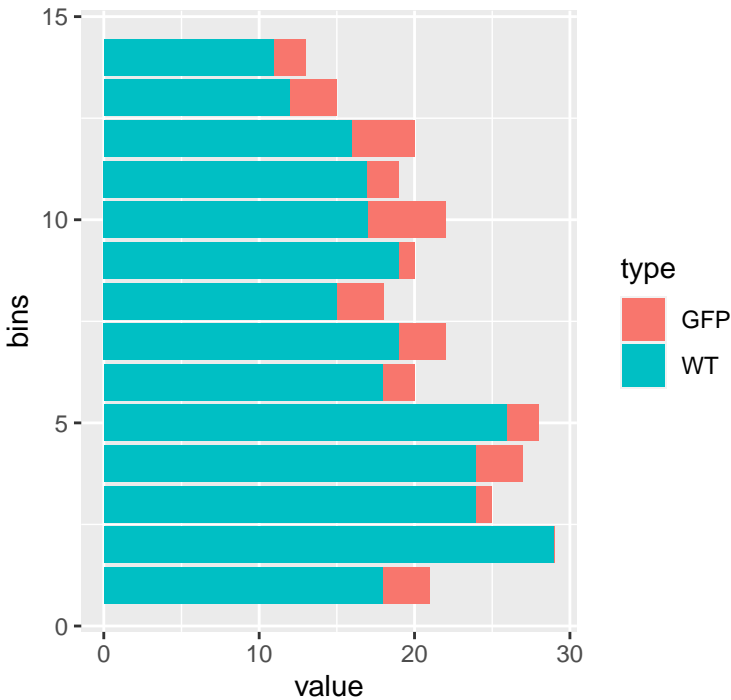
Z1x231-4_inference.xml



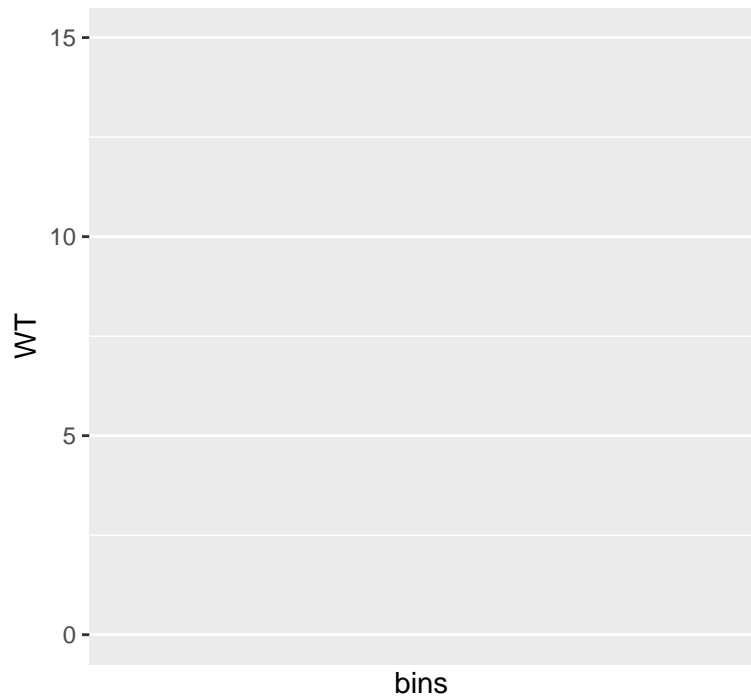
Z1x231-4_inference.xml



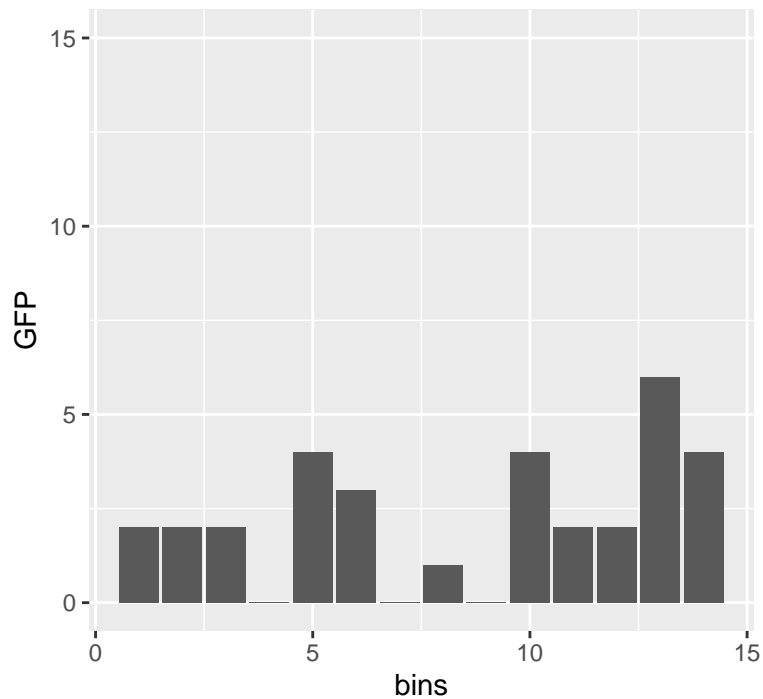
Z1x231-4_inference.xml



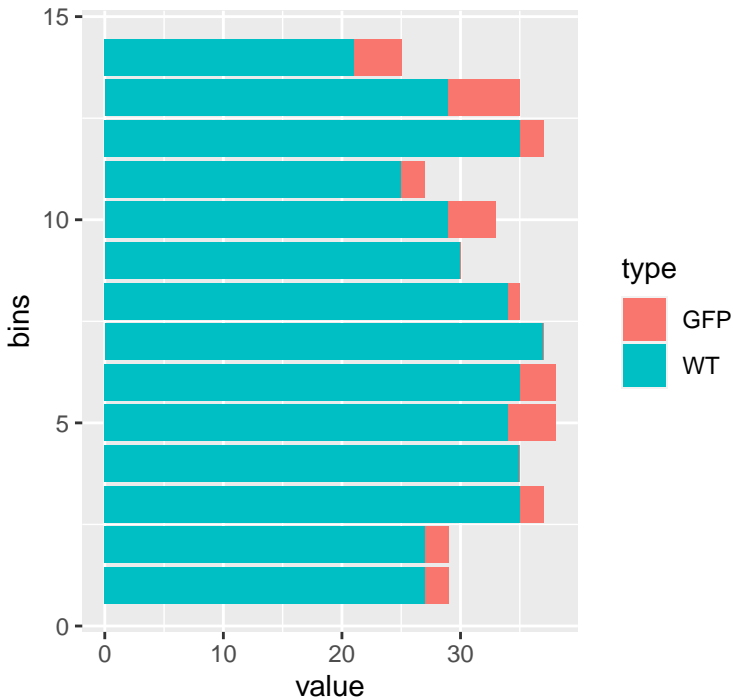
Z1x240A-2_inference.xml



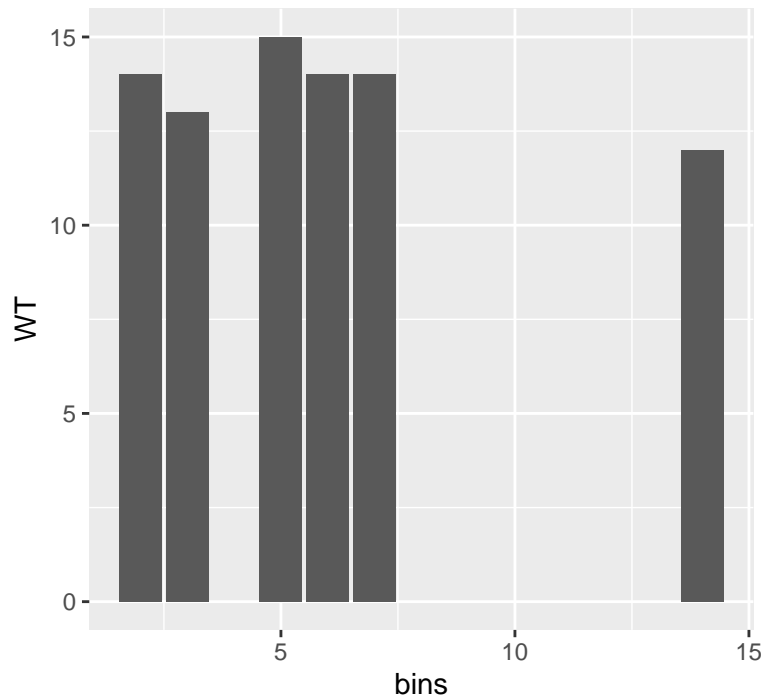
Z1x240A-2_inference.xml



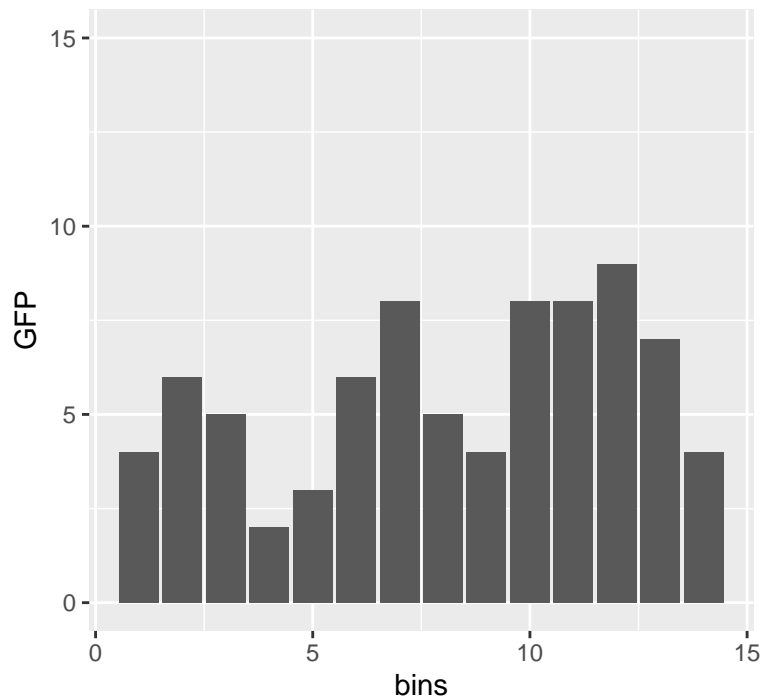
Z1x240A-2_inference.xml



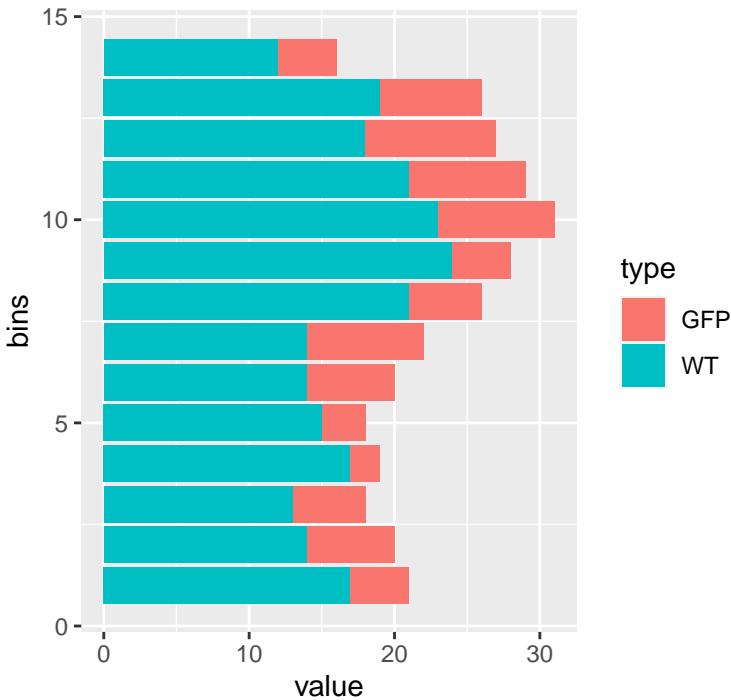
Z1x240A-6_inference.xml



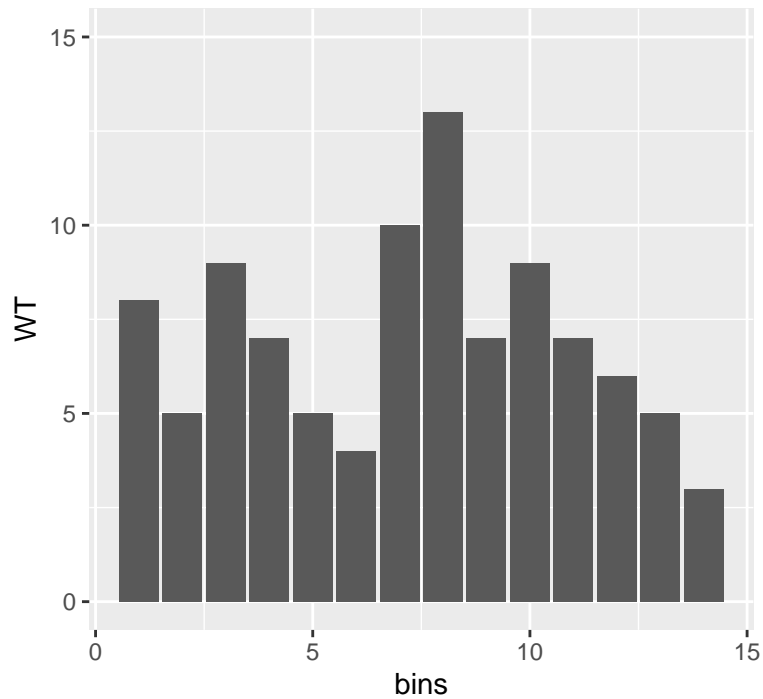
Z1x240A-6_inference.xml



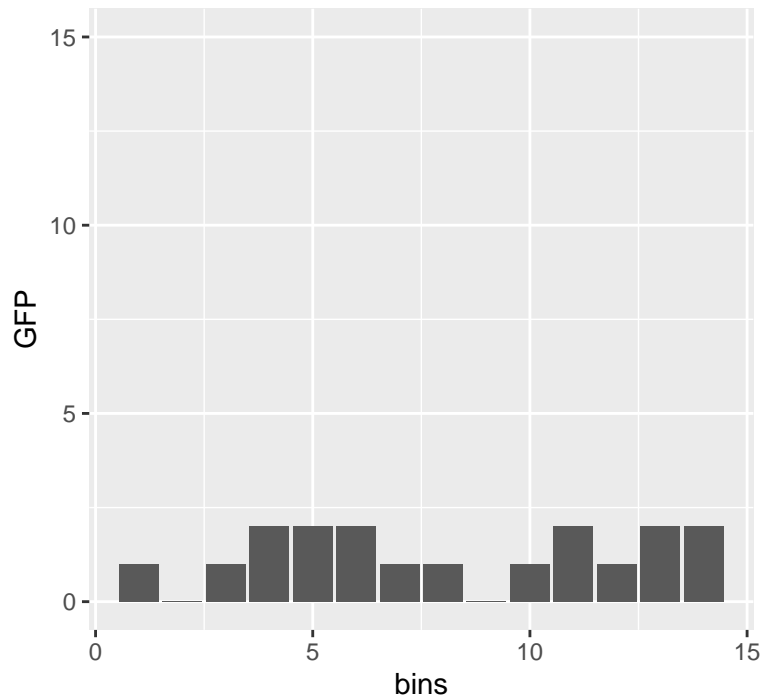
Z1x240A-6_inference.xml



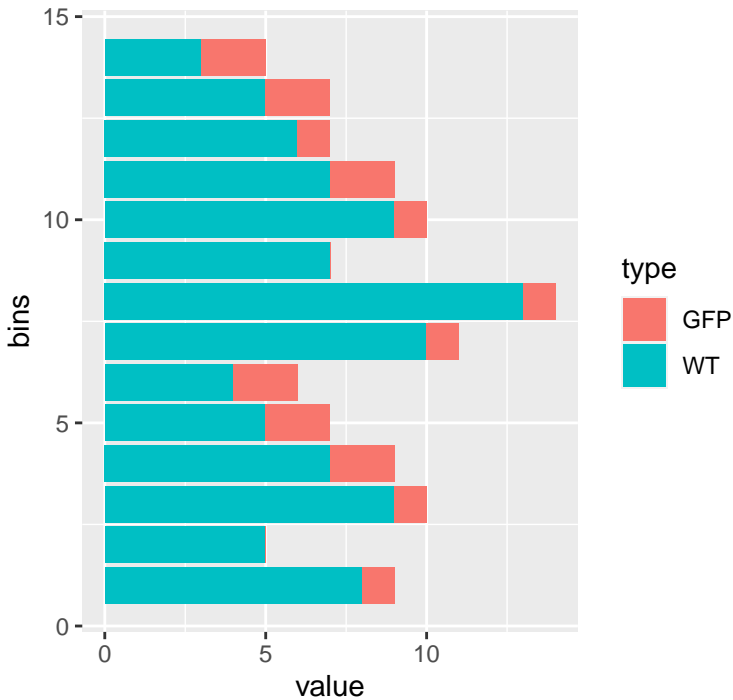
Z1x240A-7.xml



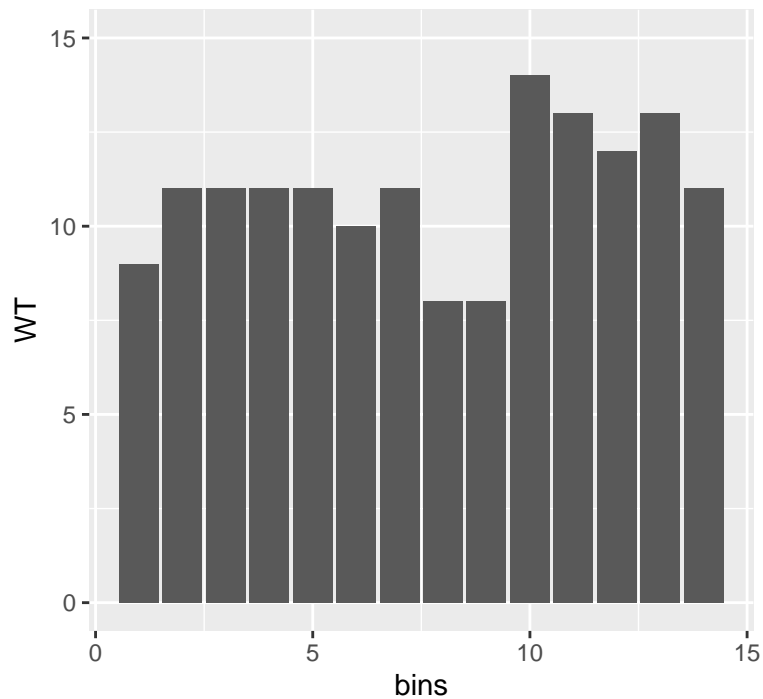
Z1x240A-7.xml



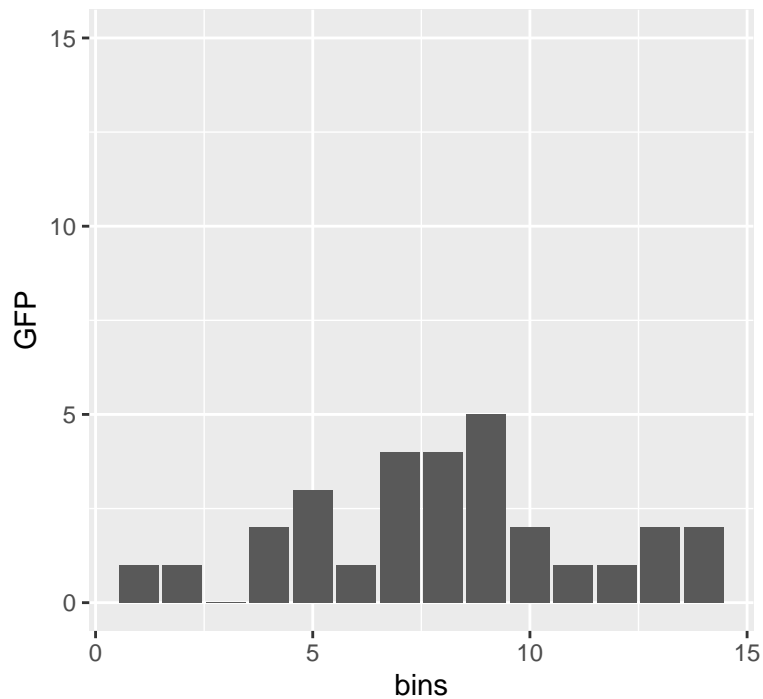
Z1x240A-7.xml



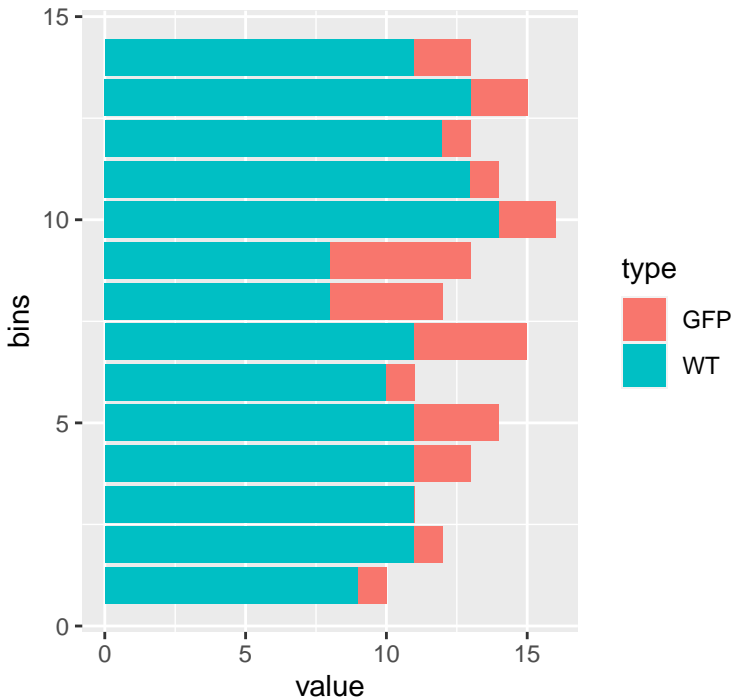
Z1x241B-3_inference.xml



Z1x241B-3_inference.xml



Z1x241B-3_inference.xml



Z2x82-1_NEW.xml

WT

bins

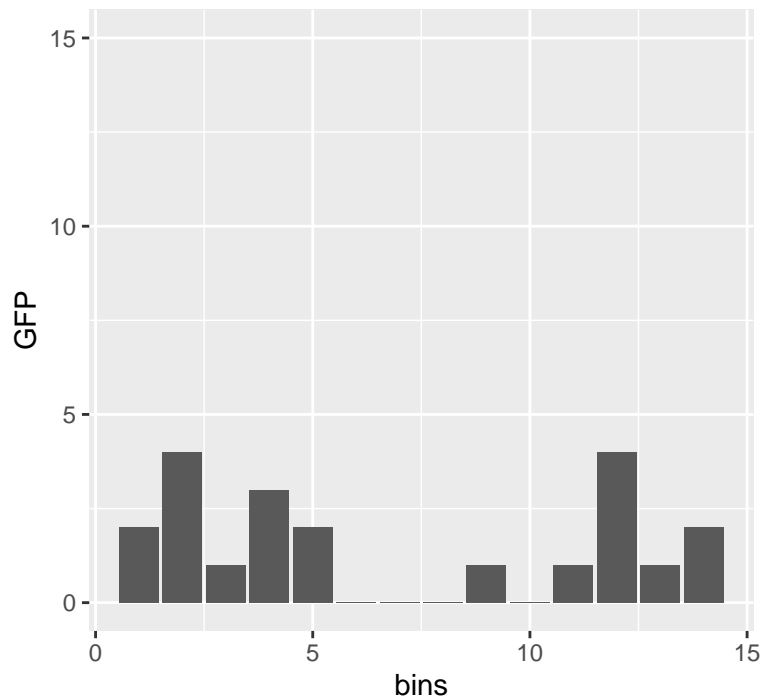
15

10

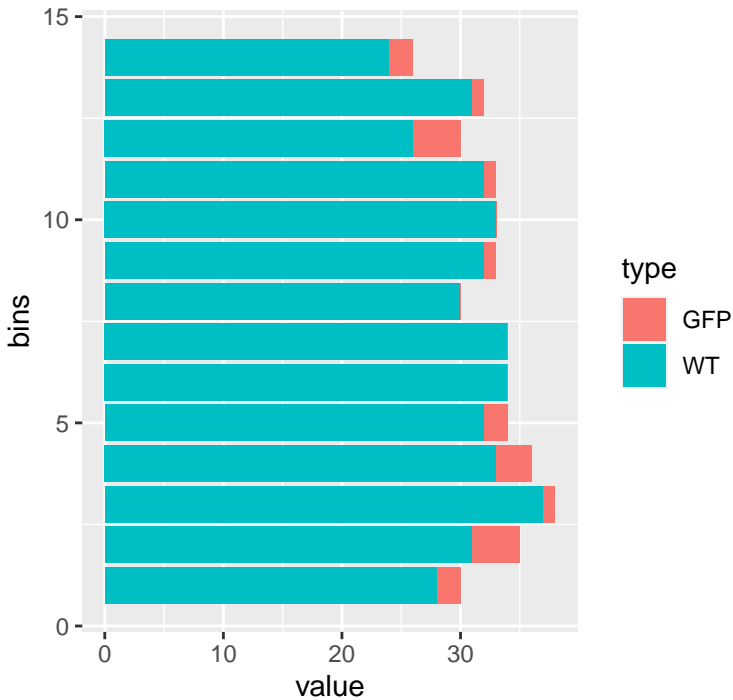
5

0

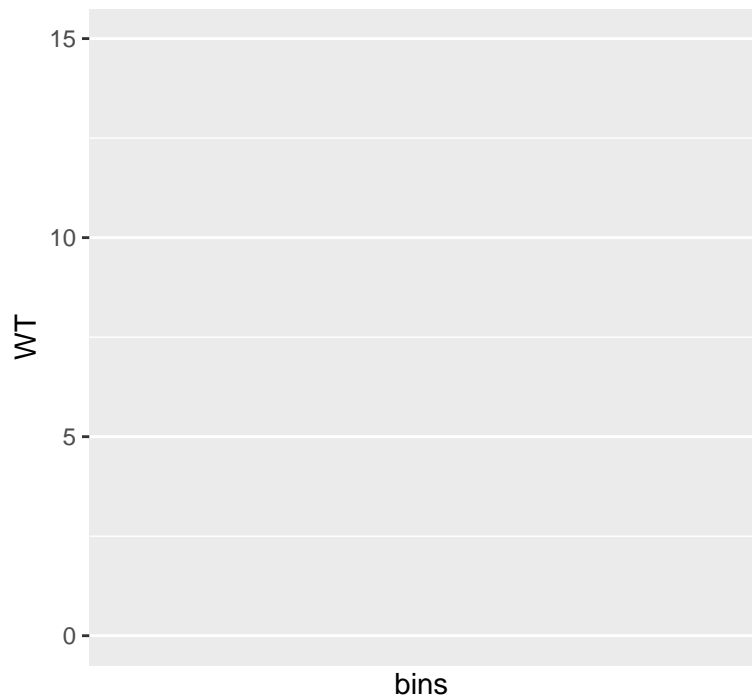
Z2x82-1_NEW.xml



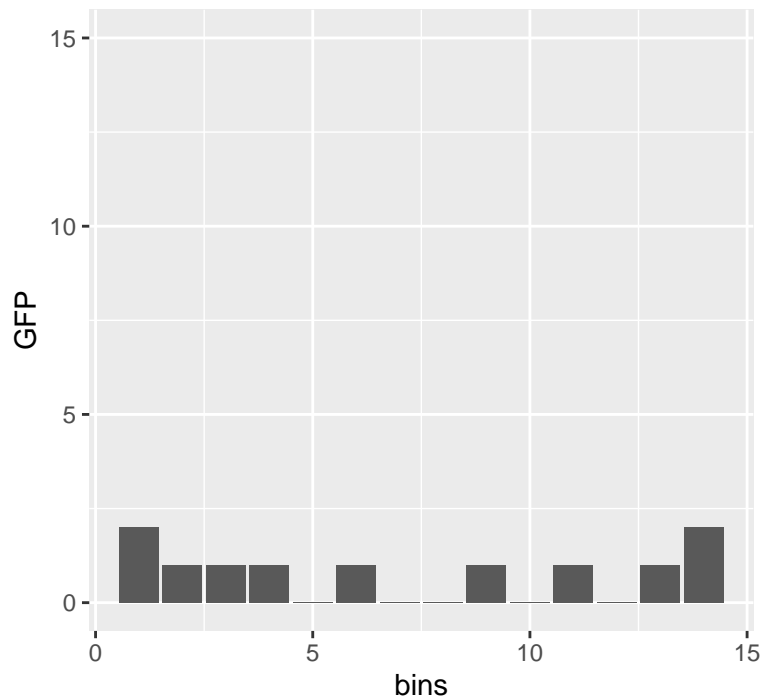
Z2x82-1_NEW.xml



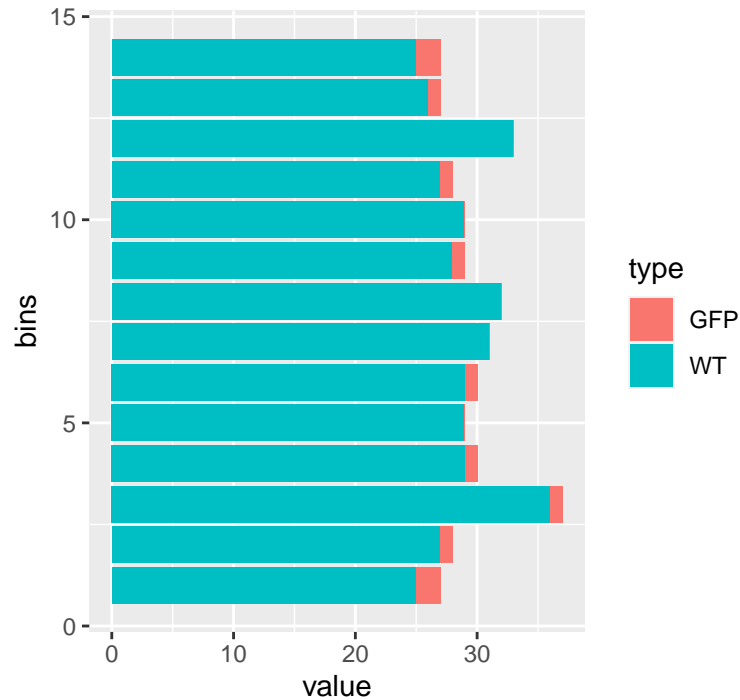
Z2x82-2.xml



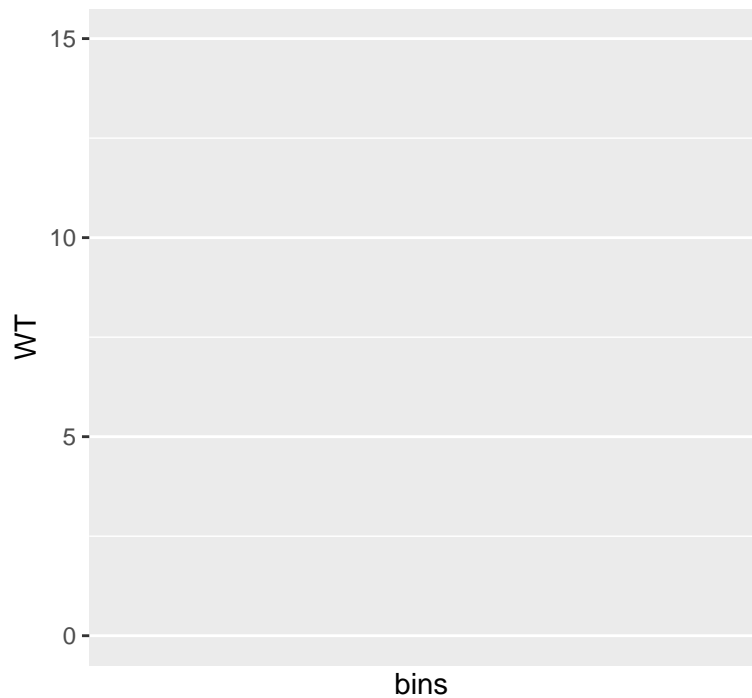
Z2x82-2.xml



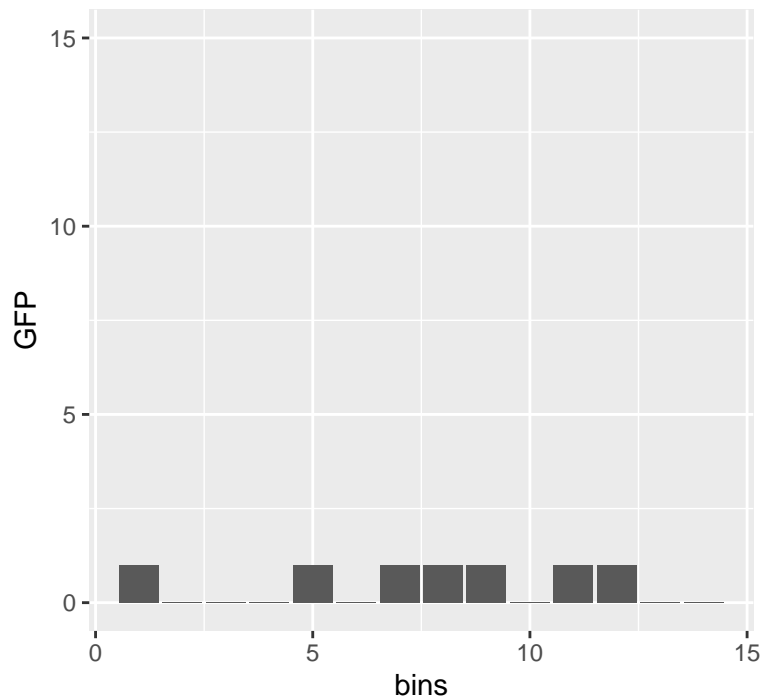
Z2x82-2.xml



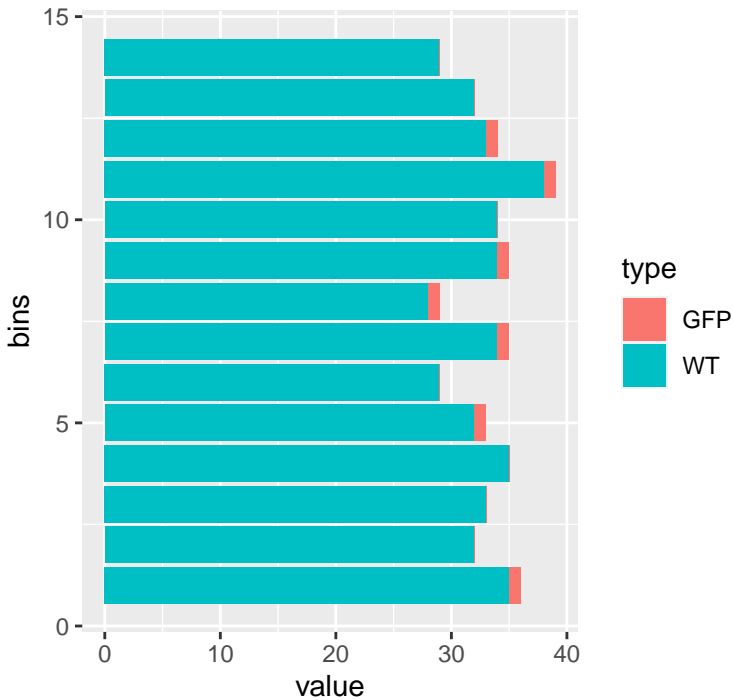
Z2x82-5.xml



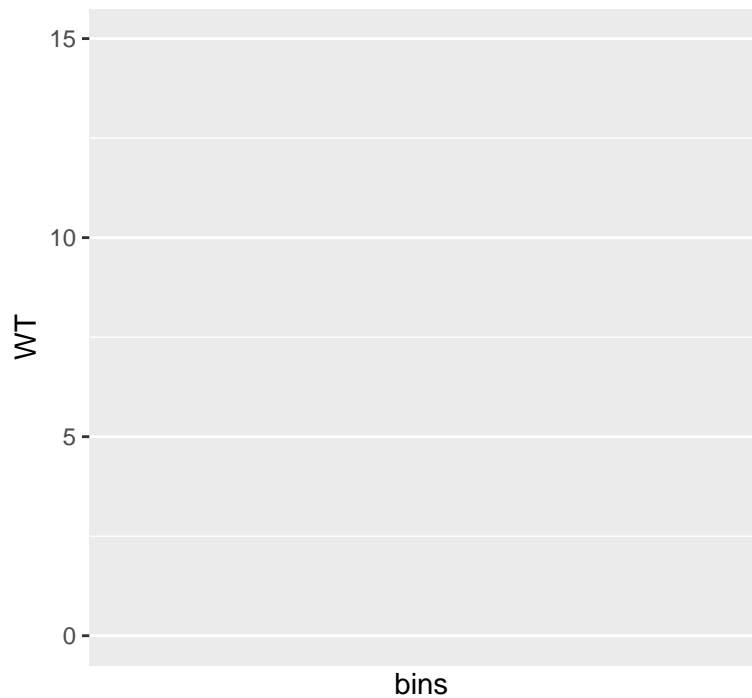
Z2x82-5.xml



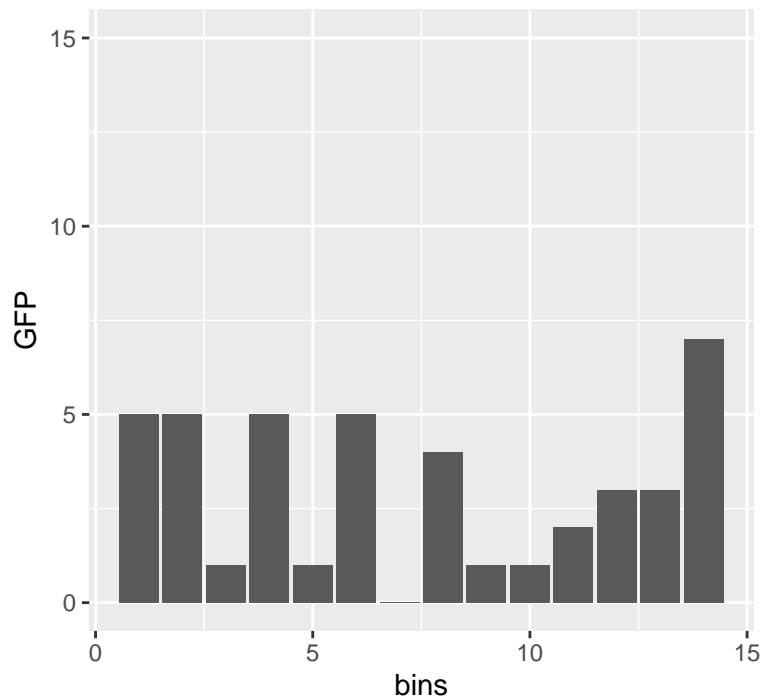
Z2x82-5.xml



Z2x82-6.xml



Z2x82-6.xml



Z2x82-6.xml

