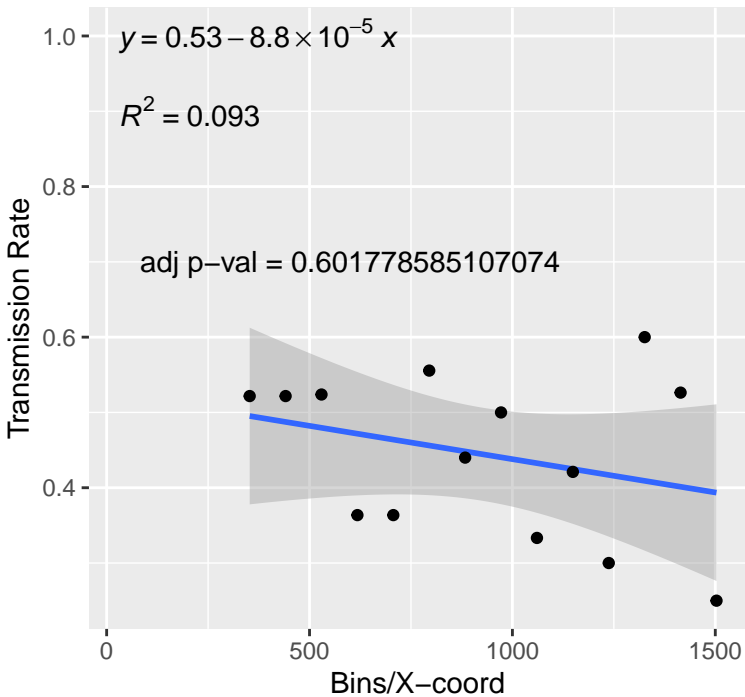
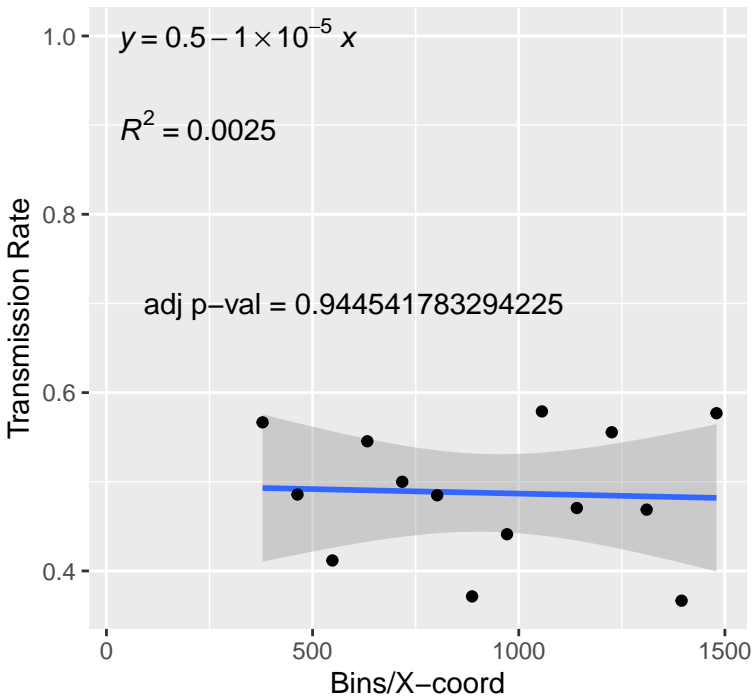


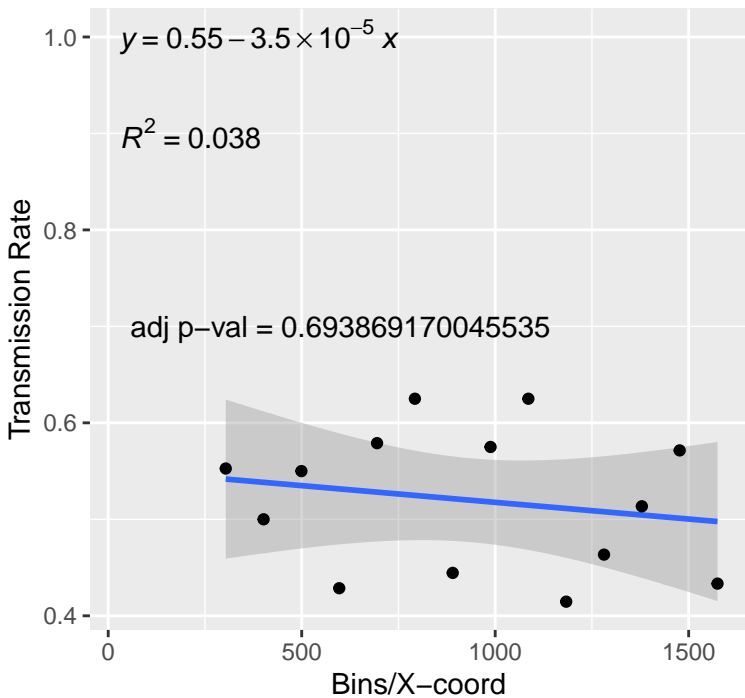
# X401x423-1m1\_inference.xml



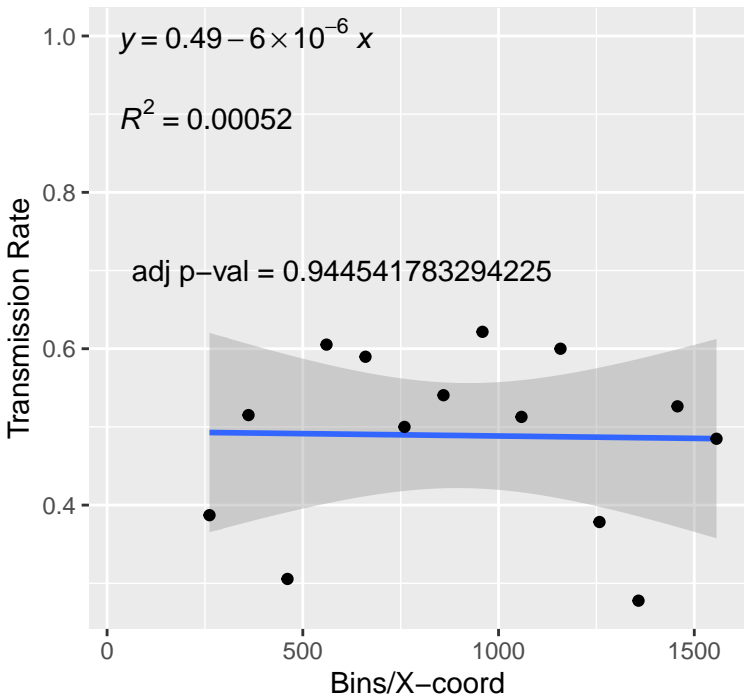
# X402x423-2m2\_inference.xml



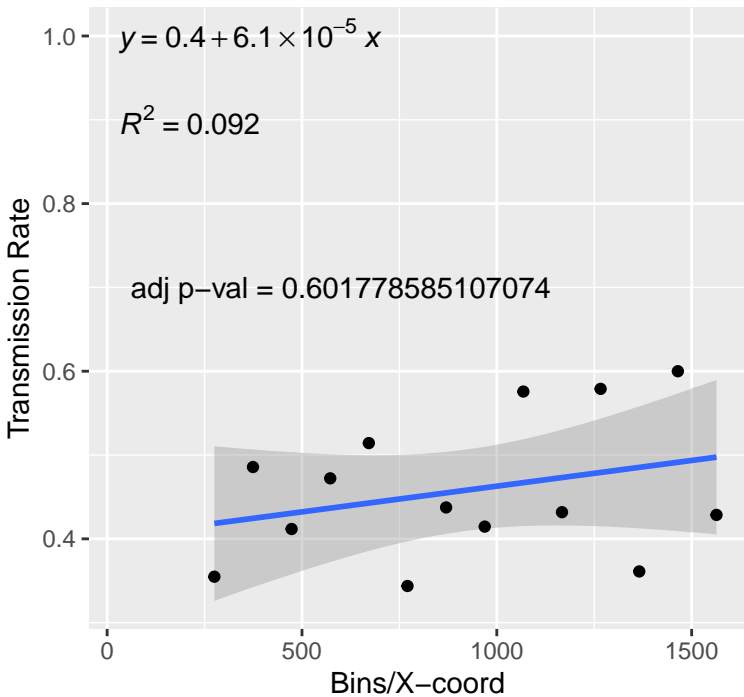
# X402x423-2m3\_inference.xml



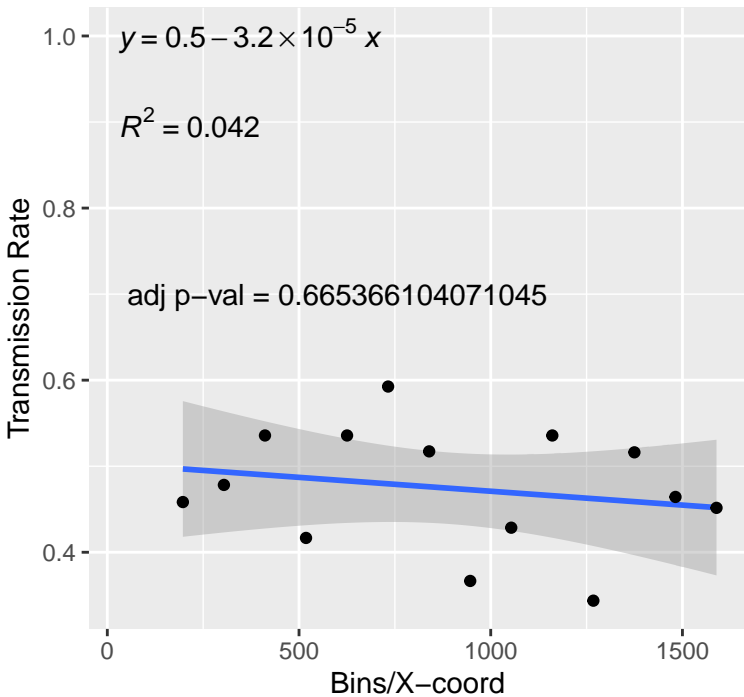
# Y2x86-1\_inference.xml



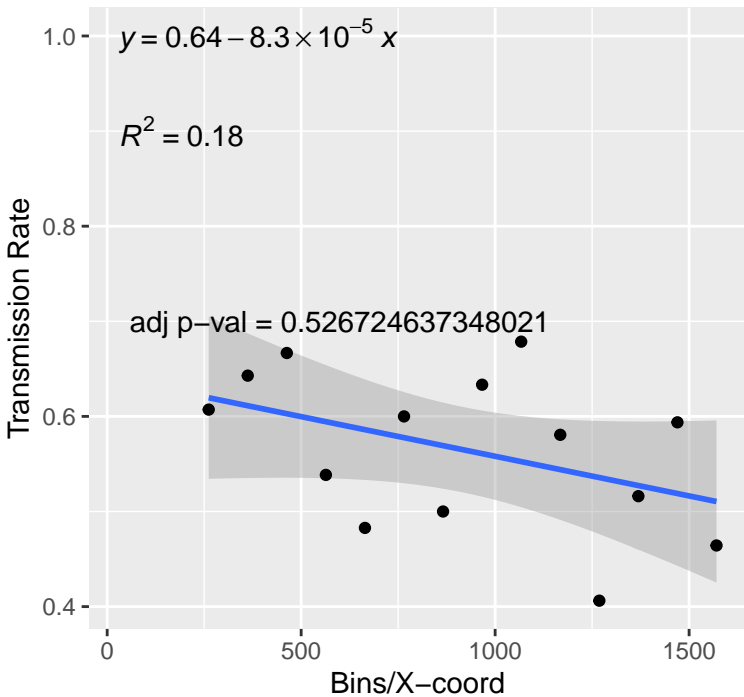
# Y2x86-2.xml



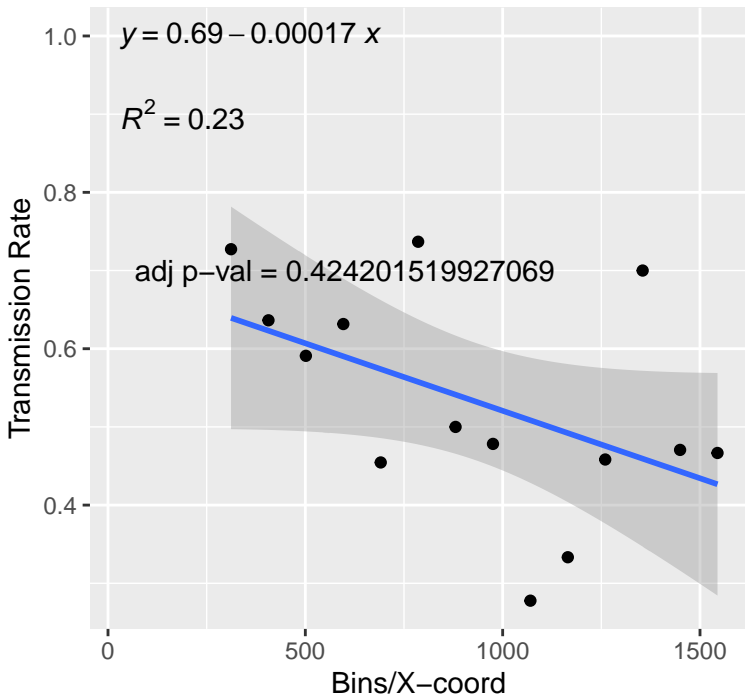
# Y2x86-3.xml



# Y2x86-4.xml

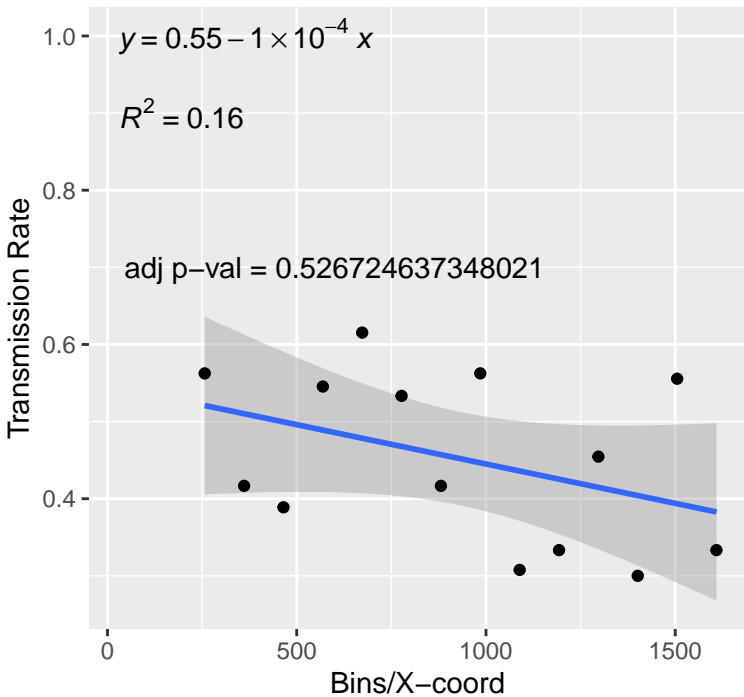


# Y2x86-5.xml

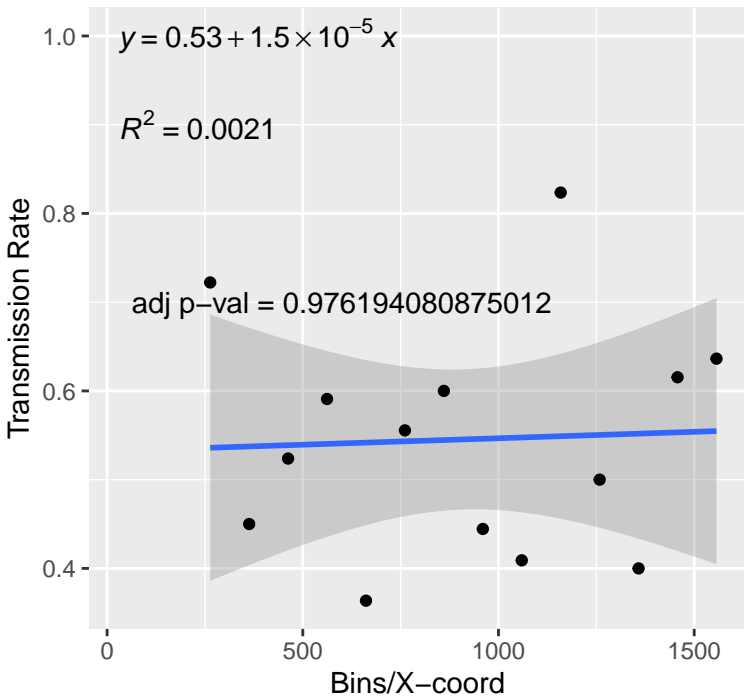




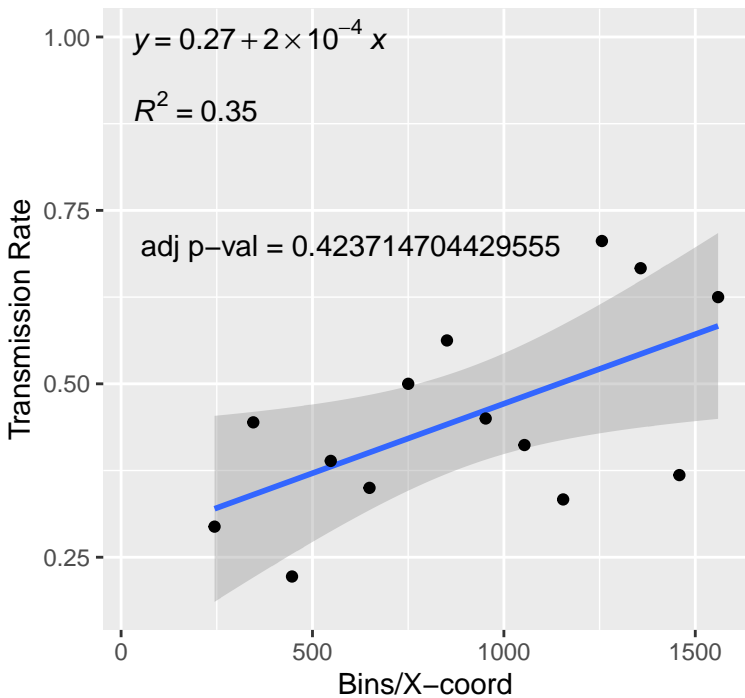
## Y2x86G-1.xml



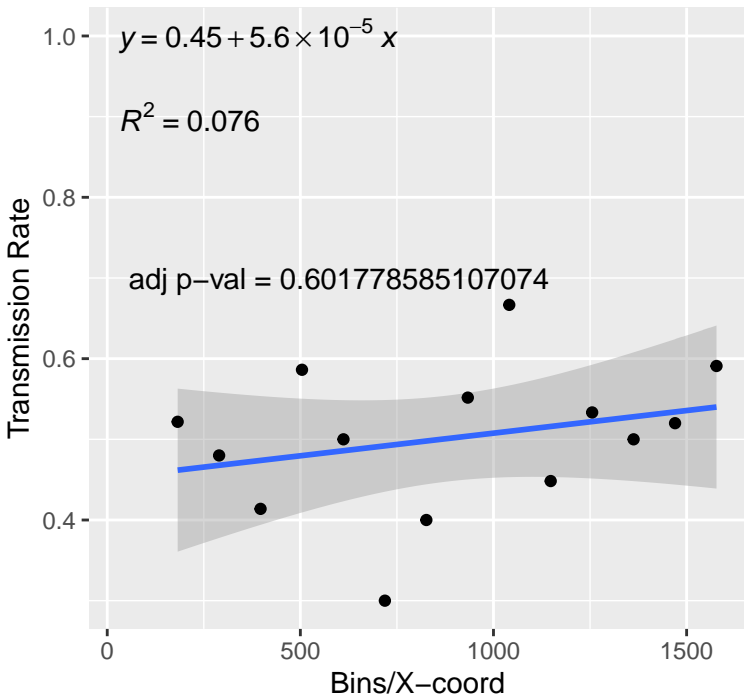
# Y2x86G-2.xml



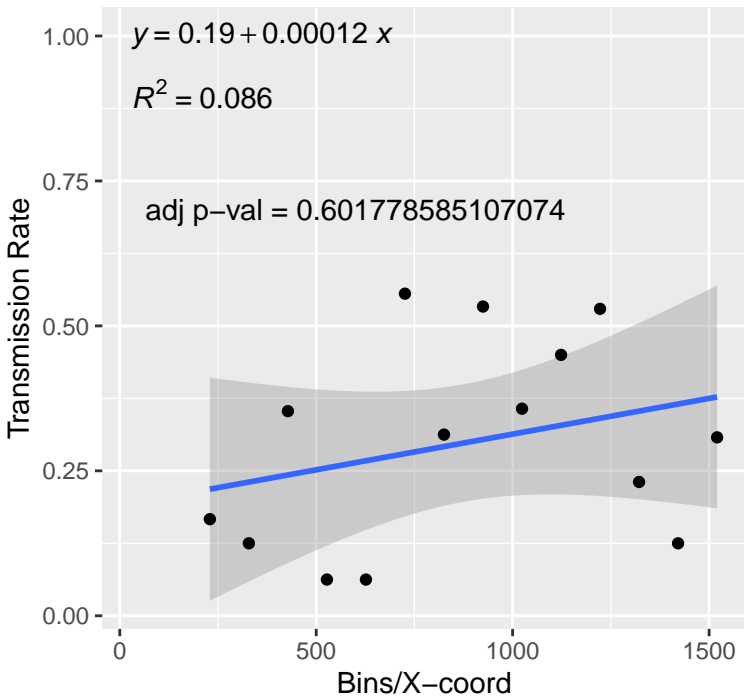
# Z102x16-1\_inference.xml



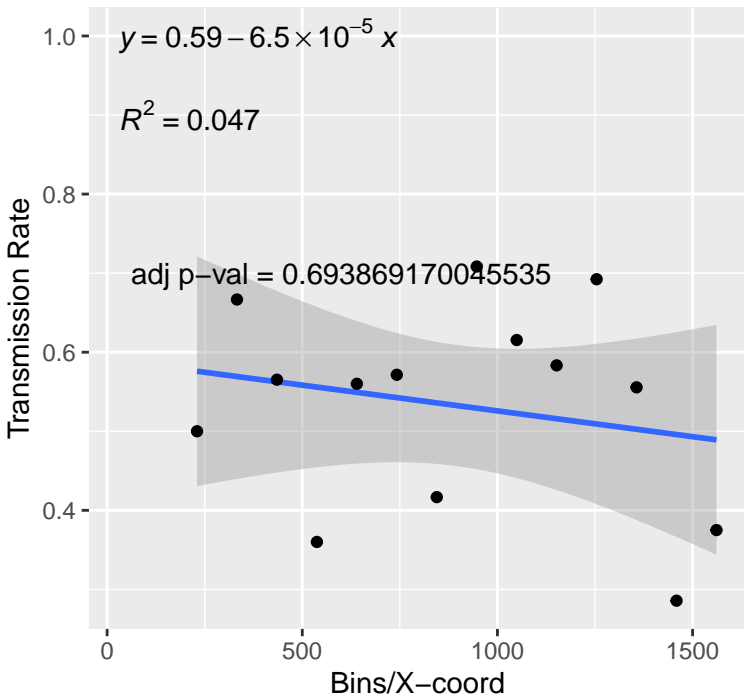
# Z102x16-2\_inference.xml



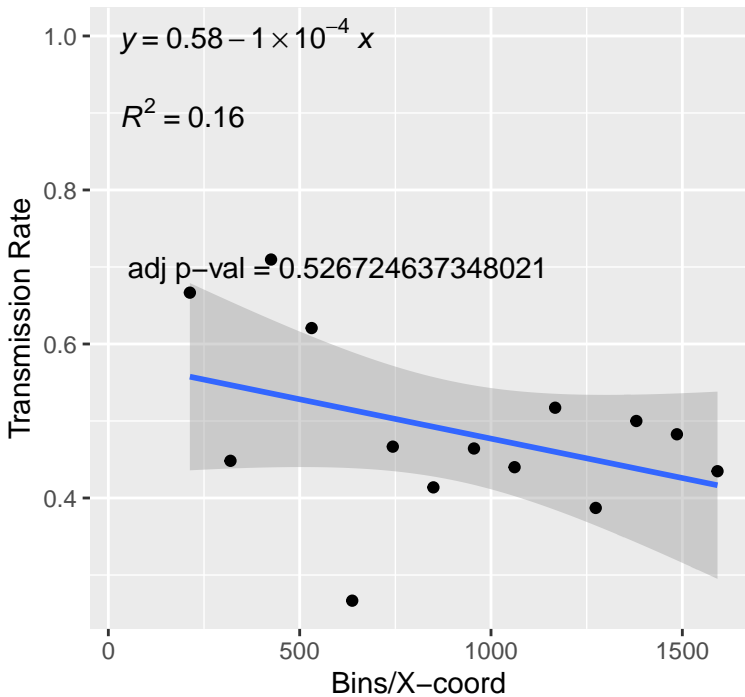
# Z102x16-3\_inference.xml



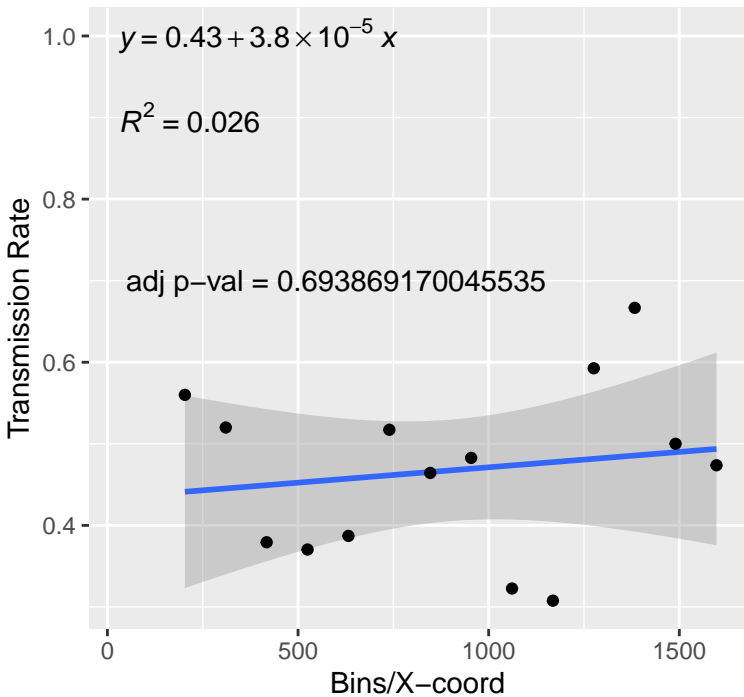
# Z102x16-4\_inference.xml



# Z102x16-5\_inference.xml



# Z102x16-7\_inference.xml





# Z102x16-8\_inference.xml

