Michele Chiari
Curriculum Vitae

✎ | Politecnico di Milano, Edificio 22 – Via Golgi 42, 20133 Milano, Italy
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Education (Degree Granting)

nov 2018 – present	**Information Technology** Ph.D. Politecnico di Milano – Milano, Italy
2016 – 2018	**Computer Science and Engineering** Master of Science (Laurea Magistrale) 110/110 cum laude Politecnico di Milano – Milano, Italy
2013 – 2016	**Computer Science** Bachelor of Science (Laurea Triennale) 110/110 cum laude University of Parma – Parma, Italy

Education (Other) and Achievements

2019	**Award for the Best Italian Master Thesis in Theoretical Computer Science** Italian Chapter of the EATCS Title: *Temporal Logic and Model Checking for Operator Precedence Words*
January 2016 – May 2016	**Study Abroad Program (undergraduate, with scholarship)** Boston College – Boston, MA, USA Computer Science, Mathematics and Statistics courses

Teaching/Work Experience

03/2018 – present	**Tutoring Sessions (30 hours)** Politecnico di Milano, Italy Classroom exercise and tutoring for the course "Algoritmi e Principi dell'Informatica" (Formal Languages, Automata and Computability Theory; Algorithms and Data Structures)
05/2018	**Tutoring Activity (10 hours)** Politecnico di Milano, Italy Development and preparation of the final project (Prova Finale) for the course "Algoritmi e Principi dell'Informatica".
26/02/2018 – 04/06/2018	**Tutoring Sessions (24 hours)** University of Parma, Italy Classroom exercise for the course "Fondamenti dell'Informatica" (Principles of Programming Languages, Automata and Computability Theory)

Language skills

Italian	Native speaker
English	Reading: excellent; Writing: excellent; Speaking: good. TOEFL iBT score: 110 (Oct 2014).

PUBLICATIONS

[1] S. Cherubin, D. Cattaneo, M. Chiari, A. Di Bello, and G. Agosta. TAFFO: Tuning Assistant for Floating to Fixed point Optimization. *IEEE Embedded Systems Letters*, 2019. To appear, `https://doi.org/10.1109/LES.2019.2913774`.

[2] D. Cattaneo, A. Di Bello, M. Chiari, S. Cherubin, and G. Agosta. Fixed point exploitation via compiler analyses and transformations: Poster. In *Proceedings of the 16th ACM International Conference on Computing Frontiers*, CF '19, pages 292–294, New York, NY, USA, 2019. ACM.

[3] M. Chiari, D. Mandrioli, and M. Pradella. Temporal logic and model checking for operator precedence languages. In Andrea Orlandini and Martin Zimmermann, editors, Proceedings Ninth International Symposium on *Games, Automata, Logics, and Formal Verification,* Saarbrücken, Germany, 26-28th September 2018, volume 277 of *Electronic Proceedings in Theoretical Computer Science*, pages 161–175. Open Publishing Association, 2018.

[4] R. Bagnara, M. Chiari, R. Gori, and A. Bagnara. A practical approach to interval refinement for `math.h/cmath` functions. Report `arXiv:1610.07390v1 [cs.PL]`, 2016.