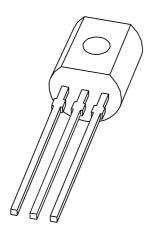
DISCRETE SEMICONDUCTORS

DATA SHEET



BC517NPN Darlington transistor

Product specification Supersedes data of 1997 Apr 23 1999 Apr 23





NPN Darlington transistor

BC517

FEATURES

- High current (max. 500 mA)
- Low voltage (max. 30 V)
- Very high DC current gain (min. 30000).

APPLICATIONS

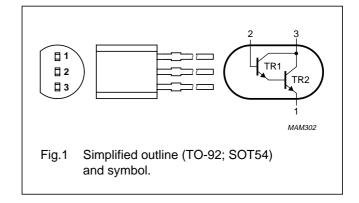
· Where very high amplification is required.

DESCRIPTION

NPN Darlington transistor in a TO-92; SOT54 plastic package. PNP complement: BC516.

PINNING

PIN	DESCRIPTION
1	emitter
2	base
3	collector



LIMITING VALUES

In accordance with the Absolute Maximum Rating System (IEC 134).

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
V _{CBO}	collector-base voltage	open emitter	_	40	V
V _{CES}	collector-emitter voltage	V _{BE} = 0	_	30	V
V _{EBO}	emitter-base voltage	open collector	_	10	V
I _C	collector current (DC)		_	500	mA
I _{CM}	peak collector current		_	800	mA
I _B	base current (DC)		_	100	mA
P _{tot}	total power dissipation	T _{amb} ≤ 25 °C; note 1	_	500	mW
T _{stg}	storage temperature		-65	+150	°C
Tj	junction temperature		_	150	°C
T _{amb}	operating ambient temperature		-65	+150	°C

Note

1. Transistor mounted on an FR4 printed-circuit board.

Philips Semiconductors Product specification

NPN Darlington transistor

BC517

THERMAL CHARACTERISTICS

SYMBOL	PARAMETER	CONDITIONS	VALUE	UNIT
R _{th j-a}	thermal resistance from junction to ambient	note 1	250	K/W

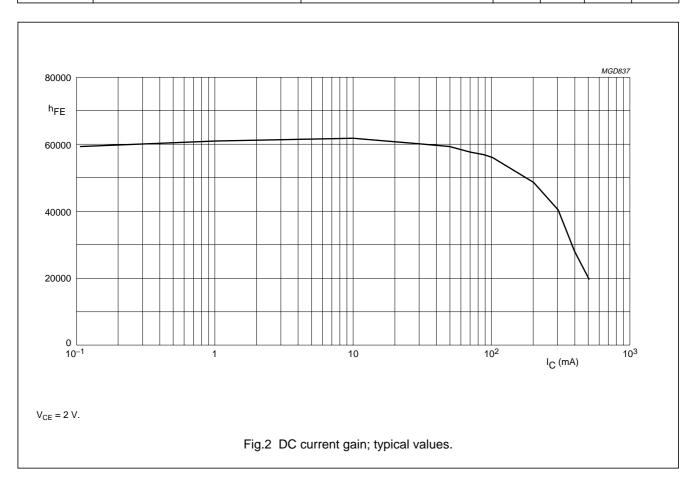
Note

1. Transistor mounted on an FR4 printed-circuit board.

CHARACTERISTICS

 $T_j = 25$ °C unless otherwise specified.

SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
I _{CBO}	collector cut-off current	I _E = 0; V _{CB} = 30 V	_	_	100	nA
I _{EBO}	emitter cut-off current	I _C = 0; V _{EB} = 10 V	_	_	100	nA
h _{FE}	DC current gain	I _C = 20 mA; V _{CE} = 2 V; see Fig.2	30000	_	_	
V _{CEsat}	collector-emitter saturation voltage	I _C = 100 mA; I _B = 0.1 mA	_	_	1	V
V _{BEsat}	base-emitter saturation voltage	I _C = 100 mA; I _B = 0.1 mA	_	_	1.5	V
V_{BEon}	base-emitter on-state voltage	I _C = 10 mA; V _{CE} = 5 V	_	_	1.4	V
f _T	transition frequency	I _C = 30 mA; V _{CE} = 5 V; f = 100 MHz	_	220	_	MHz



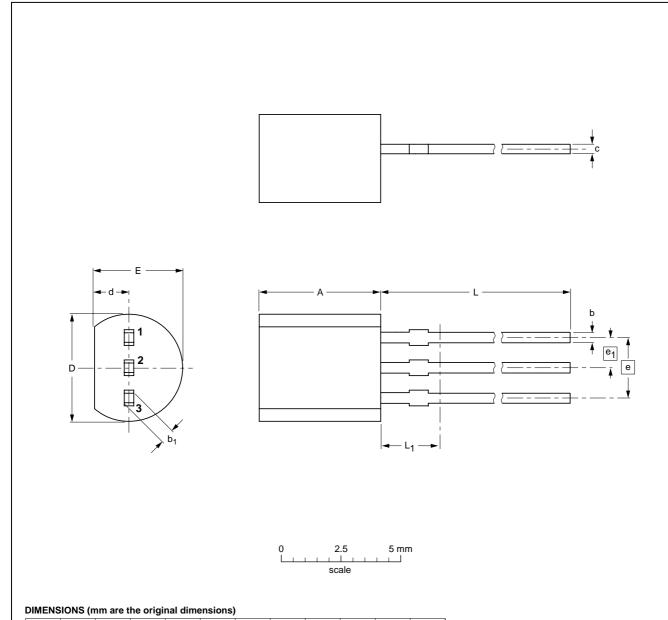
NPN Darlington transistor

BC517

PACKAGE OUTLINE

Plastic single-ended leaded (through hole) package; 3 leads

SOT54



UNIT	A	b	b ₁	С	D	d	E	е	e ₁	L	L ₁ ⁽¹⁾
mm	5.2 5.0	0.48 0.40	0.66 0.56	0.45 0.40	4.8 4.4	1.7 1.4	4.2 3.6	2.54	1.27	14.5 12.7	2.5

Note

1. Terminal dimensions within this zone are uncontrolled to allow for flow of plastic and terminal irregularities.

OUTLINE		REFERENCES				ISSUE DATE
VERSION	IEC	JEDEC	EIAJ		PROJECTION	ISSUE DATE
SOT54		TO-92	SC-43			97-02-28

Product specification Philips Semiconductors

NPN Darlington transistor

BC517

DEFINITIONS

Data Sheet Status	
Objective specification	This data sheet contains target or goal specifications for product development.
Preliminary specification	This data sheet contains preliminary data; supplementary data may be published later.
Product specification	This data sheet contains final product specifications.
Limiting values	

Limiting values given are in accordance with the Absolute Maximum Rating System (IEC 134). Stress above one or more of the limiting values may cause permanent damage to the device. These are stress ratings only and operation of the device at these or at any other conditions above those given in the Characteristics sections of the specification is not implied. Exposure to limiting values for extended periods may affect device reliability.

Application information

Where application information is given, it is advisory and does not form part of the specification.

LIFE SUPPORT APPLICATIONS

These products are not designed for use in life support appliances, devices, or systems where malfunction of these products can reasonably be expected to result in personal injury. Philips customers using or selling these products for use in such applications do so at their own risk and agree to fully indemnify Philips for any damages resulting from such improper use or sale.

1999 Apr 23 5 Philips Semiconductors Product specification

NPN Darlington transistor

BC517

NOTES

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NPN Darlington transistor

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