The Boolean difference indicates that the output (g9) changes if the signal g5 changes as well. But we should know that g9 depends not only on g5, but g8 and f, so the reason that g9 changes could be g8 or f.

Also, g9 is an AND gate, so g9|g5=1 would be the same as g9, but not for g9|g5=0. To see if g5 is redundant, we should assert that g5=0, i.e. diff & ~g5. If wire(g5 -> g9) stuck-at-1 is untestable, then wire(g5 -> g9) is proved to be redundant.

```
BddNode g9_1 = f & g8 & BddNode::_one;
BddNode g9_0 = f & g8 & BddNode::_zero;
BddNode diff = g9_1 ^ g9_0;

BddNode T1 = ~g5 & diff;
cout << T1 << endl;
```

The corresponding T1 is constant zero, so wire(g5 -> g9) is indeed redundant.