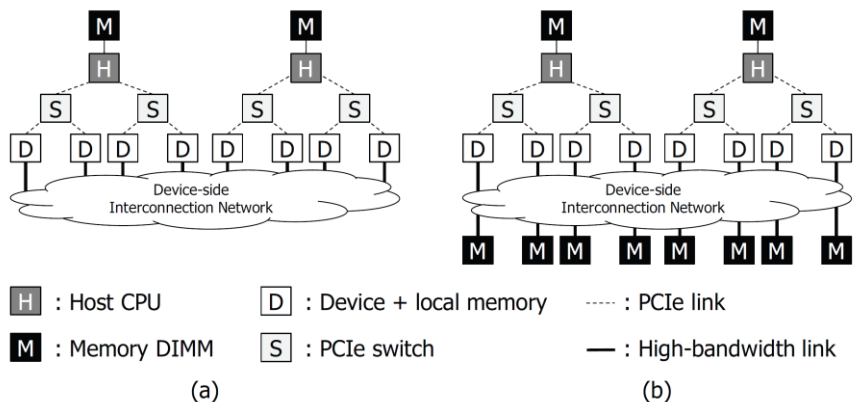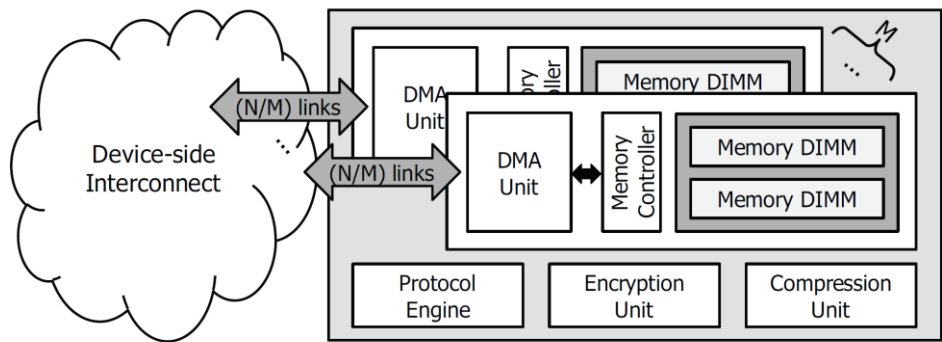Youngeun Kwon and Minsoo Rhu, "Beyond the Memory Wall: A Case for Memory-Centric HPC System for Deep Learning," in *Proceedings of the International Symposium on Microarchitecture (MICRO)*, 2018

Fig. 1: (a) A device-centric deep learning system architecture, and (b) a memory-centric deep learning system architecture.

Fig. 6: Memory-node architecture.