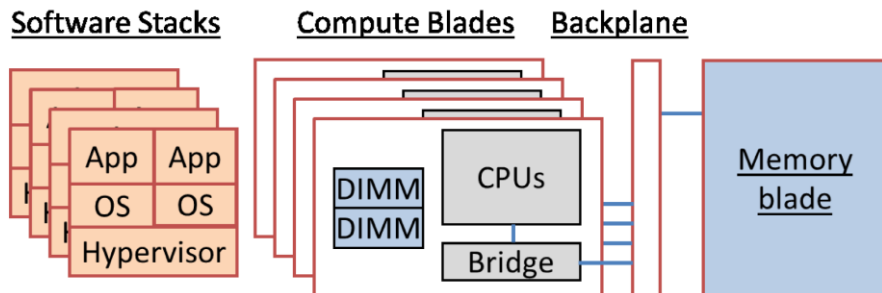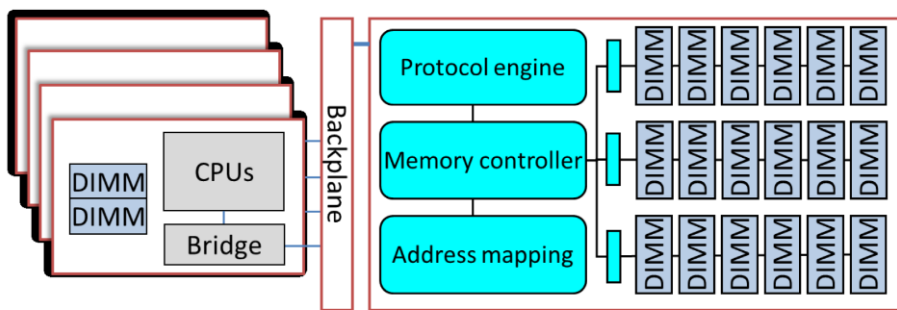Kevin Lim, Yoshio Turner, Jose Renato Santos, Alvin AuYoung, Jichuan Chang, Parthasarathy Ranganathan, and Thomas F. Wenisch, "System-level Implications of Disaggregated Memory," in *Proceedings of the International Symposium on High-Performance Computer Architecture (HPCA)*, 2012

Figure 1. System with memory blades

Figure 2. Memory blade architecture