AVR128DA48-CNANO MPPB-Adapter

AVR128DA48

Curiosity Nano
Curiosity Nano No-BOM 56 Pin

J1

Pin	Signal		Pin
1	NC	VBUS	56
2	ID	VOFF	55
3	CDC RX	DBG3	54
4	CDC TX	DBG0	53
5	DBG1	GND	52
6	DBG2	VCC	51
7	0 TX	ADC 7	50
8	1 RX	ADC 6	49
9	2 SDA	ADC 5	48
10	3 SCL	PWM 4	47
11	4 MOSI	PWM 3	46
12	5 MISO	ADC 2	45
13	6 SCK	ADC 1	44
14	7 SS	ADC 0	43
15	GND	GND	42
16	0 (TX)	7	41
17	1 (RX)	6	40
18	2	5	39
19	3	4	38
20	0	7	37
21	1	6	36
22	2	5	35
23	3	4	34
24	GND	GND	33
25	0	7	32
26	1	6	31
27	2	5	30
28	3	4	29

DRIVE_A_HIGH
DRIVE_B_HIGH
GND
DRIVE_A_LOW
DRIVE_B_LOW
DRIVE_C_LOW
GND
LED
SWITCH

VOFF
GND
+3V3
FEEDBACK_N
FEEDBACK_A
FEEDBACK_B
POTENTIOMETER
V_SUPPLY_MONITOR
FEEDBACK_C
GND
CURRENT_SENSE
PWM_IN
DRIVE_C_HIGH
GND

MPPB_Connector
J3
HDR-2.54 Male 2x20

VOFF
GND
+3V3
SWITCH
LED
POTENTIOMETER
DRIVE_A_HIGH
GND
GND
DRIVE_A_LOW
DRIVE_B_HIGH
DRIVE_B_LOW
DRIVE_C_HIGH
DRIVE_C_LOW
PWM_IN
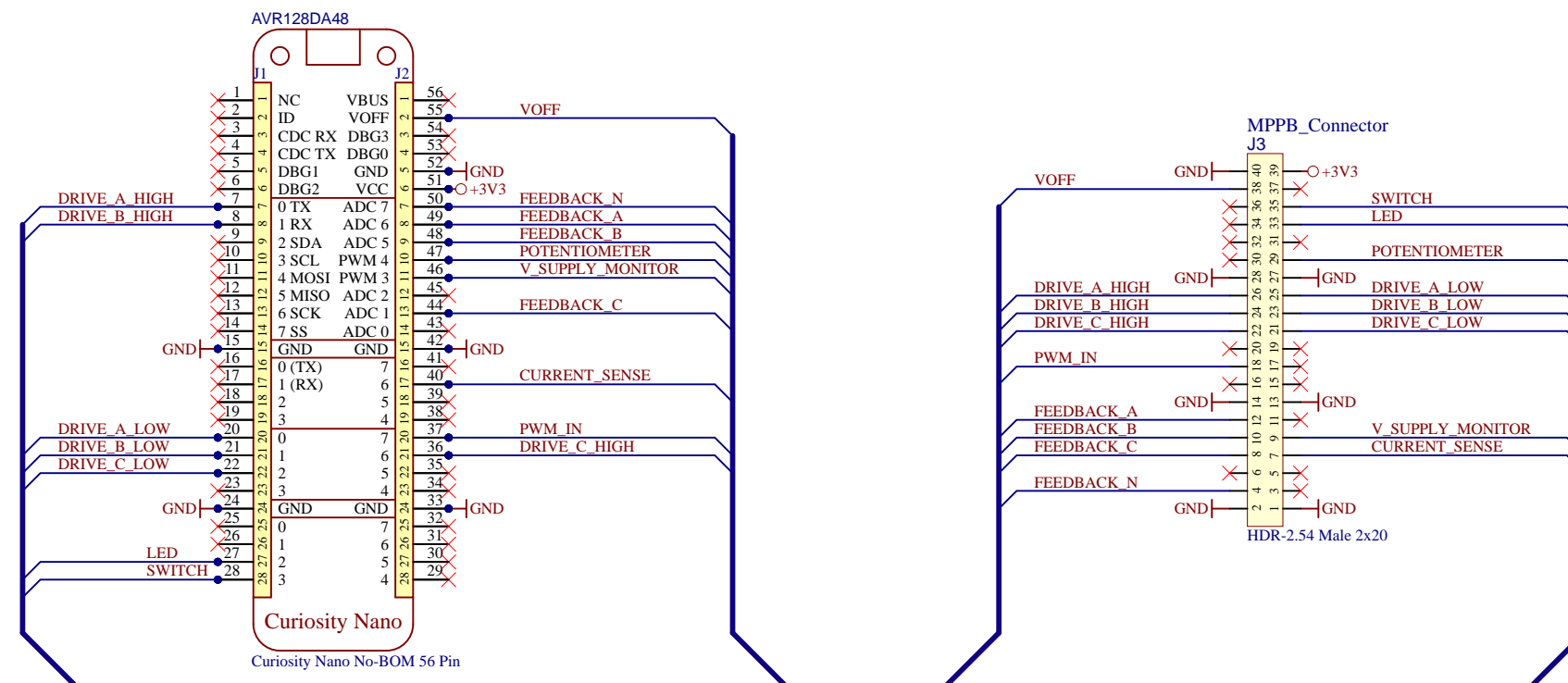FEEDBACK_A
GND
GND
FEEDBACK_B
V_SUPPLY_MONITOR
FEEDBACK_C
CURRENT_SENSE
FEEDBACK_N
GND
GND

PAD1
Rubber Pad Cyl D7.9H5.3

PAD2
Rubber Pad Cyl D7.9H5.3

TEST1
$ >_
13-00902

LABEL1
[ASSY# / REV]
SN: [SERIAL]
[DATE yyyy.mm.dd]
PCBA LABEL 18X6mm

S3
AVR128DA48-CNANO_MPPB-Adapter_Revision_History.SchDoc

Revision History

PCB Assembly Rev 1:

```
-----------------------------------------------------------------
Design Changes:
-----------------------------------------------------------------
Initial Design

-----------------------------------------------------------------
PCB:
-----------------------------------------------------------------
PCB revision 1
```

PCB Assembly Rev 2:

```
-----------------------------------------------------------------
Design Changes:
-----------------------------------------------------------------
Net name and silk changes

-----------------------------------------------------------------
PCB:
-----------------------------------------------------------------
PCB revision 2
```