

# Using Position Manager T-Format Library on IDDK Hardware and Software

## User's Guide



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# ***Using Position Manager T-Format Library on IDDK Hardware and Software***

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## **1 Introduction to IDDK**

The DesignDRIVE Kit (IDDK) is a single platform that makes it easy to develop and evaluate design solutions for many industrial drive and servo topologies. The IDDK offers support for a wide variety of motor types, sensing technologies, encoder standards and communications networks, as well as easy expansion to develop with real-time Ethernet communications and functional safety topologies, enabling more comprehensive, integrated system solutions. Based on the real-time control architecture of TI's C2000™ microcontrollers (MCUs), the kit is ideal for the development of industrial inverter and the servo drives used in robotics, computer numerical control machinery (CNC), elevators, materials conveyance, and other industrial manufacturing applications.

The IDDK offers an integrated drive design with a full-power stage to drive a three-phase motor, easing the evaluation of a diverse range of feedback sensing and control topologies. The kit includes a 180-pin HSEC controlCARD based on the TMS320F28379D C2000 Delfino™ MCU, which integrates dual C28x real-time processing cores and dual CLA real-time co-processors, providing 800 MIPS of floating-point performance with integrated trigonometric and FFT acceleration.

The sophisticated sensing peripherals on the TMS320F28379D MCU, including sigma-delta filter modules with up to eight input channels, four high-performance 16-bit ADCs and eight windowed comparators, enable the IDDK to support shunt, flux gate/HALL, and sigma-delta current sensing simultaneously. For position feedback, the IDDK leverages integrated MCU support for resolver and incremental encoder interfaces. In addition, customers can also explore configuration options that allow the MCU to be placed on either side of the high voltage isolation barrier.

The kit is designed to plug into 110V/220V AC mains, delivers up to 8 Amps, and is rated to drive motors up to one horsepower.

This document covers the kit contents and hardware details, and explains the functions and locations of various connectors present on the board. This document supersedes all the documents available for the kit.

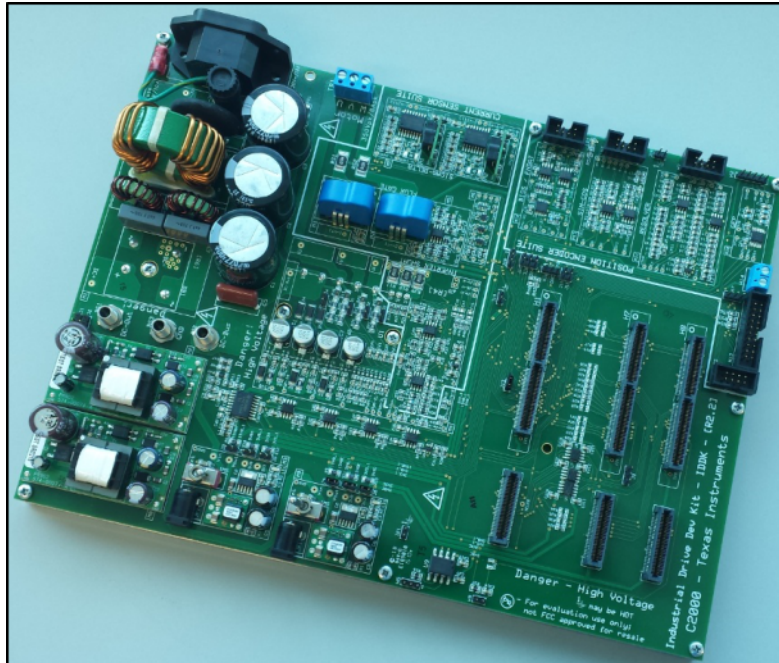
For more details, see the *DesignDRIVE Development Kit IDDK Hardware Reference Guide* ([SPRUI23](#)).

## **2 Hardware Configuration**

To experiment with IDDK for the Position Manager T-Format Library evaluation, the following components are needed:

- An IDDK EVM
- TMDXCNC28379D
- USB-B to A cable
- Encoder/connectors NOT included in the kit – required for evaluation
  - T-FormatEncoder from Heidenhain (for example, TS5700N8501)
  - Cable – length as needed by the application (max 100m)
  - Adapter to connect cable to IDDK

- An external isolated 15 V power supply needed for MCU code development (preferably with a barrel connector commonly referred to as DC jack). Power supply EMSA150120-P5P-SZ is used for testing.
- PC with Code Composer Studio™ (CCSv6 or greater) installed
- IDDK Hardware Reference Guide located at:  
`controlSUITE\development_kits\TMDSIDDK_v2.0\~Docs\`
- For the schematic details of the IDDK EVM, refer to the schematic file located at:  
`controlSUITE\development_kits\TMDSIDDK_v2.0\IDDK_HwDevPkg\IDDK_HwDevPkg_v2.2.1`



**Figure 1. IDDK EVM Kit**

### 3 Hardware Overview

This section describes the subset of the components required for the Position Manager T-Format Library evaluation only. Complete hardware overview of the kit can be obtained from *DesignDRIVE Development Kit IDDK Hardware Reference Guide* ([SPRU123](#)) available on controlSUITE.

Evaluation of the Position Manager T-Format Library requires usage of:

- Processor (CPU) block for control
- Position encoder suite
- On-board power supplies

#### 3.1 Functional Blocks

**Table 1** illustrates the subset of functional blocks on the DesignDRIVE Development Kit IDDK Hardware, along with the macro names used for the Position Manager T-Format Library evaluation.

**Table 1. Hardware Macros in IDDK Used for PM T-Format Evaluation**

Functional Block	Macro Reference	Macro Function
Power Supplies	M9	DC Power Supply – Linear Reg 15 V-5 V/3.3 V
Position Encoder Suite	M12	T-Format Encoder Interface
Processor/ControlCard	H1	All other functions

Each functional block, and the macros that make them, are presented in brief detail in the following sections. The layout of various macros in the board is given in [Figure 2](#). Schematic details of the individual macros are available at:

`controlSUITE\development_kits\ TMDSIDDK_v2.0\IDDK_HwDevPkg\IDDK_HwDevPkg_v2.2.1`

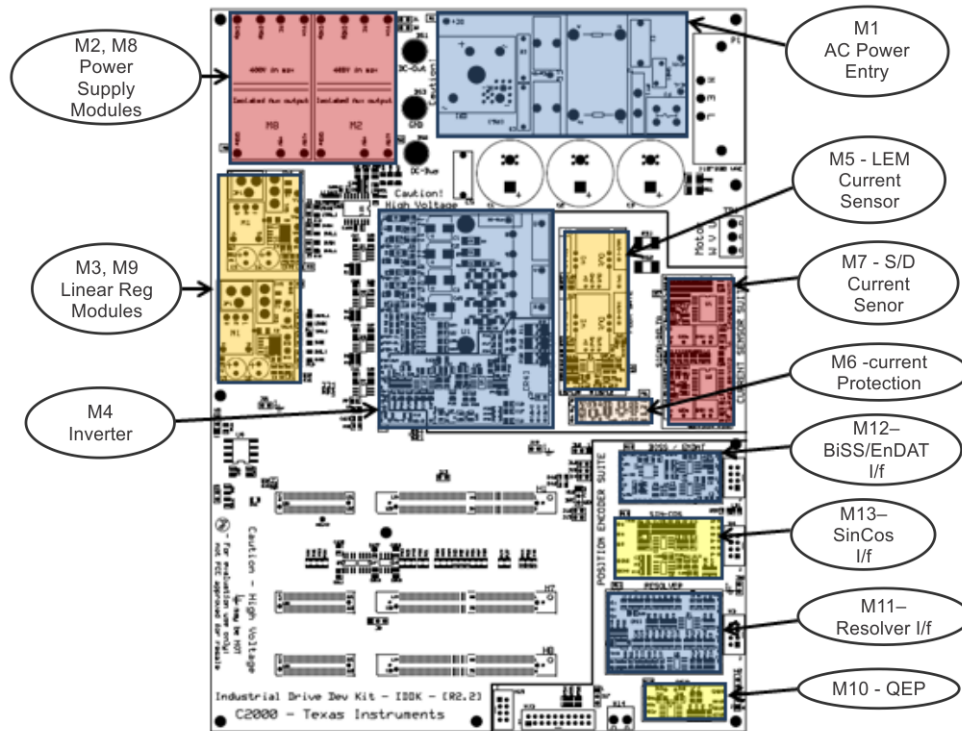


Figure 2. Layout of IDDK EVM With its Functional Macros

### 3.2 Processor Section (Control Processor Slot – H1)

The IDDK is designed around the main control processor card in slot H1. This is designed to plug in a C2000 Delfino (TMS320F28379D) MCU control card TMDXCNC28379D designed with a HSEC180-pin edge connector (see [Figure 3](#)).

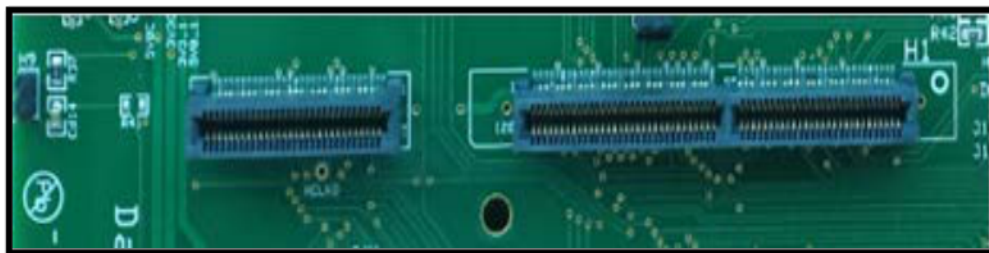
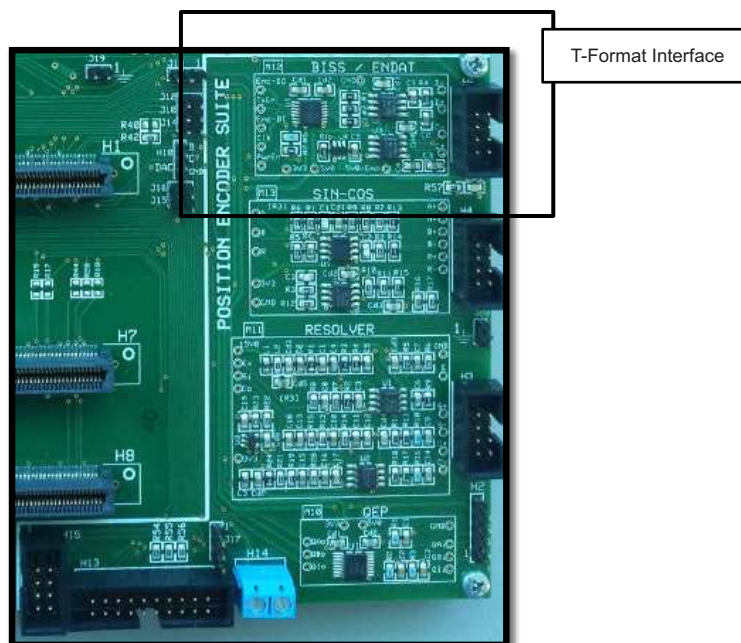


Figure 3. Processor Block

### 3.3 Position Encoder Suite

This block provides a range of position encoder and sensing interfaces (see [Figure 4](#)) such as:

- QEP
- Resolver
- Sin-Cos
- T-Format/EnDat/BiSS

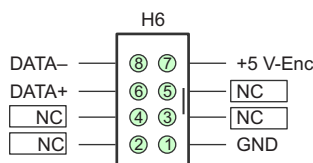


**Figure 4. Position Sensor Suite**

The PM T-Format Library evaluation uses this interface for T-Format communication.

#### 3.3.1 T-Format Encoder

This is designed as a macro, referred to as M12, which is a common interface for T-Format encoders. H6 is the external interface header and it only interfaces digital signals as shown in [Figure 5](#). GPIO32 is used to control on/off of the 5 V supply details in [Table 2](#).



**Figure 5. T-Format/EnDat/BiSS Interface Header**

PM T-Format Library evaluation uses this interface for T-Format communication.



### 3.4 Power Supply

The kit only needs via M9 that has a power supply jack JP1 and a toggle switch SW1. External (15 V) power supply can be fed in through JP1 while SW1 should be turned on to feed the linear regulators of M9. External power supply is preferred to power the controller during code development so that the board operates in low voltage. This ensures a safe environment as there is no high-voltage node present on the board.

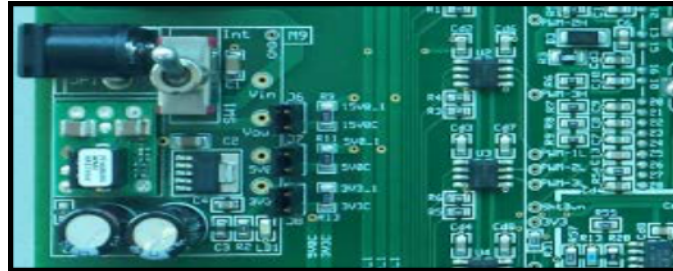


Figure 6. Power Supply Block

## 4 Hardware Resource Mapping

Section 4.1 through Section 4.3 shows hardware resource requirements and signal mapping for the PM T-Format Library evaluation on IDDK.

### 4.1 Digital Signal Mapping

Table 2 shows the functional mapping of signals connected to the control processor on H1 that are relevant to PM T-Format Library evaluation.

Table 2. Digital Signal Mapping on Control Card H1

IDDK Signal Name	MCU GPIO	MCU Peripheral Associated With GPIO	Function
C1-SPIB-CLK	7	PEM CLK	Encoder SPI Clock out
En-DO	24	SPIB	SPI I/f for Abs Encoder
En-DI	25	SPIB	SPI I/f for Abs Encoder
C1-SPIB-CLK	26	SPIB	SPI I/f for Abs Encoder
C1-SPIB-STE	27	SPIB	SPI I/f for Abs Encoder
En-PwrEn	32	GPIO output	Power Enable for Abs Encoder
En-TxEn	34	(GPIO) PEM TX En	Tx Enable for Abs Encoder



## 4.2 Jumpers and Switches

The jumpers shown in [Table 3](#) need to be populated for the PM T-Format Library evaluation.

**Table 3. Purpose of Jumpers and Switches**

Jumpers and Switches	Configuration	Description
[Main] - J6- J8	Populate	Jumpers to bring out linear regulator block M9 voltages
[Main] - J10	Populate	Connects GPIO to T-Format Clock
[Main] - J12	Don't Populate	
[Main] - J13	Populate	Connect SPISTE to ground
[Main] - J14-17	Don't populate	
[Main] - J18	Populate (1-2)	Connects Encoder data out (En-Do-1 to the processor)
[Main] - J19-J21		GND headers for probe access
[M9] – SW1	Turn ON	Select 15 V supply from JP1 or onboard from M8 to generate 5 V and 3.3 V

## 4.3 Headers and Connectors

[Table 4](#) shows the headers and connectors available on the board.

**Table 4. Headers and Connectors**

Headers/ Connectors	Description
[M9] – JP1	15 V DC power supply jack adaptor
[Main] - H1	180 pin HSEC connector slot for control processor card
[Main] – H6	4x2 header for T-Format

## 5 Hardware Setup Instructions for PM T-Format Library Evaluation

1. Ensure default configuration. Make sure that jumpers [Main]-J6, [Main]-J7 and [Main]-J8 (in front of macro M9) and resistors [Main] R8-R13 are populated, and that GND plane resistors R14 and R15 are mounted as shown in the *Various GND Planes on the Bottom of Board* and *Default Connection of Various GND Planes* figures of the *Power Supplies* section in the *DesignDRIVE Development Kit IDDK v2.2 - Hardware Reference Guide* ([SPRUI23](#)).
  2. Unpack the TMDXCNC28379D control card and slide it in the connector slot of [Main]-H1. Push down vertically using even pressure on both ends of the card until it cannot slide down further. Spread open the retaining clips, if present, to remove the card and pull the card out by applying even force at the far edges.
  3. Connect a USB cable to connector J1 on the control card. The control card isolates the JTAG signals between the C2000 device and the computer. LED D2 on the control card should light.
  4. Connection to the encoder:
    - (a) Prepare an adapter to connect the cable to the IDDK T-Format interface ([Main] – H6 4x2 header).
    - (b) Typically, the following connections are needed from IDDK to the encoder:
      - (i) D+ → Data + signal
      - (ii) D- → Data - signal
      - (iii) 5 V → Encoder Power Supply
      - (iv) GND → Ground
    - (c) Cable connections between the Encoder and IDDK. Insert the header created using the Adapter mentioned above in [4a](#) into ([Main] – H6 4x2 header) of IDDK; the other end of the Adapter connects to the Male side of the cable. The Female end of the cable connects to the encoder.
- To make the appropriate connections, see the encoder/cable specifications and schematics IDDK hardware (located in [ControlSuite](#)).

5. Ensure that the toggle switch [M9]-SW1 is in “Int” position. Connect an isolated 15 V DC power supply to [M9]-JP1.
6. Turn on the toggle switch [M9]-SW1, then [M9]-LD1 should turn on. Note that more LEDs on the control card should light up indicating that the control card is receiving power from the board.

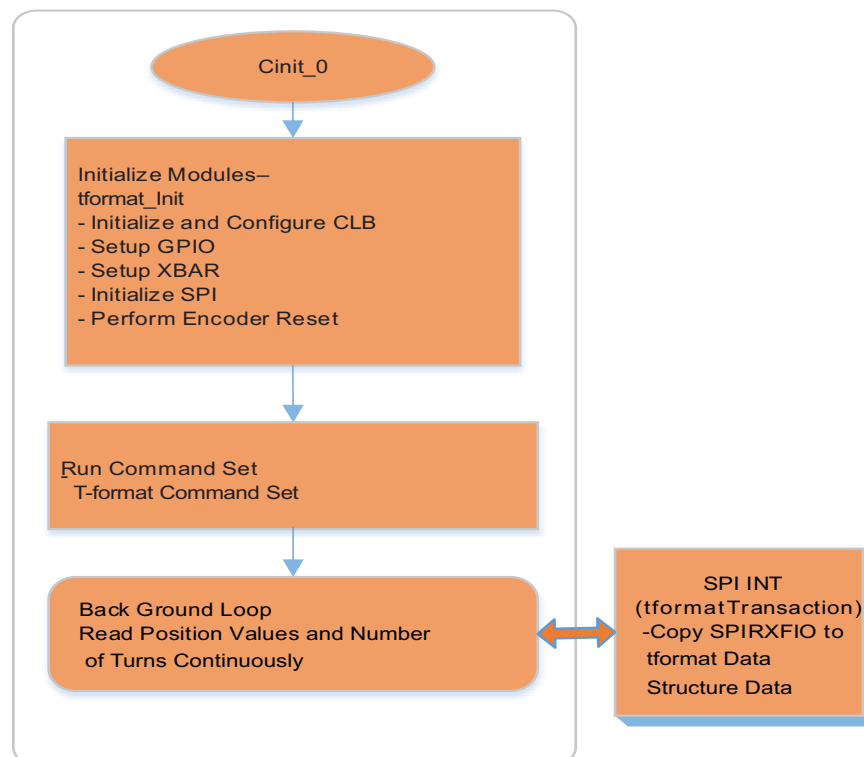
## 6 Software Setup for PM T-Format Evaluation Example Projects

### 6.1 Installing Code Composer and controlSUITE

1. Install Code Composer v6.x or later from <http://www.ti.com/tool/CCSTUDIO>, if not already installed.
2. Go to <http://www.ti.com/controlsuite> and run the controlSUITE installer. Allow the installer to download and update any automatically checked software for C2000.
3. T-Format Library is available at:  
 <base> install director is  
 C:\ti\controlSUITE\libs\app\_libs\position\_manager\vX.X  
 The following sub-directory structure is used:  
 <base>\Doc - Documentation  
 <base>\Float - Contains implementation of the library and corresponding include file  
 <base>\examples - Example using T-Format Library


### 6.2 Software Flow Diagram

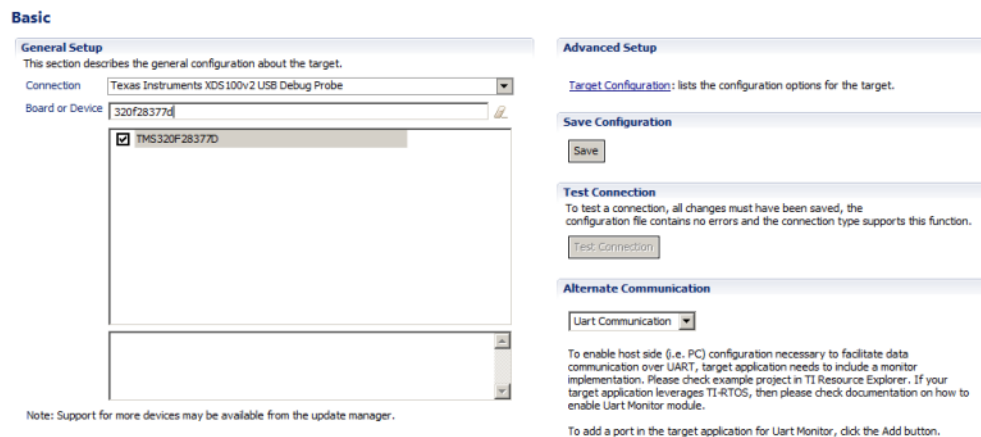
The software flow used in the example project *PM\_tformat\_systemtest* is depicted in Figure 7.



**Figure 7. Software Flow Diagram for the Example Project PM\_tformat\_systemtest**

## 7 Setup Code Composer Studio to Work With TMDXIDDK379D

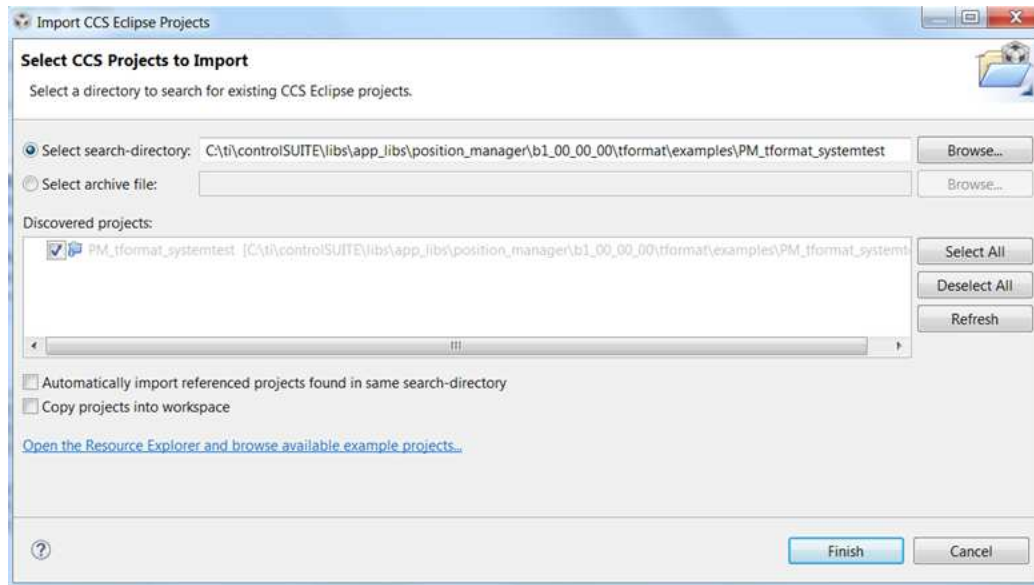
1. Open "Code Composer Studio". Note that this document assumes that version 6 or later is used.
2. Once Code Composer Studio opens, the workspace launcher may appear that would ask to select a workspace location. Note that workspace is a location on the hard drive where all the user settings for the IDE (which projects are open), what configuration is selected, and so forth are saved. This can be anywhere on the disk, the location mentioned below is just for reference. Note that if this is not your first-time running Code Composer the dialog below may not appear).
  - Click the "Browse..." button
  - Create the following path by making new folders as necessary:  
C:\c2000\_projects\CCSv6\_workspaces\PM\_tformat\_eval\_workspace
  - Uncheck the box that says "Use this as the default and do not ask again".
  - Click "OK"
3. This will open a 'Getting Started' tab with links to various tasks from creating a new project, importing an existing project, to watching a tutorial on CCS. You can click the 'Import Project' icon that will skip the procedure to step 2 in [Section 7.1](#). Or, you can close the 'Getting Started' Tab and go to next step.
4. Code Composer is configured in order to know which MCU it will be connecting to. This is done by setting up the 'Target Configuration'. All of these are already set up and configured in "xds100v2\_F2837x.ccxml" (provided as part of the files in the project), therefore, the user can skip to step 1 in [Section 7.1](#). However, for general information regarding setting up this configuration file, steps 6, 7 and 8 can be used.
5. A new configuration file can be set by clicking "View → Target Configuration". This will open the Target Configuration window. In this window, click on . Give a name to the new configuration file depending on the target device. If "Use shared location" checkbox is checked, then this configuration file can be stored in a common location by CCS for use by other projects as well. Then, click Finish.
6. This should open up a new tab as shown in [Figure 8](#). Select and enter the following options:
  - (a) Connection – Texas Instruments XDS100v2 USB Emulator (or)  
Texas Instruments XDS100v2 USB Debug Probe
  - (b) Device – the C2000 MCU on the control card, TMS320F28379D, for example
  - (c) Click Save and close



**Figure 8. Configuring a New Target**

7. Click "View → Target Configurations". In the "User Defined" section, find the file that was created in steps 6 and 7. Right click on this file and select "Set as Default". To use the configuration file supplied with the project, click "View → Target Configurations, expand "Projects → PM\_tformat\_systemtest, and right-click on the "xds100v2\_F2837x.ccxml" and "Set as Default" files. This tab also allows you to reuse the existing target configurations and links them to specific projects.

8. Add the PM Tformat evaluation example project into the current workspace by clicking "Project → Import CCS Project".
  - (a) Select the project by browsing to:  
`C:\ti\controlSUITE\libs\app_libs\position_manager\b1_00_00_00\tformat\examples\PM_tformat_systemtest`

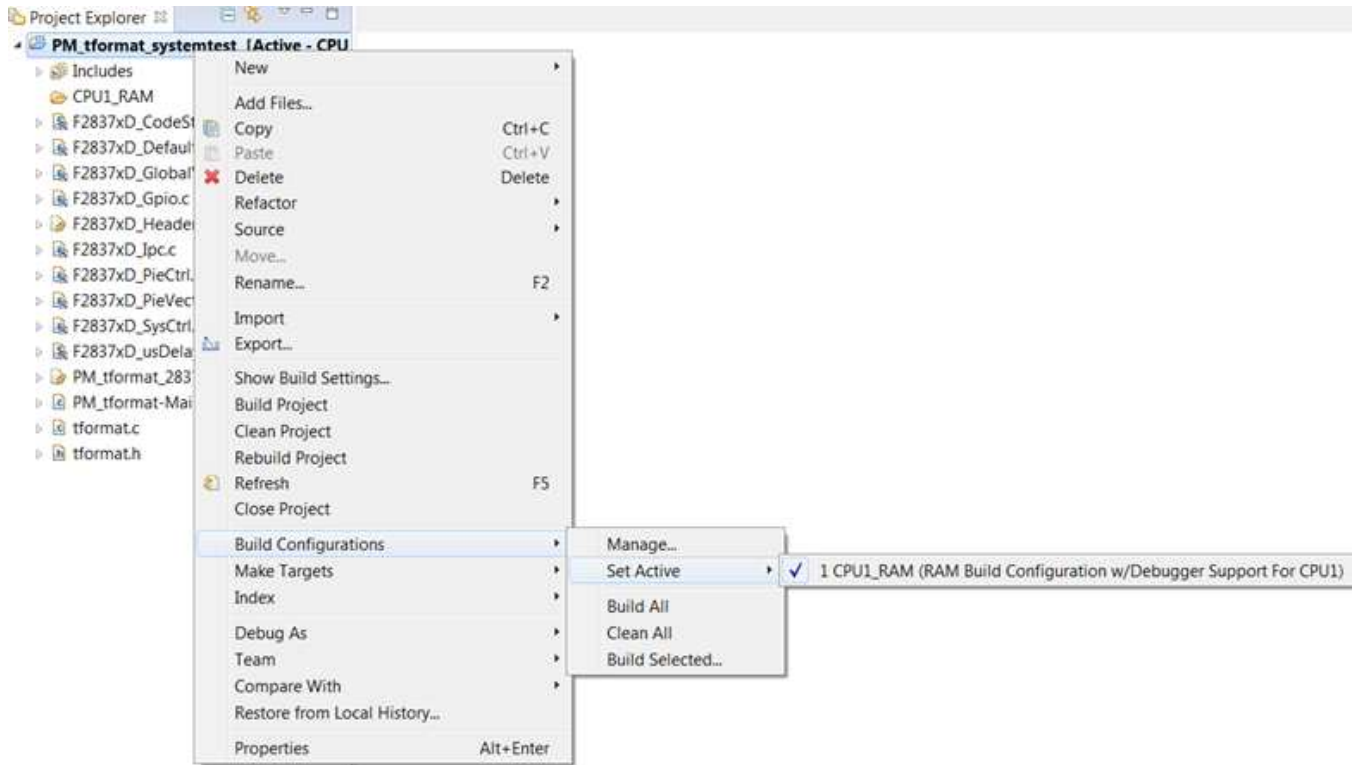


**Figure 9. Adding PM T-Format Example Project to Workspace**

- (b) If there are multiple projects in this directory, choose the projects to import and then click Finish. This copies all of the selected projects into the workspace. There is only one project in [Figure 9](#), select it and click Finish.


## 7.1 Configuring a Project

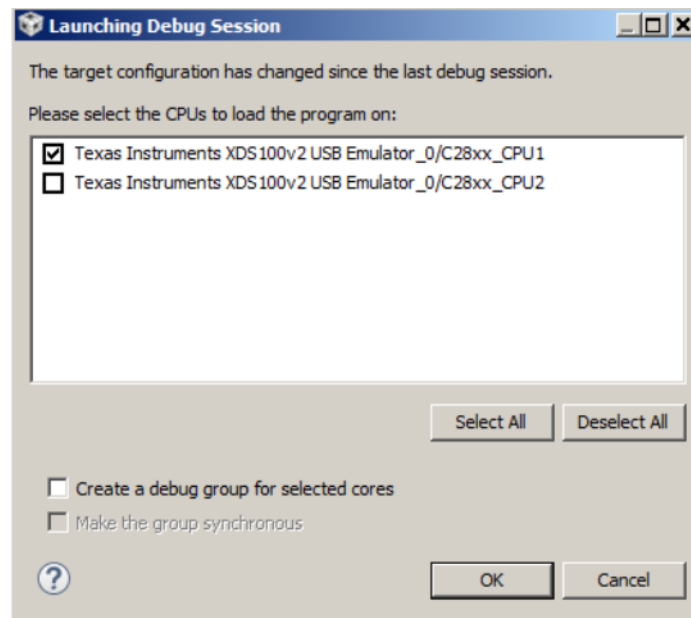
1. Assuming this is your first time using Code Composer, the xds100v2-F2837x should have been set as the default target configuration. Verify this by viewing the xds100v2-f2837x.ccxml file in the expanded project structure and an [Active/Default] status written next to it. By going to "View → Target Configurations", you can edit existing target configurations or change the default or active configuration. You can also link a target configuration to a project in the workspace by right clicking on the Target configuration name and selecting Link to Project.
2. The project can be configured to create code and run in either Flash or RAM. You can select either of the two, however, RAM configuration is used most of the time for lab experiments and Flash configuration for production. As shown in [Figure 10](#), right-click on an individual project and select Active Build Configuration → CPU1\_RAM configuration.





**Figure 10. Selecting the F2837x\_RAM Configuration**

## 7.2 Build and Load the Project

1. Open the tformat.h file and make sure that TFORMAT\_FREQ\_DIVIDER is set as needed; save this file. For more details, see the *C2000 Position Manager T-Format Library Module User's Guide* (SPRU171).  
C:\ti\controlSUITE\libs\app\_libs\position\_manager\v1\_00\_00\_00\tformat\Doc\PM\_tformat\_Lib.pdf
2. Right click on the Project Name and select "Rebuild Project", then watch the Console window. Any errors in the project will be displayed in the Console window.
3. On successful completion of the build, click the "Debug" button  (located in the top-left side of the screen). The following window may pop up if it is the first time that it is used, requesting that you select which of the two CPUs (in F28379x) to connect to (see Figure 11). Select CPU1 by clicking the box next to CPU1.



**Figure 11. Selecting the CPU(s) to Connect**

4. The IDE will automatically connect to the target, load the output file into the device, and change to the Debug perspective.
5. Click the Tools → Debugger Options → Program / Memory Load Options. You can enable the debugger to reset the processor each time it reloads the program by checking "Reset the target on program load or restart" and click "Remember My Settings" to make this setting permanent.
6. Click on the "Enable silicon real-time mode" button , which auto selects the "Enable polite real-time mode" button . This allows you to edit and view variables in real time. Do not reset the CPU without disabling these real-time options.
7. Select YES to enable debug events, if a message box appears. This will set bit 1 (DGBM bit) of the status register 1 (ST1) to a "0". The DGBM is the debug enable mask bit. When the DGBM bit is set to "0", memory and register values can be passed to the host processor for updating the debugger windows.




### 7.3 Setup Watch Window and Graphs

1. Click View → Expressions on the menu bar to open a *watch window* to view the variables being used in the project. Add variables to the watch window as shown in [Figure 12](#). It uses the number format associated with variables during declaration. You can select a desired number format for the variable by right clicking on it and choosing. [Figure 12](#) shows a typical expressions window.




Expression	Type	Value
tformatData	struct <unnamed>	{...} (Hex)
controlField	unsigned int	0x0058 (Hex)
statusField	unsigned int	0x0004 (Hex)
dataField0	unsigned int	0x007C (Hex)
dataField1	unsigned int	0x003E (Hex)
dataField2	unsigned int	0x00EA (Hex)
dataField3	unsigned int	0x00E8 (Hex)
dataField4	unsigned int	0x0000 (Hex)
dataField5	unsigned int	0x0000 (Hex)
dataField6	unsigned int	0x0000 (Hex)
dataField7	unsigned int	0x0003 (Hex)
crc	unsigned int	0x0061 (Hex)
eepromAddress	unsigned int	0x0081 (Hex)
eepromWrDdata	unsigned int	0x0000 (Hex)
eepromRdDdata	unsigned int	0x0000 (Hex)
spi	struct SPI_REGS *	0x00006110 (Hex)
sdata	unsigned long[16]	0x0000AA10@Dat...
rdata	unsigned long[16]	0x0000AA30@Dat...
dataReady	unsigned int	0x0001 (Hex)
fifo_level	unsigned int	0x0008 (Hex)
rxPkts	unsigned long[3]	0x0000AA52@Dat...
crcResult	unsigned long	0x000000DF (Hex)
position	unsigned long	0x00577C3F (Hex)
turns	unsigned long	0
Add new expression		

**Figure 12. Configuring the Expressions Window**

Alternately, a group of variables can be imported into the Expressions window, by right clicking in the Expressions Window and clicking Import, then, browse to the .txt file containing these variables. Browse to the root directory of the project and pick 'PM\_tformat\_systemtest\_VAR.txt' and click OK to import the variables shown in [Figure 12](#).

2. Click on the Continuous Refresh button  in the watch window. This enables the window to run with real-time mode. By clicking the down arrow in this watch window, you can select "Customize Continuous Refresh Interval" and edit the refresh rate of the watch window. Note that choosing too fast an interval may affect performance.



### 7.4 Run the Code

1. Run the code by pressing the Run Button  in the Debug Tab.
2. The project should run and the values in the watch window should continuously update. Below are screen captures of typical CCS perspectives while using this project. You may want to resize the windows according to your preference.
3. Once complete, reset the processor (Run → Reset → CPU Reset)  and terminate the debug session by clicking (Run → Terminate) . This halts the program and disconnects Code Composer from the MCU.
4. As the encoder sends the position information, observe the same in the watch window, turns variables.
5. If the encoder is not mounted on a spinning Motor, you can manually rotate the encoder shaft and observe the position changing in the watch window.



## 7.5 Next Steps

It is not necessary to terminate the debug session each time you change or run the code. Instead, the following procedure can be followed:

1. After rebuilding the project, (Run → Reset → CPU Reset) , (Run → Restart) , and enable real-time options. Once complete, disable real-time options and reset the CPU. Terminate the project if the target device or the configuration is changed (Ram to Flash or Flash to Ram) and prior to shutting down CCS.
2. Customize the project to meet your encoder and application requirements. Change the encoder type in the encoder.h file.
3. Modify the example code as needed and perform tests with different cable lengths.

## 8 References

- *DesignDRIVE Development Kit IDDK Hardware Reference Guide* ([SPRUI23](#))
- *C2000 Position Manager T-Format Library Module User's Guide* ([SPRUI71](#))

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Only those TI components which TI has specifically designated as military grade or "enhanced plastic" are designed and intended for use in military/aerospace applications or environments. Buyer acknowledges and agrees that any military or aerospace use of TI components which have **not** been so designated is solely at the Buyer's risk, and that Buyer is solely responsible for compliance with all legal and regulatory requirements in connection with such use.

TI has specifically designated certain components as meeting ISO/TS16949 requirements, mainly for automotive use. In any case of use of non-designated products, TI will not be responsible for any failure to meet ISO/TS16949.

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