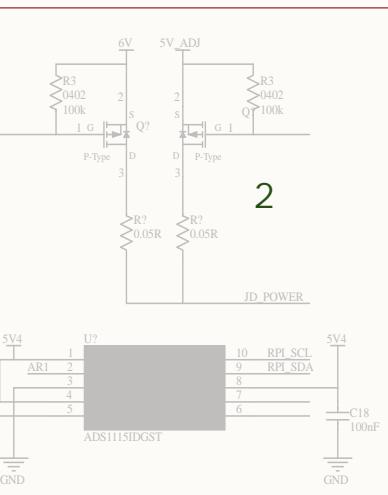
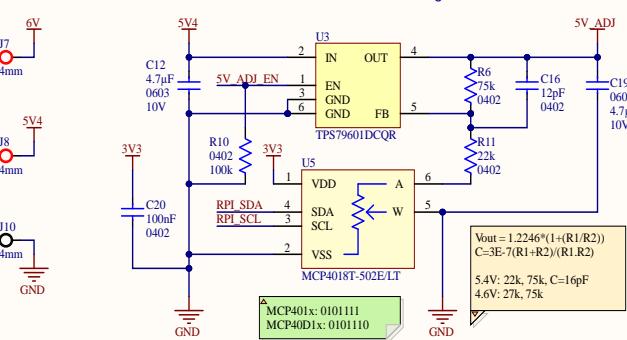
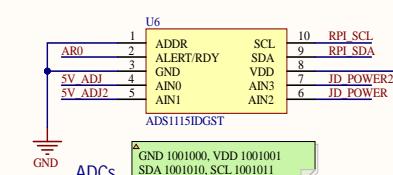
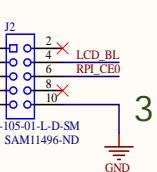
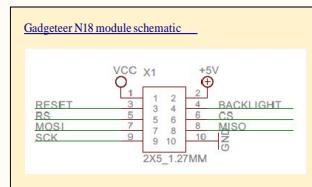


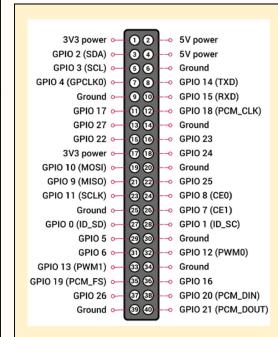
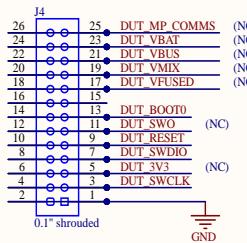
## Power in



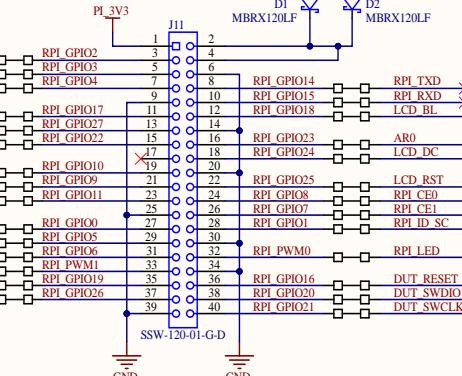
## Display connector



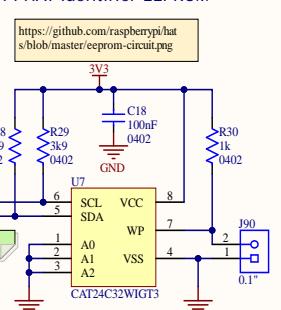
## DUT programming, debug &amp; test connector footprints



## R Pi header connections

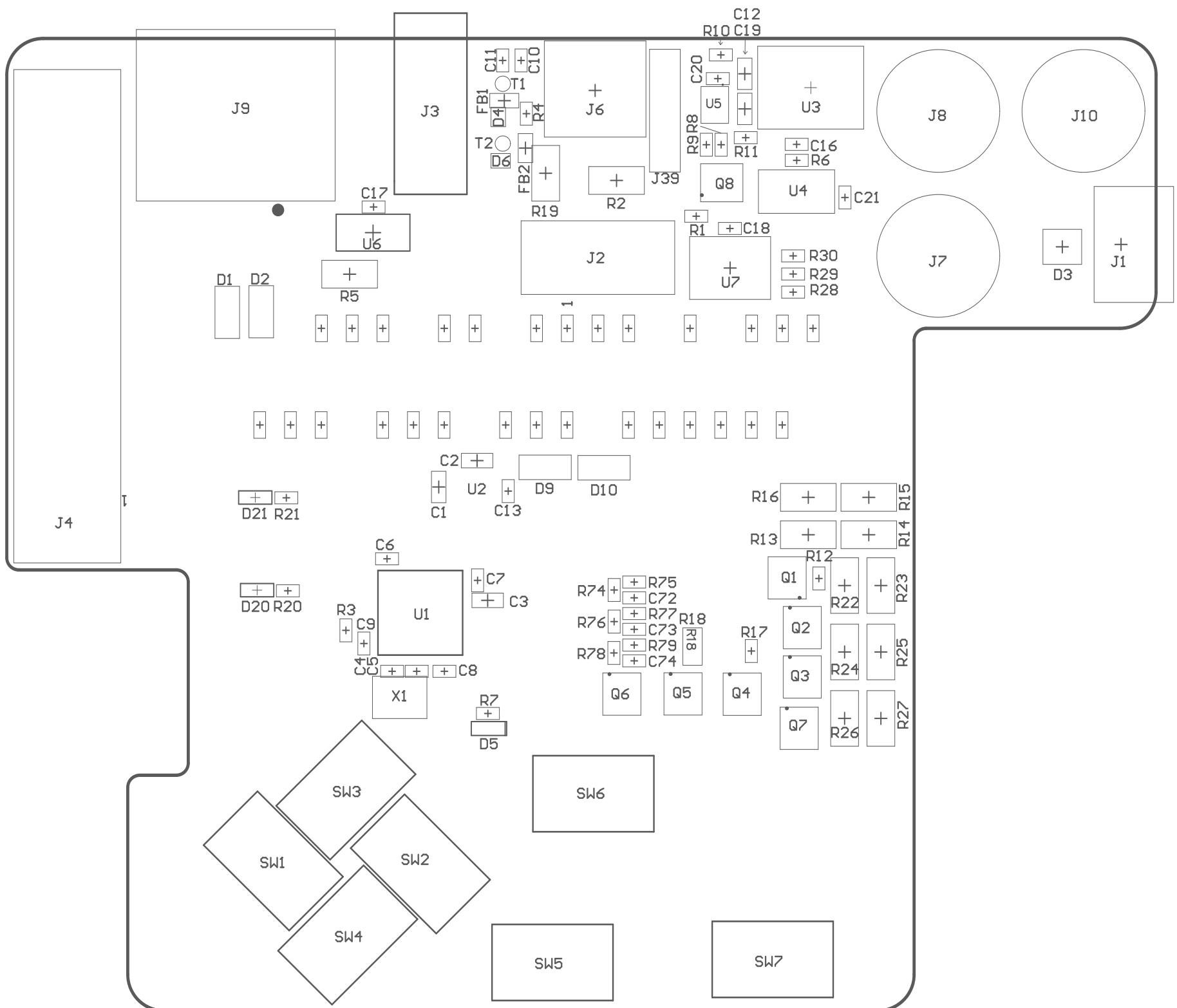


## R Pi HAT identifier EEPROM



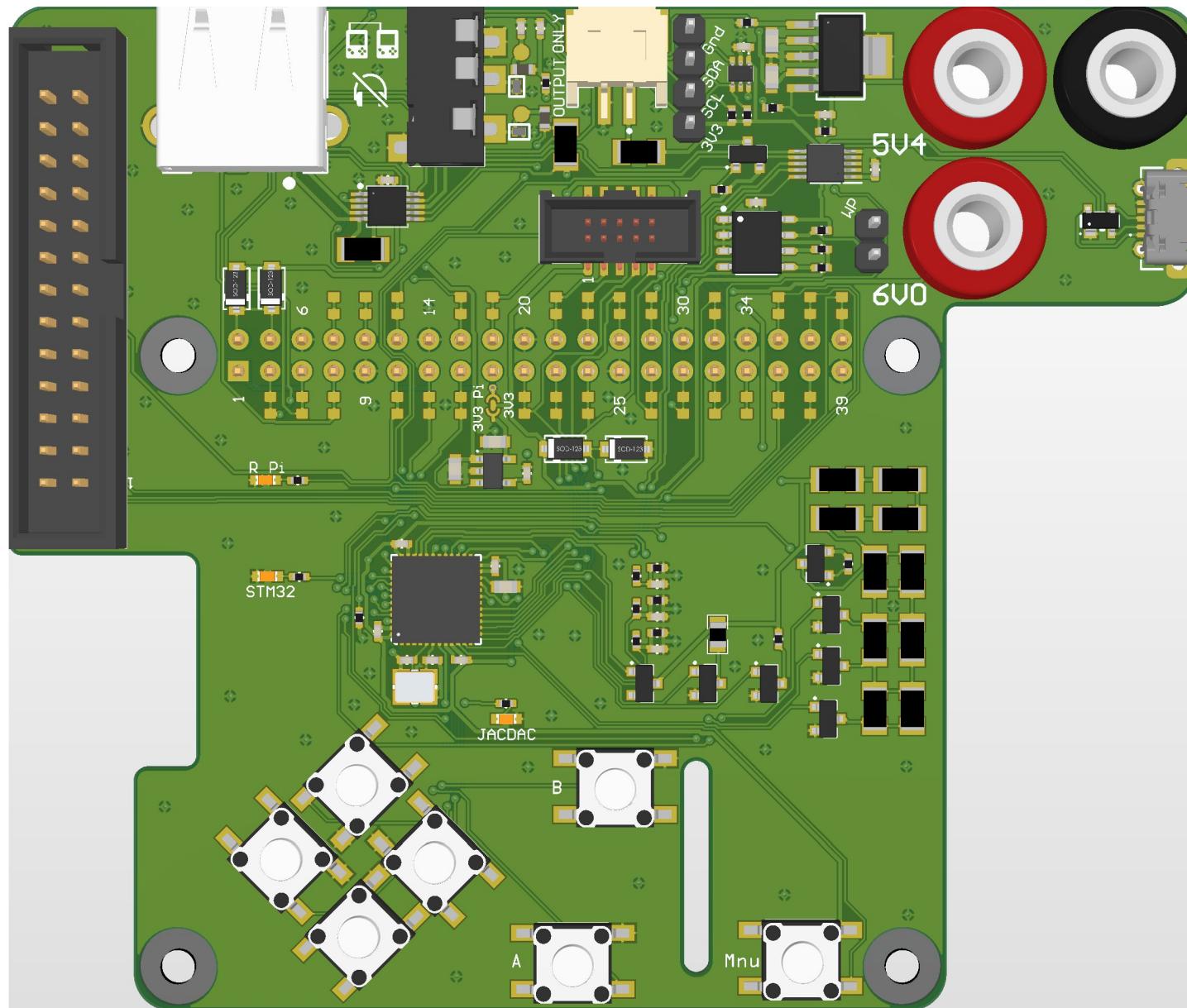
Connector footprints which support a manufacturing jig and also allow a header to be used to support ad-hoc and low volume testing. Place footprint so that hole centres are exactly 0.1" from board edge.

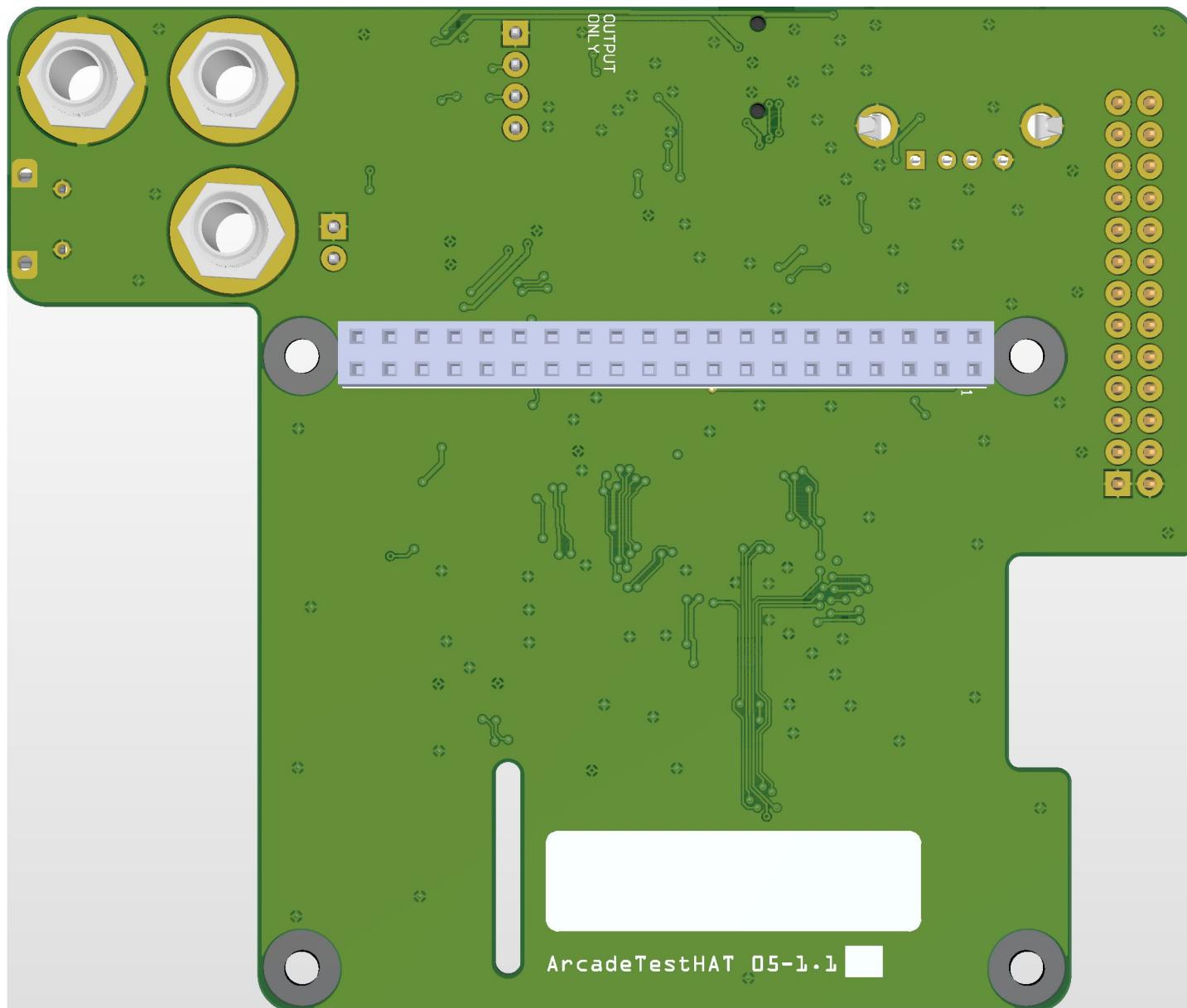


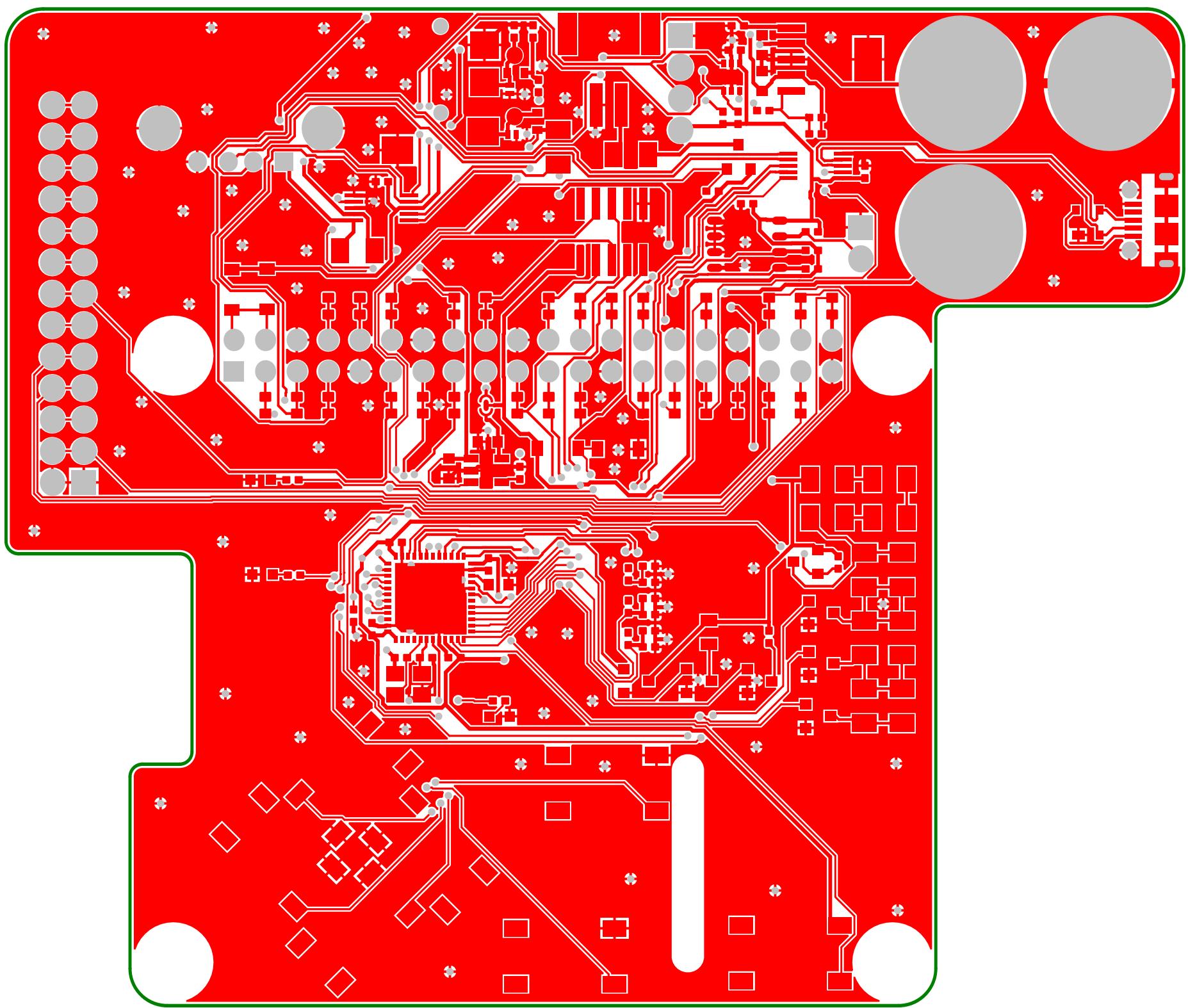


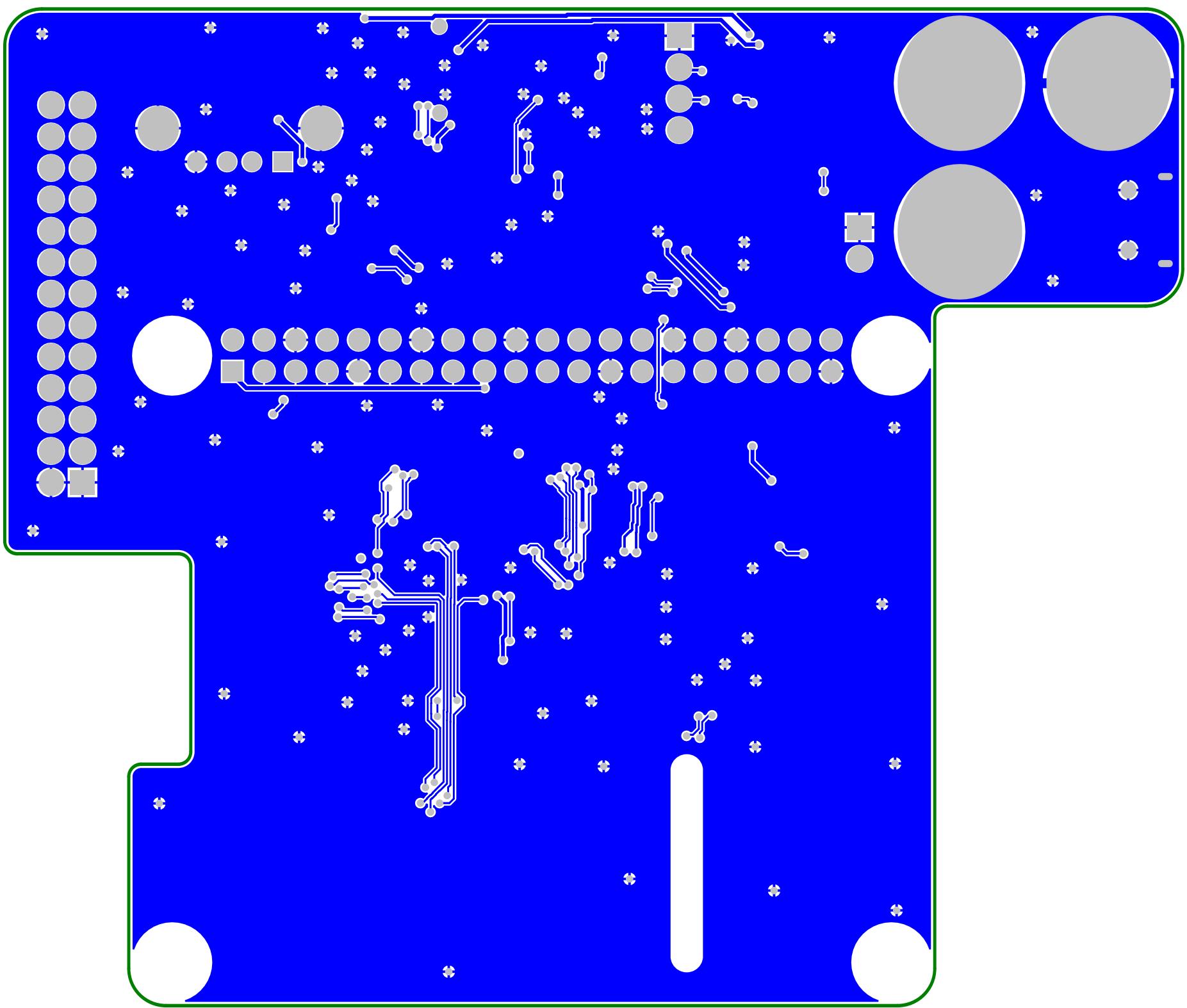
III

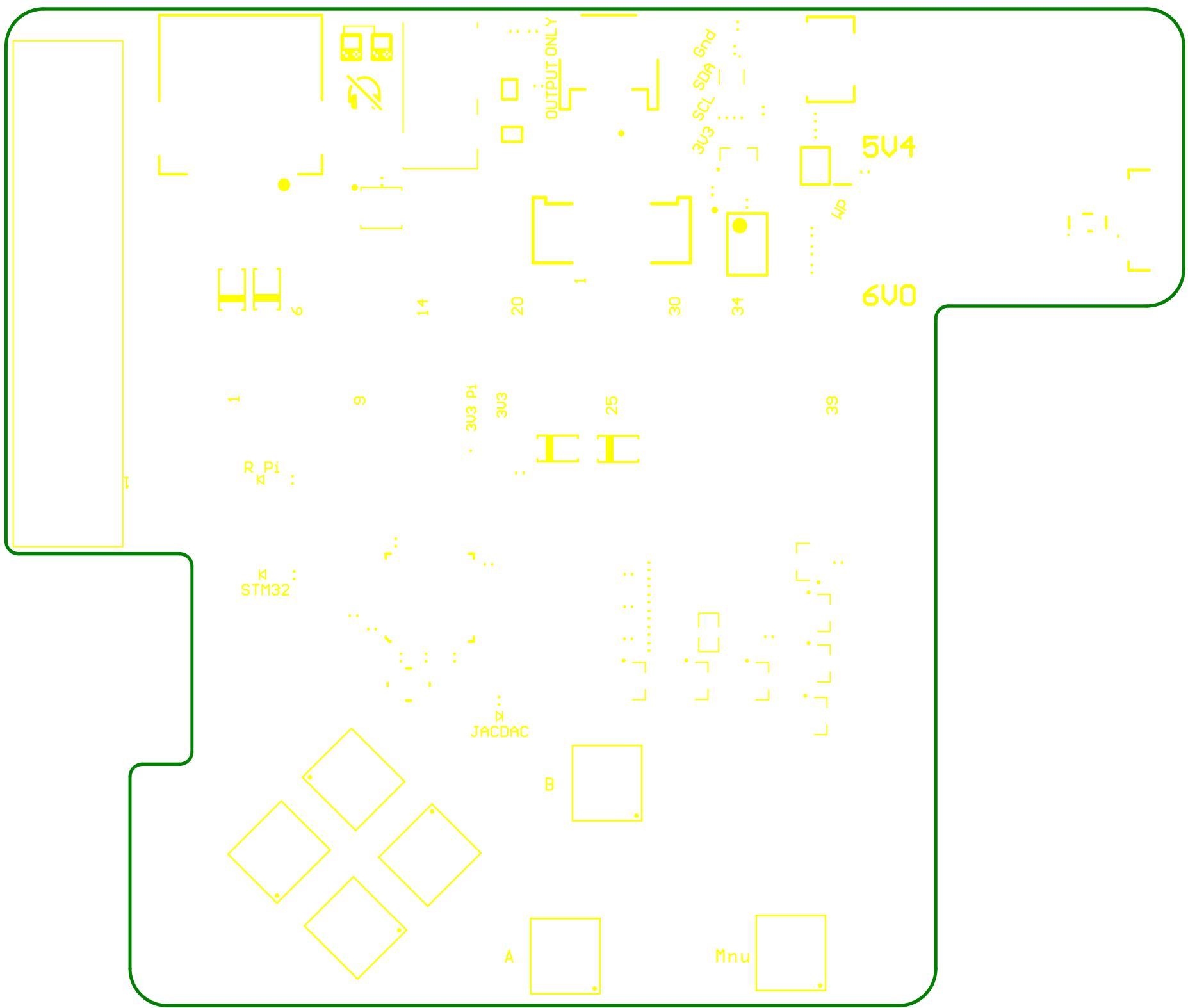
III











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