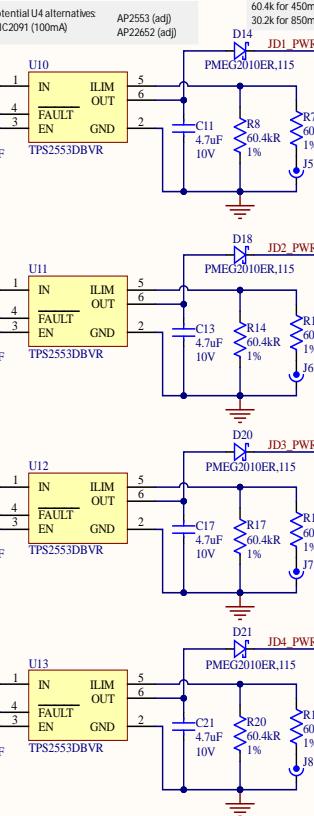
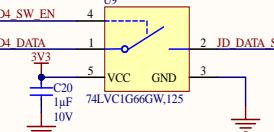
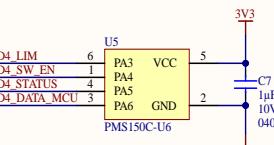
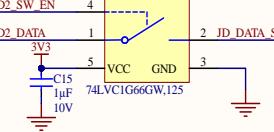
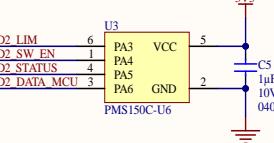
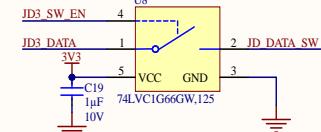
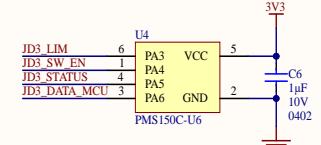
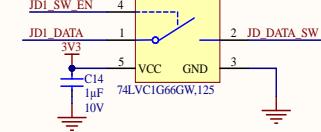
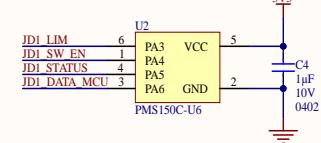
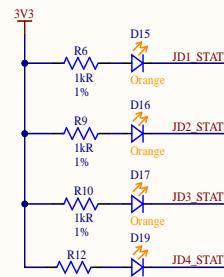


U2, U3, U4 & U5 must be programmed before being soldered down due to restrictive in-circuit programming requirements. JD_STATUS is PWM-capable GPIO.

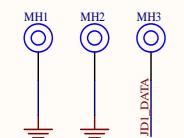
MCUs & JD SWITCHES



Status LEDs.

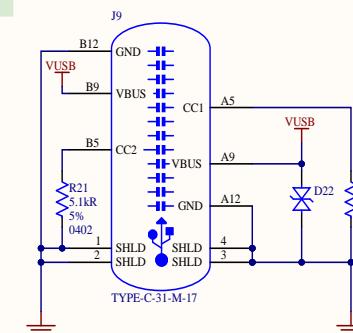


Mounting holes.

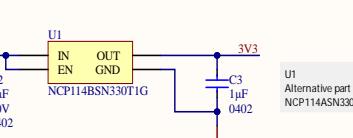


JACDAC mounting holes are grounded plated through hole, finished diameter of 2.2mm and annular ring of 3.2mm diameter.

Power supply.



3V3 Regulator



Key:

Silkscreen & layout notes

Block name

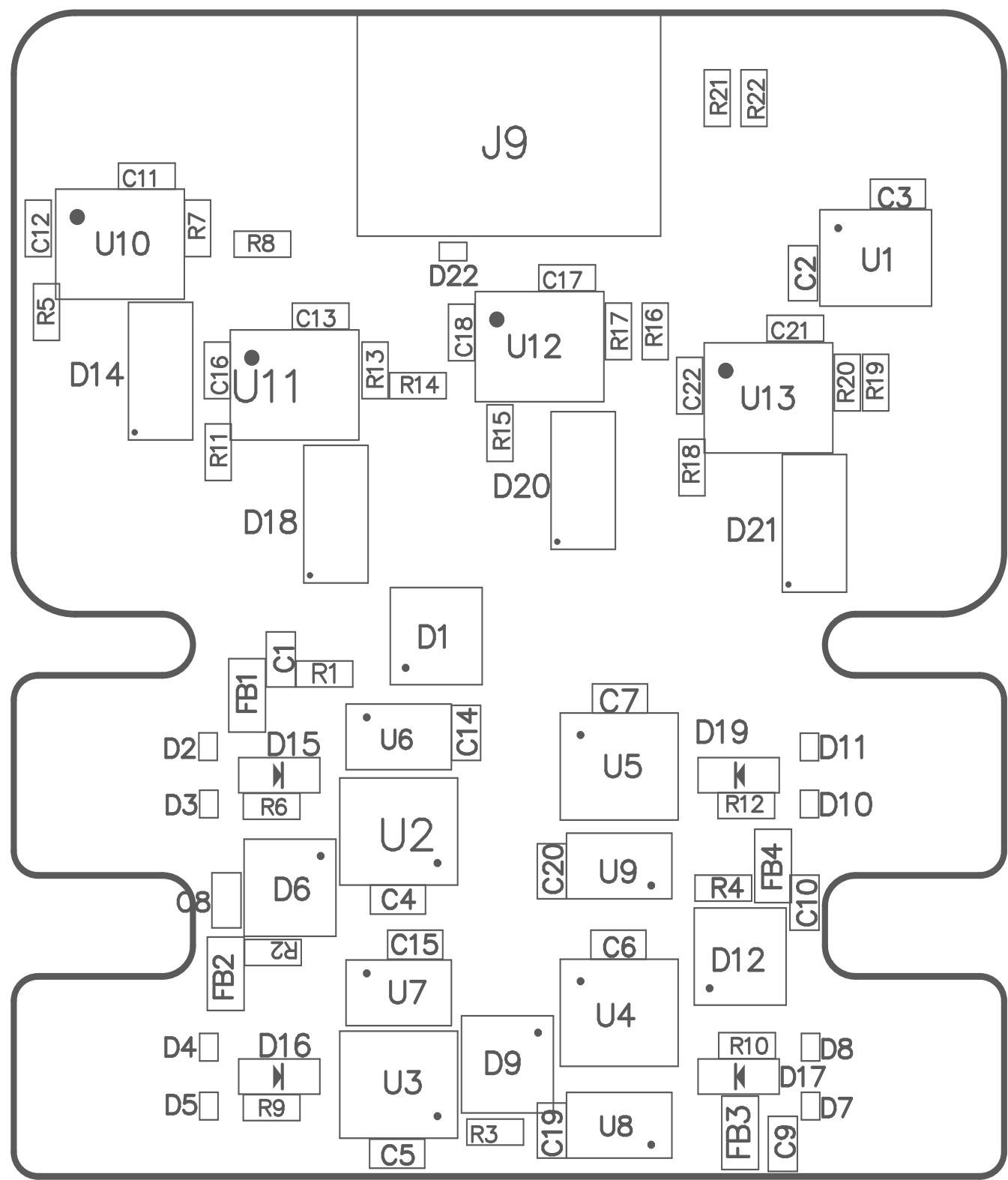
Design notes

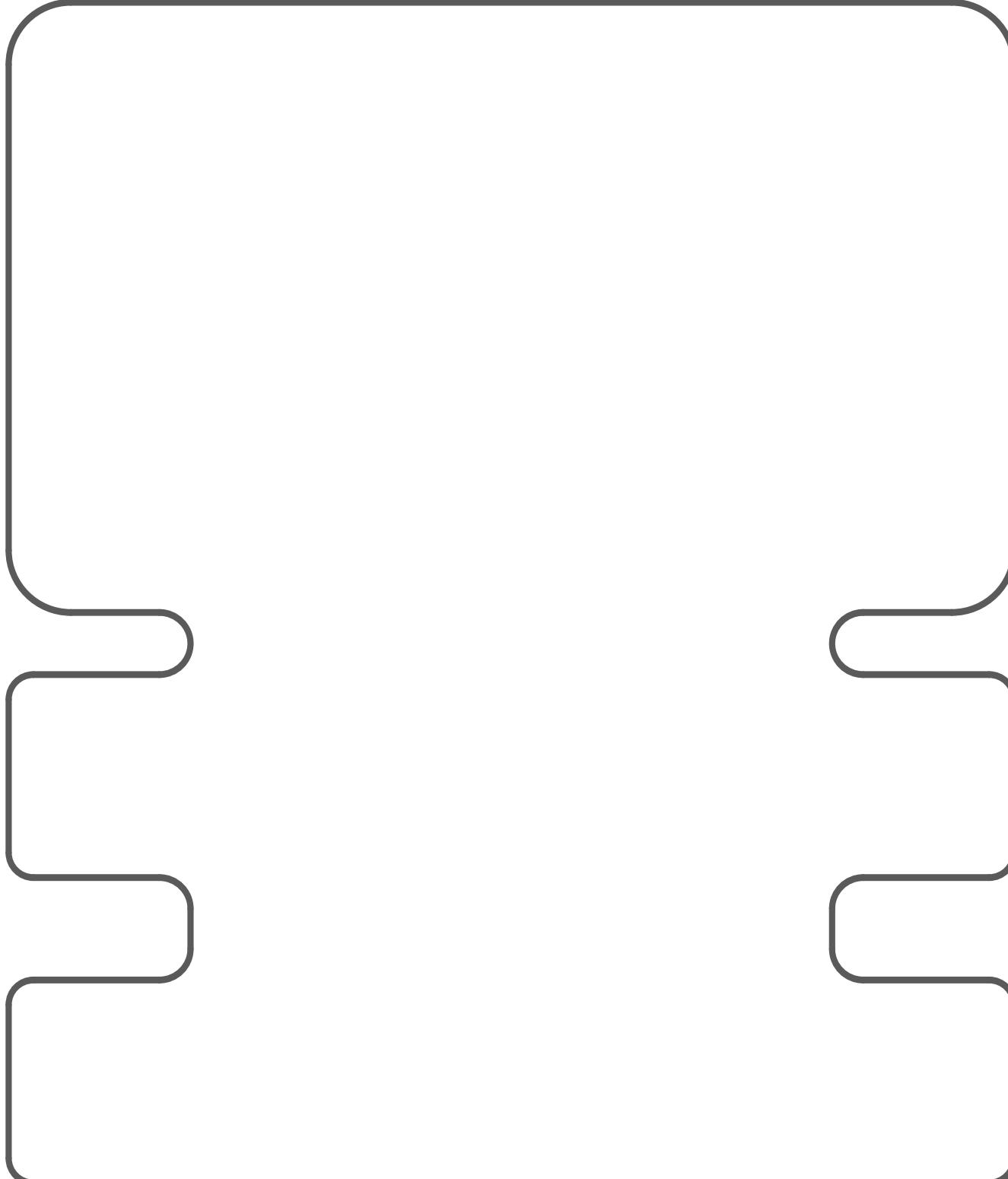
Microsoft

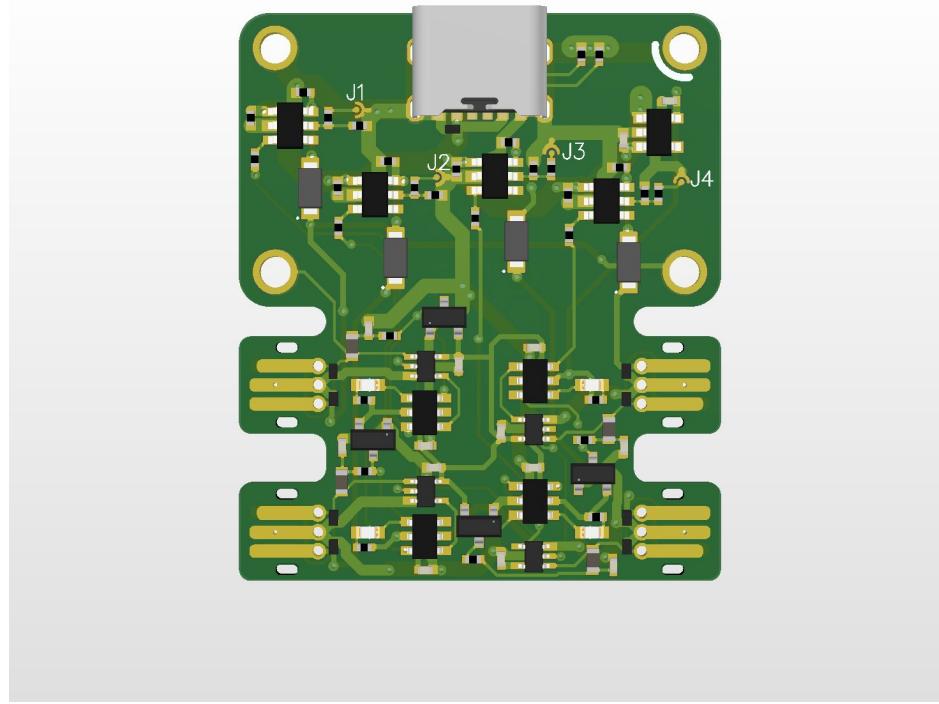
PROJECT DESCRIPTION
Jacdac smart power provider

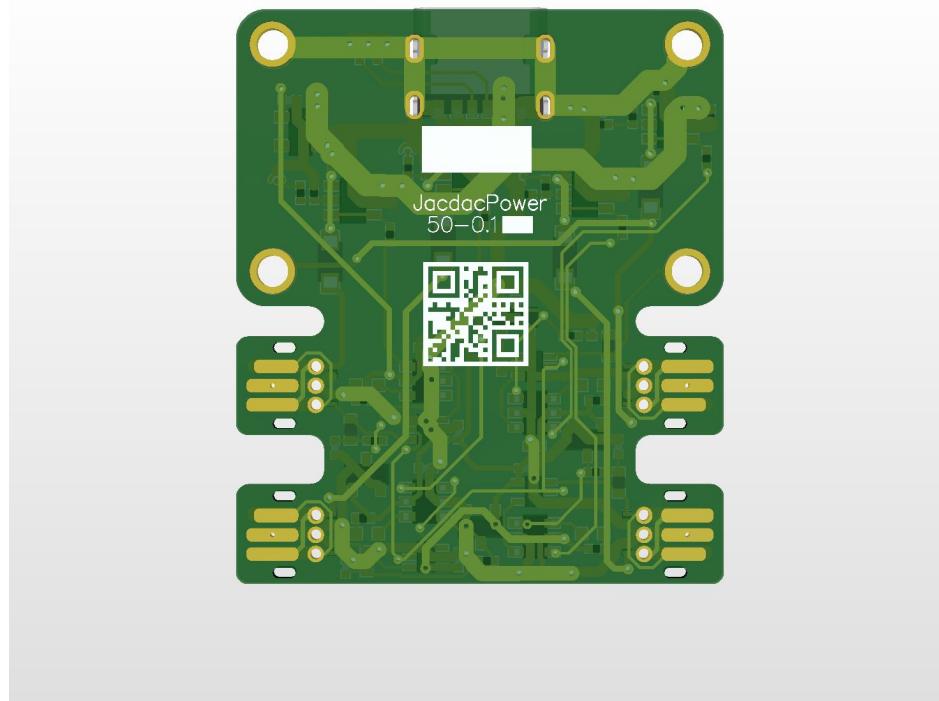
SHEET DESCRIPTION
Complete design

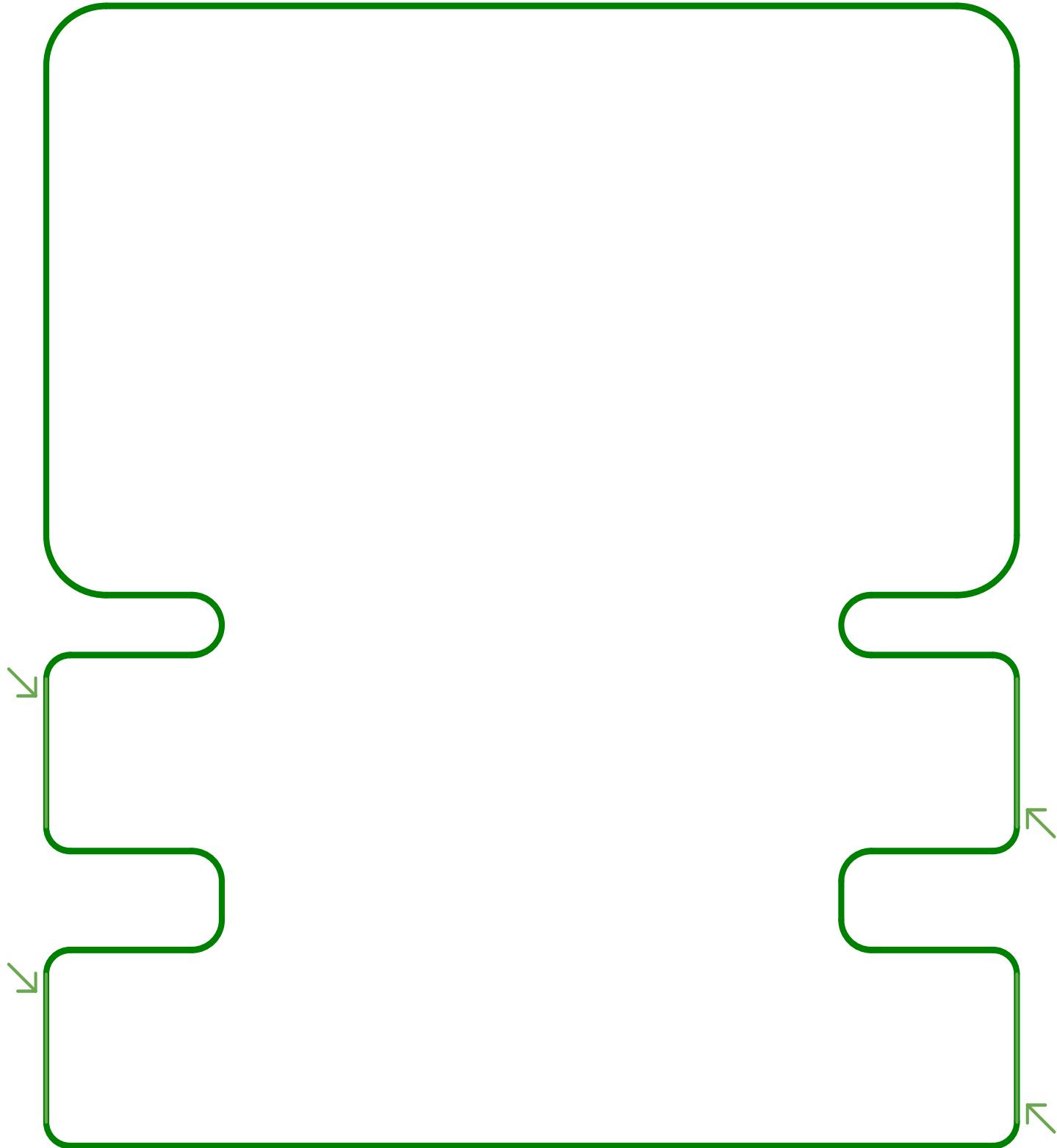
LAST MODIFIED 7/1/2021 PAGE 1 OF 1 DRAWN BY DG, MM, JD & SH REVISION 0.1 PCB ID 50-0.1

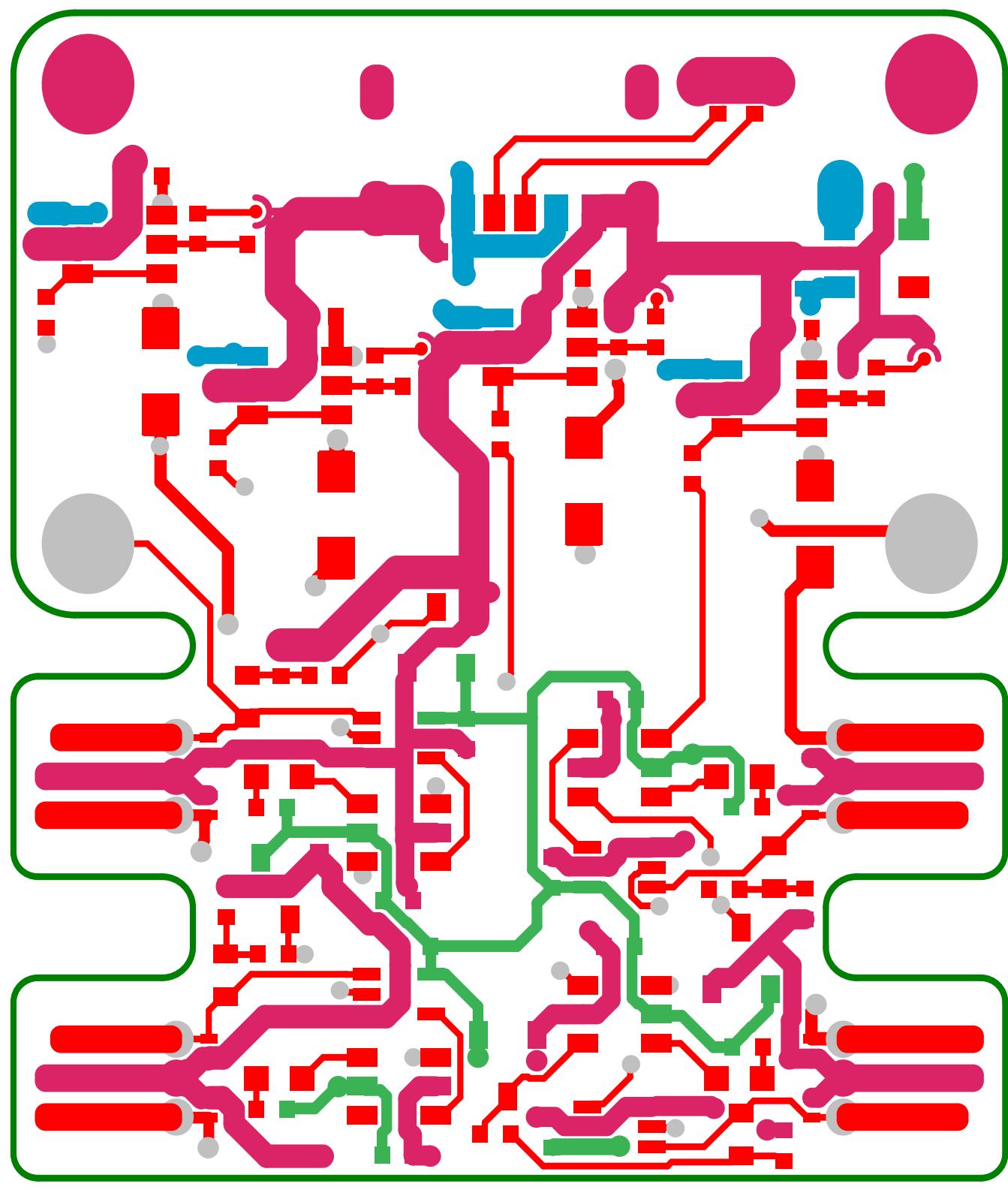


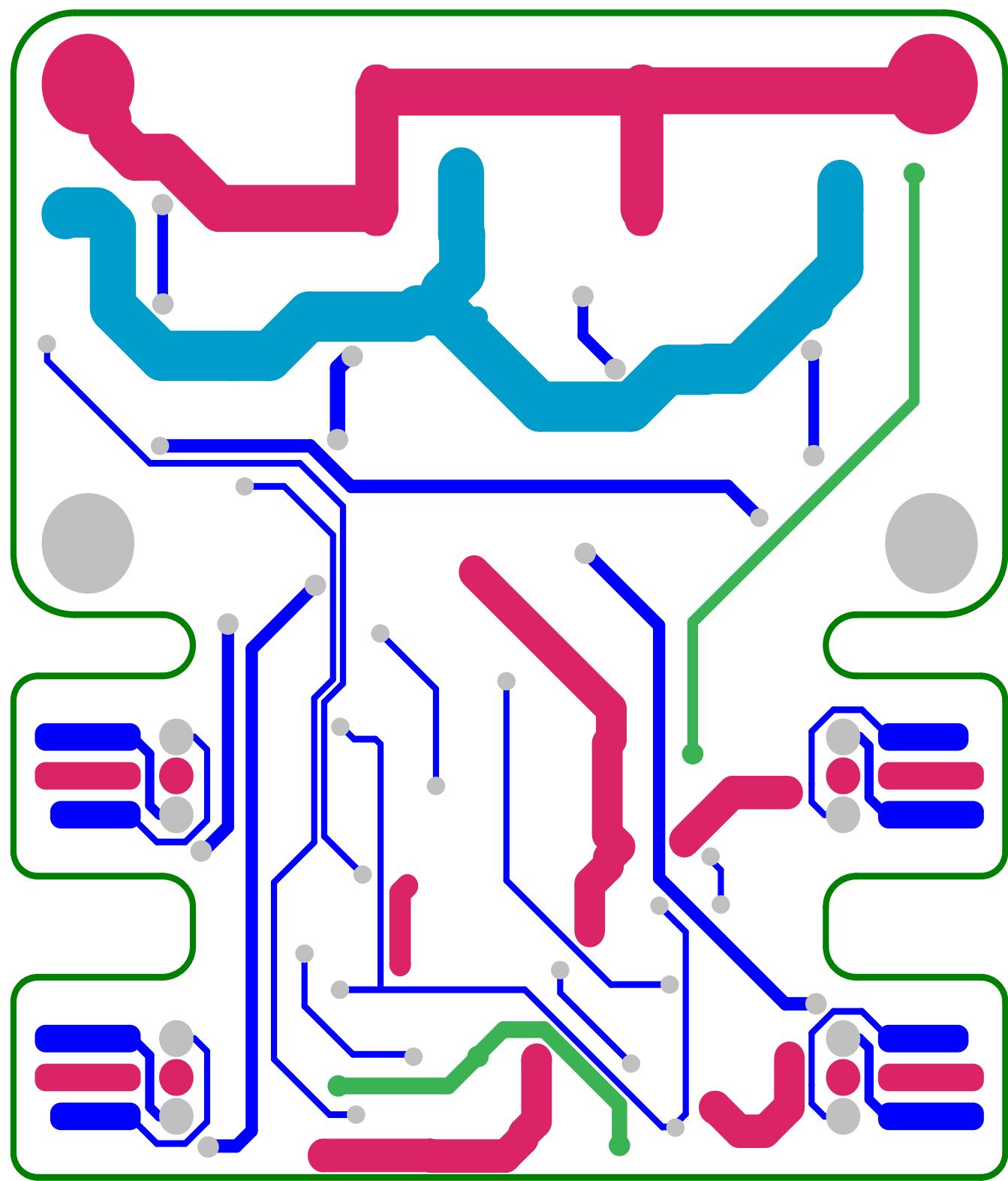


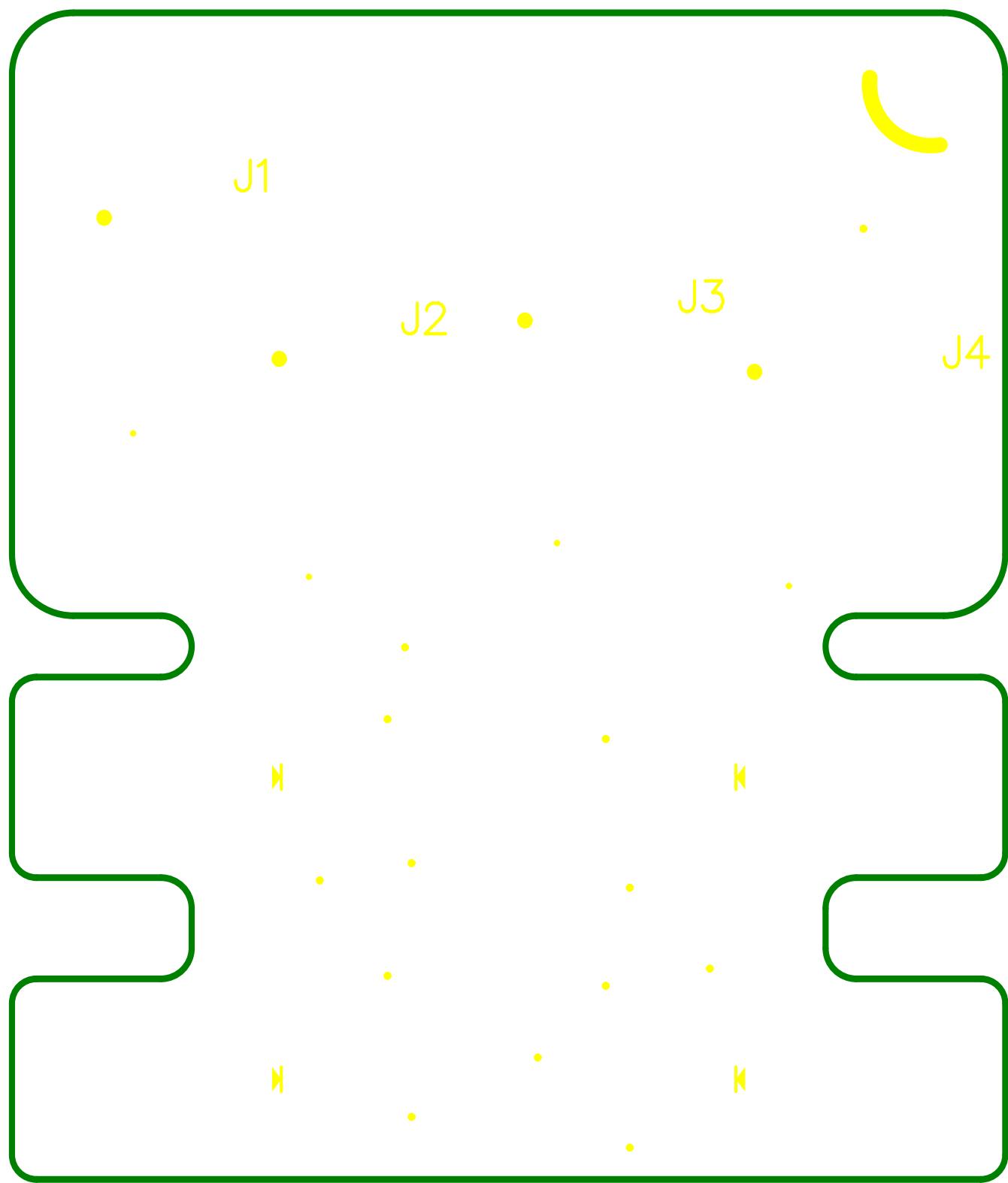














PowerCapcadca
1.0-250

