The opposition softler	s to 0.01% (6.5LS	B) in 1,5us.	
The DC College	Alac to 1010 in 1	0 τ, τ = 10ns, 10 ?= 100 ns,	
The first	TIES 10 0,016 IVI	11 the the USART XOK OF	
ine lastest we can	A CLOCK THE AUC W	with the USART XCK OFF	
on a 10MHZ A	Imegasily is SMHZ		
on a 10MHz A 186 Sampling lasts f	or 16 SCK. 16° 5M	HZ - 3.2 US.	
The only open ques	stion; what effect d	ses ADC sampling cap have	
on soffling time?			
If we lange this	: We already have	TSETTLE 4 16 TSCK i	
\$ - - - - - - - - -			
	15,4 4 32,4	$\Rightarrow f_{cr} = 5MHz$	
A	arbitrary	=> fsch = 5MHZ 5ms (>3.2ms + blan) - + 5ms+	
Anc Colu	3.2/15	APCI converts	
ADC CONV		Sample	
	+ + + + + + + + + + + + + + + + + + + +	-))	
LIS CLK			-
	1	(check	
ADC SCK	14 J	1) A A / Toffor revol	
	sad I	transfer / 16-bits.	
	IDRn	done Read.	
	o start	Time to	
a	transfer	transmit	
ADC SPO	415 414	13 (1 0) over sp	
1100 300	USART SAMPLÉS	Or save	•
	SD0	well, in order to pull to memory	
	10 6 3-1		
5 us high + 5 us low =	7 1 1 1 1 1 1		7
is no problem assumin		already knew the	
enough time to trav		USART transmission	
or write to memory		finished. 6.4m 50	
And assuming capaci	hive loading	(+ ~2,16	wait for
is not a problem	that is the	100KHZ LISCUK	ove to load
purpose of the isola	tion cap		V
after all.		784 pixels × 10ms 784 pix	×2 bylg & tys
			1 Jus
At IMAKUZ: [cano ral - interati	on time + 7 04m and biggs	2 2
/11 1001412. 1	- I I O	on time + 7.84ms readout time +1 21ms +	COANNIL
	- Int, ia. +	4 LIMD	ranmit
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