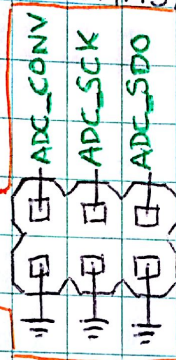
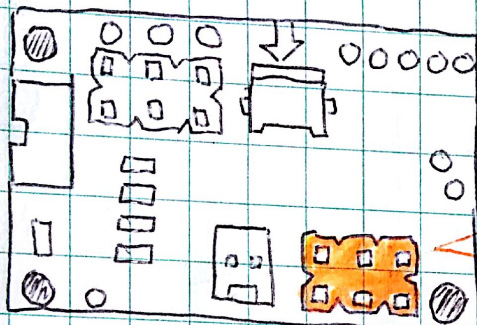


Expected waveforms for ADC-SPI pins SDO, SCK, CONV and how to capture those waveforms.



Atmel Studio
LIS-770i_Interface.atstn
20151020_LIS-770i_mBrd.cproj

module: usart_spi

Check supply rails: 3.3V input and regulated 3.0V.
Check ADC voltage reference: 2.5V

