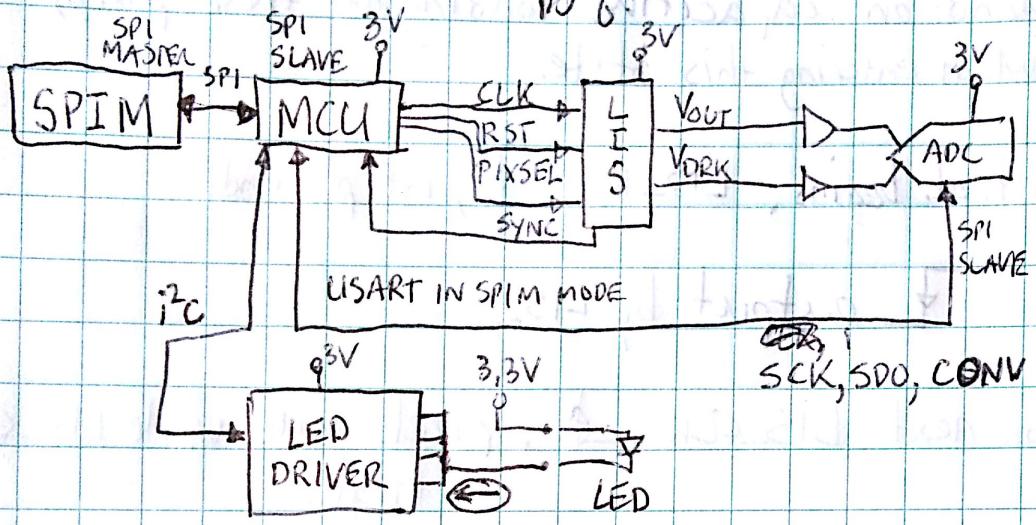
DO 6:




SPIM requests a frame: $\overline{SS}$ ↓, SPIM xmts "REQUEST_FRAME", ~~MCU parses~~,
MCU parses request -- it will pull MISO low when a frame is ready.

MCU runs an integration period on the LIS, ~~and~~ then

Immediately after integration, readout begins. The MCU clocks the ADC to convert.

The frame is buffered. (SRAM: 2048 bytes, one frame = 1568 bytes)

MCU pulls MISO low. SPIM $\overline{SS}$ ↓, MISO is now controlled by SPI hardware.

SPIM reads out the frame.