

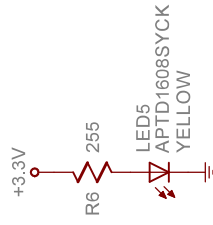
PCB LAYOUT NOTES:

1. Split the ground plane below the voltage regulator to isolate return signals for input and output. See the ground plane layout sketch "LDO voltage regulator ground plane layout.pdf".
2. Pin 1 on the motherboard ZIF maps to pin 8 on the daughterboard ZIF: M1->D8, M2->D7, M3 -> D6, M4 -> D5, etc. See sketch in "LIS-770i dBrd and mBrd cable.pdf"

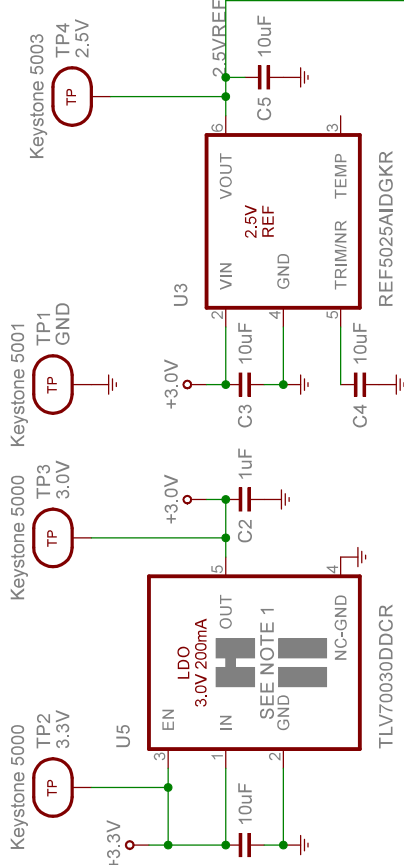
DISCRETE TEST POINT COLOR CODE

RED	= POS. SUPPLY
BLACK	= NEG. SUPPLY
YELLOW	= PIXEL VOLTAGE
WHITE	= DARK VOLTAGE
ORANGE	= 2.5V REFERENCE

POWER INDICATOR LED



VOLTAGE REGULATOR AND REFERENCE



DETECTOR ANALOG INTERFACE

