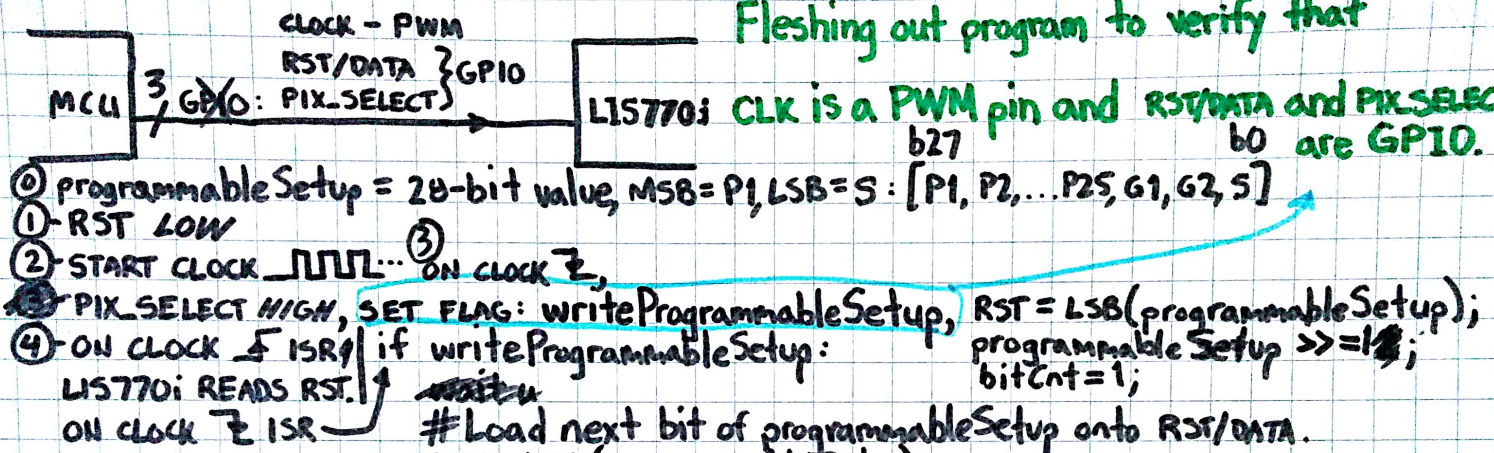
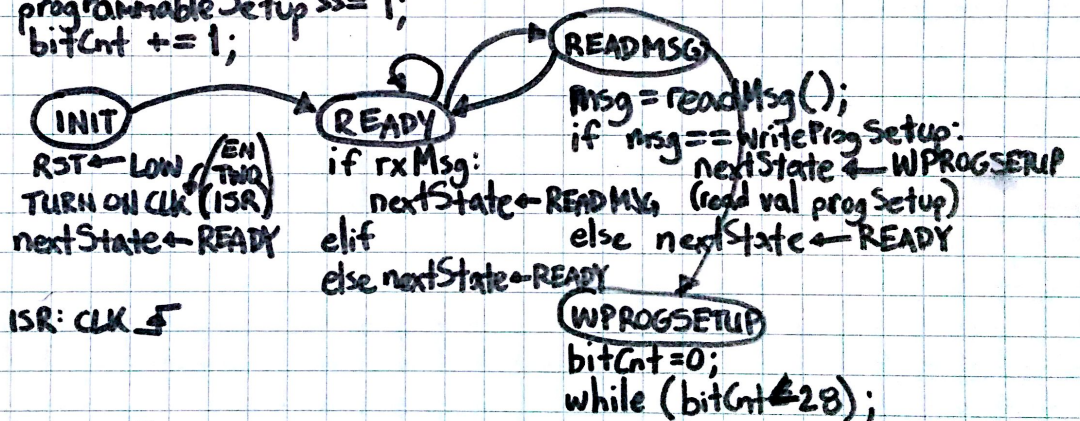


Fleshing out program to verify that

CLK is a PWM pin and RST/DATA and PIX_SELECT are GPIO.



PWROn: RST LOW, CLK ON,
STATE = READY
LOAD progSetup



ISR: CLK

if State == WPROGSETUP:
if bitCnt == 0: PIX_SELECT ← HIGH;
RST = LSB(progSetup);
progSetup >>= 1;
bitCnt += 1;