

Goharian

Automotive and high-speed
System & Hardware Senior
Engineer.

Product Manager

Contact

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Education

1998-2001

BSc in Electric and Electronic
Engineering - Ariel University.

2012-2014

MBA master's degree in
business administration - Open
University.

Army Service

A communication officer, at the
rank of captain, is responsible
for intelligence communication
systems

Languages

Hebrew - mother tongue
English - fluent

Profile

My skills are automotive hardware design, high-speed board design, and hardware product management.
Multi-disciplinary project development and management of communication, high speed, and automotive projects.

Professional Experience

Aug 2019 – Present



Senior Automotive Hardware Engineer (3 years) at Mobileye Vision

Senior Automotive Hardware Engineer and Hardware Product Manager
Autonomous Vehicle (AV) hardware product board design, high speed and power design, SI and PI Hyperlynx simulations, plan DV&PV and engineering tests.

REM and ADAS Camera Projects Manager (2 years)

Projects Manager (some projects in parallel)
I managed all project aspects, hardware, software, validation, mechanic, engineering, move from development to production.
The projects support company data collection, map building for autonomous vehicles. See project clip: <https://youtu.be/2iZFQ-vCm48>

Hardware Team Manager (9 years)

Hardware Board Design and System team leader.
Our team developed After Market device, high speed PCB design, full system development from architecture, development (board design, layout, DV and PV tests), production and testers support, final mass production line support.

Feb 2006 – Jun 2010



Hardware Board Design Team Leader at Intel

Technical leader, High-Speed Board and FPGA design, Communications Components Development Division into the Desktop, Laptop and Server.

HDL Logic FPGA Design, based on Verilog. FPGA controlled the ASIC operation, communication, and power up.

Dec 1999 – Jan 2006



Hardware and FPGA Engineer at Cisco (formerly NDS)

Hardware Engineer, which develops hardware and software solutions in the field of pay-TV broadcasts, with Smart Cards.
Development of digital circuits (Board Design) multi-layers at high rates, containing FPGA components and FPGA HDL design.

Apr 1998 – Dec 1999 **Hardware Designer** at Visionix



