Midong Zhou

Electrical and Computer Engineering

Dallas, TX | 972.369.3343 | Mzhou2018@tamu.edu | /in/Midong-Zhou

Education

Texas A&M University – College Station, TX
Exp. 2024
Bachelor of Science – Electrical and Computer Engineering

Core Competencies

Specialization	Technical		Languages	Equipment
Electrical Circuit Design	FPGA Board	Arduino	C++	Multimeter
LIN Bus Communications	MATLAB	AutoCAD	Python 3	Oscilloscope
Programming	Xilinix Vivado	Microsoft Office	Verilog	DC Source

Work Experience

Valeo – Dallas, TX August 2022 – January 2023
Electrical Engineering Intern

Led project on updating its belt driven compressor test bench to be capable of testing the electrical driven compressor (EDC), part of Valeo's evolving compressor electrification objective

- Sized and sourced a DC power supply and configuring a testing bench
- Specified and sourced various electrical test equipment setup troubleshooting.
- Trained warranty technicians to use new test equipment and performance electrical function checks

Conducted EDC warranty return inspection (testing for resistance, impedance, capacitance, insulation, etc.)
Configure EDC controller with the LIN/CAN-bus interface.
Supported Valeo warranty team on its customer annual joint warranty inspection, including updating work instructions, participating in compressor teardown, bench testing, and report preparation.
handling customer EDC inquiries, system setups, and troubleshooting

Texas A&M Department of IT – College Station, TX July 2021 – January 2022
Student IT Technician

Communicated with customers, scheduled appointments, and managed deployments for their IT service needs.
Diagnosed and solved computer hardware, printer, and peripheral device problems.
Reimaged laptops, updated licensed software packages, managed Outlook profiles, and set up printers/devices.
Provided routine maintenance for university devices with software installations and hardware upgrades.

Academic Projects

Electrical Analog Circuit Design and Analysis
Fall 2021
Designed and prototyped an electronic security system to detect interruptions in a light beam and prompt a response.
Used opAmps to magnify an input signal, compare it to a reference, and trigger an indicator.
Built, operated, and simulated Electrical circuits: amplifiers, comparators, filters, etc.

Digital Circuit Design and Simulation Spring 2021 – Spring 2022
Built single cycle processor including the register file, sign extender, ALU module, data memory, and PC logic in Verilog.
Created a State Diagram of a Mealy machine that implemented a controller, Verilog counter, and the finite state machine.
Simulated a traffic light cross section on a Zybo board by designing a Verilog programmed controller based on a state diagram.

Debugged logic gates, Boolean algebra, and combinational logic via Zybo Field Programmable Gate Array (FPGA) board.

Organizations

Aggie Coding Club Fall 2019-Spring 2021

Competed and participated in various events that challenge C++ and Python programming skills.

Code a “dungeon crawler” game in C++ in which users input move commands to get to a treasure and earn a high score.

Formed a study group for members to collaborated on various coding problems and class assignments.