CPE301 – SPRING 2019

Design Assignment 2C

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Primary Github address: https://github.com/miggnuggets/submissions.git

Directory: Repository\_301

1. **COMPONENTS LIST AND CONNECTION BLOCK DIAGRAM w/ PINS**

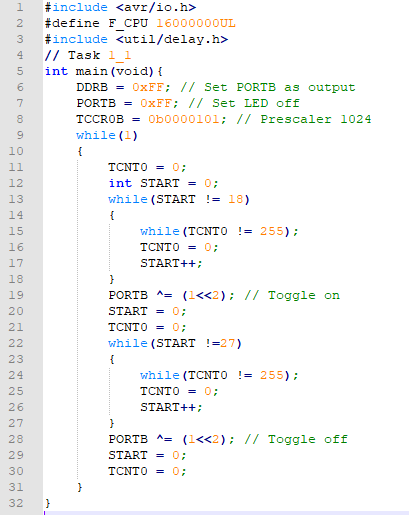
List of Components used

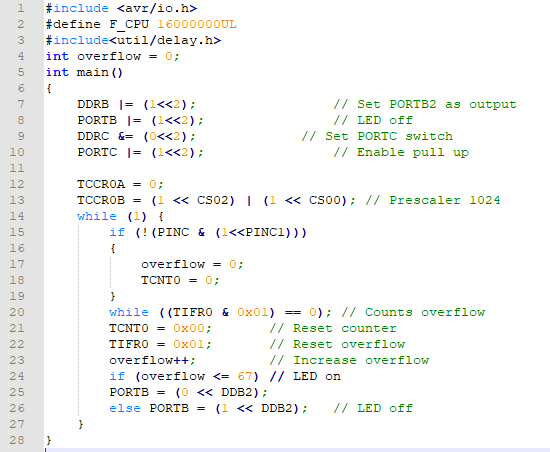
Block diagram with pins used in the Atmega328P

1. **INITIAL/MODIFIED/DEVELOPED CODE OF TASK 1/A**

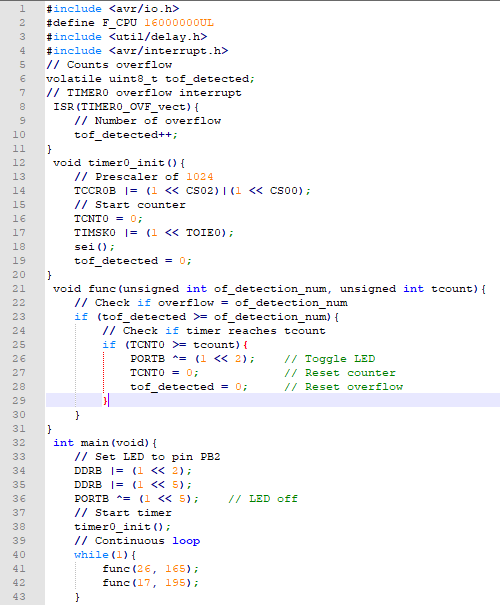
**ALL CODE IN NOTEPAD SO ITS CLEANER**

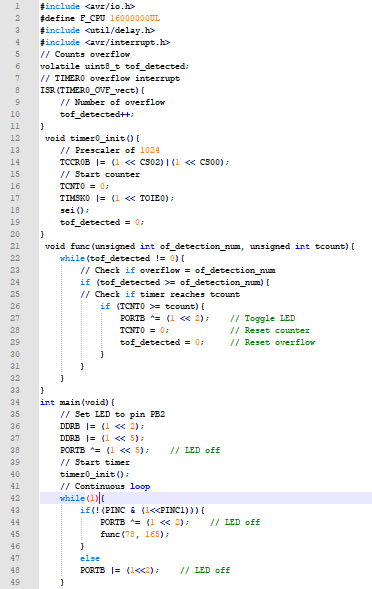
Task 1 Part 1 and 2 – Normal mode



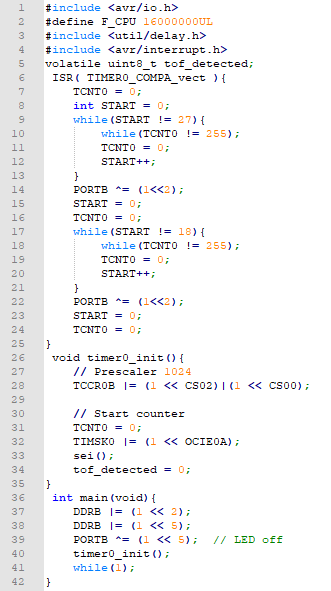


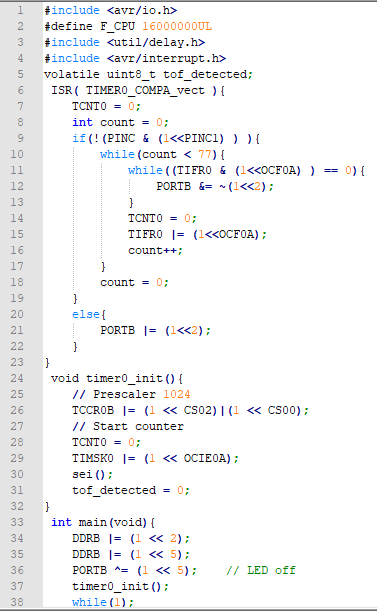
Task 2 Part 1 and 2 – OVF



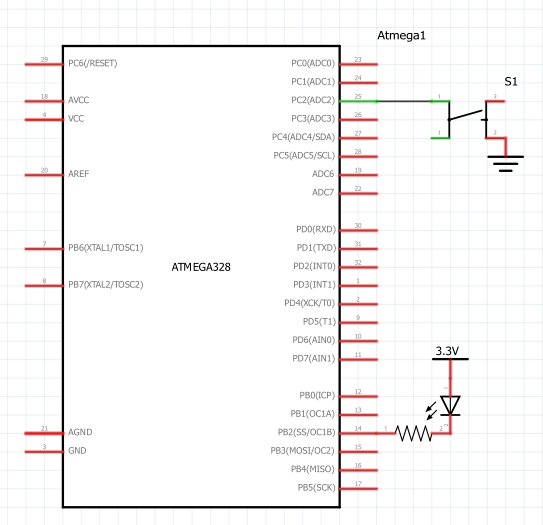


Task 3 Part 1 and 2 - COMPA





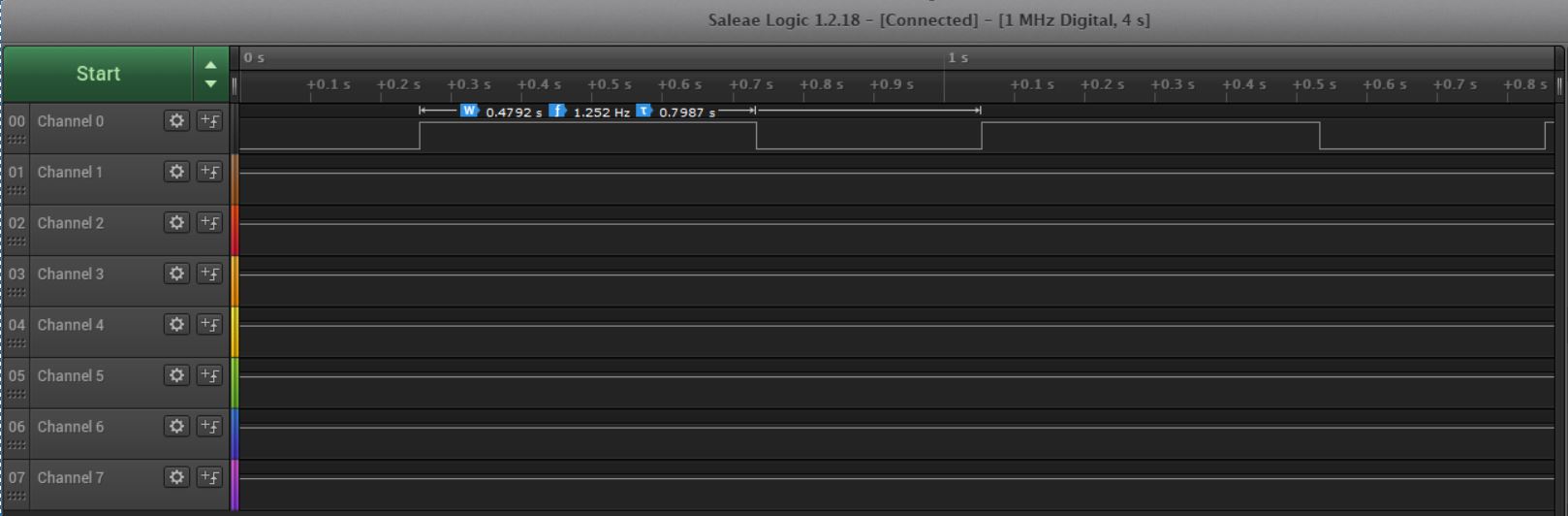
1. **SCHEMATICS**

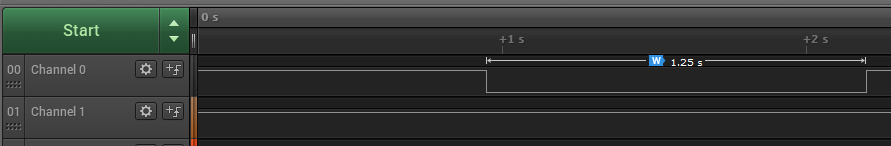


Use fritzing.org

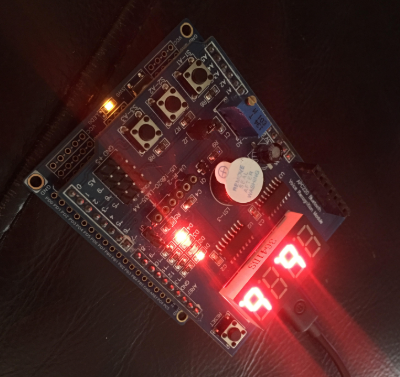
1. **SCREENSHOTS OF EACH TASK OUTPUT (ATMEL STUDIO OUTPUT)**

Task 1,2 and 3 Part 1



Task 1,2 and 3 Part 2

1. **SCREENSHOT OF EACH DEMO (BOARD SETUP)**



1. **VIDEO LINKS OF EACH DEMO**

<https://youtu.be/NKgvdfXaNdQ>

<https://youtu.be/EfHb-L_5Sk4>

1. **GITHUB LINK OF THIS DA**

**https://github.com/miggnuggets/submissions.git**

**Student Academic Misconduct Policy**

<http://studentconduct.unlv.edu/misconduct/policy.html>

“This assignment submission is my own, original work”.

Michael Johnson