S5D9 Lab Watch Dog Timer WDT

By
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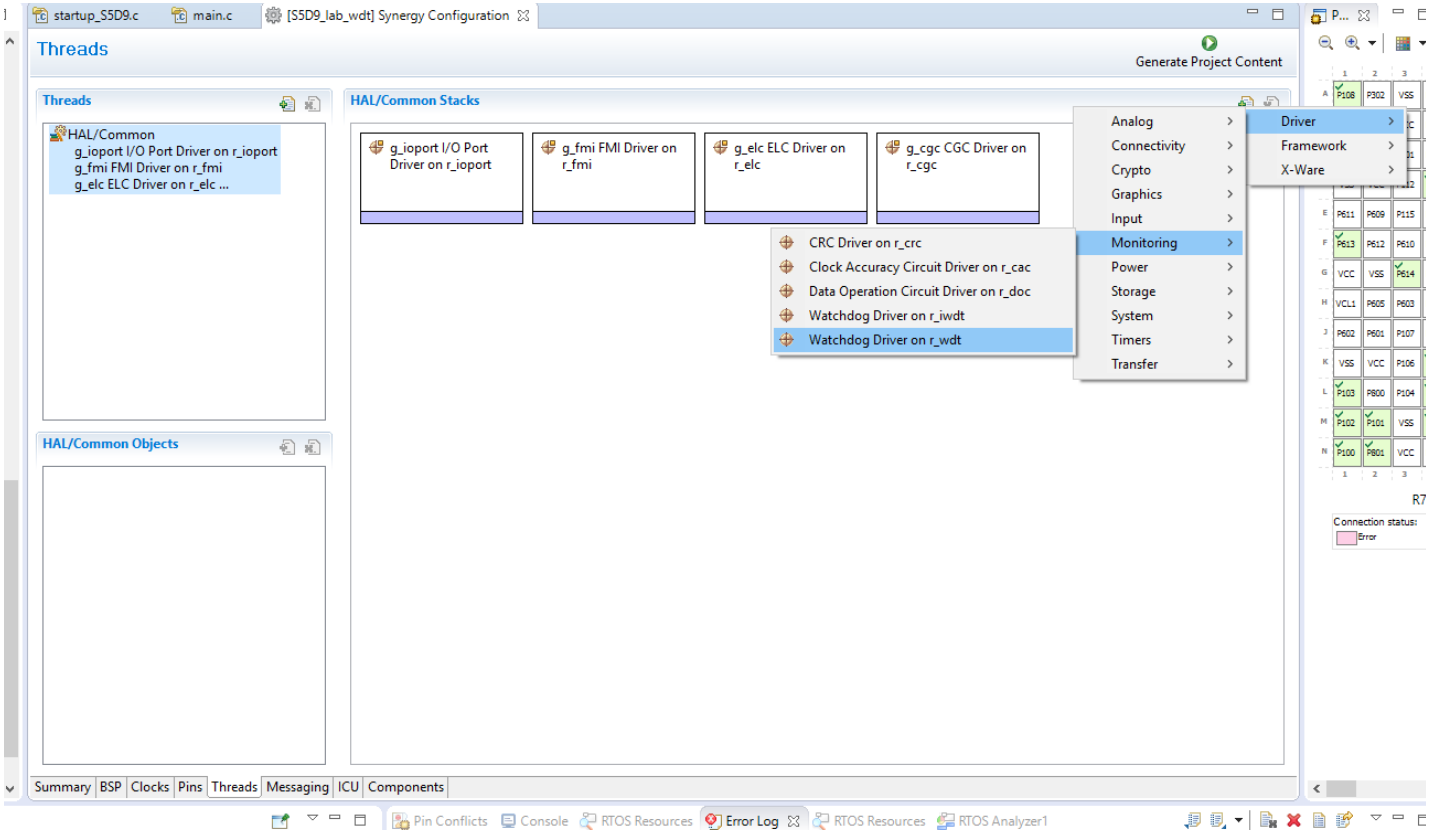
(2/5/2018)
https://www.miketechuniverse.com

E2 Studio 5.4.0.023
SSP 1.3.0

WDT Driver


startup_S5D9.c
main.c
[S5D9_lab_wdt] Synergy Configuration
Threads
Generate Project Content
Threads
HAL/Common
g_ioport I/O Port Driver on r_ioport
g_fmi FMI Driver on r_fmi
g_elc ELC Driver on r_elc ...
HAL/Common Objects
HAL/Common Stacks
g_ioport I/O Port Driver on r_ioport
g_fmi FMI Driver on r_fmi
g_elc ELC Driver on r_elc
g_cgc CGC Driver on r_cgc
Analog
Connectivity
Crypto
Graphics
Input
Monitoring
Power
Storage
System
Timers
Transfer
Driver
Framework
X-Ware
CRC Driver on r_crc
Clock Accuracy Circuit Driver on r_cac
Data Operation Circuit Driver on r_doc
Watchdog Driver on r_iwdt
Watchdog Driver on r_wdt
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Clocks
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Threads
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ICU
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Pin Conflicts
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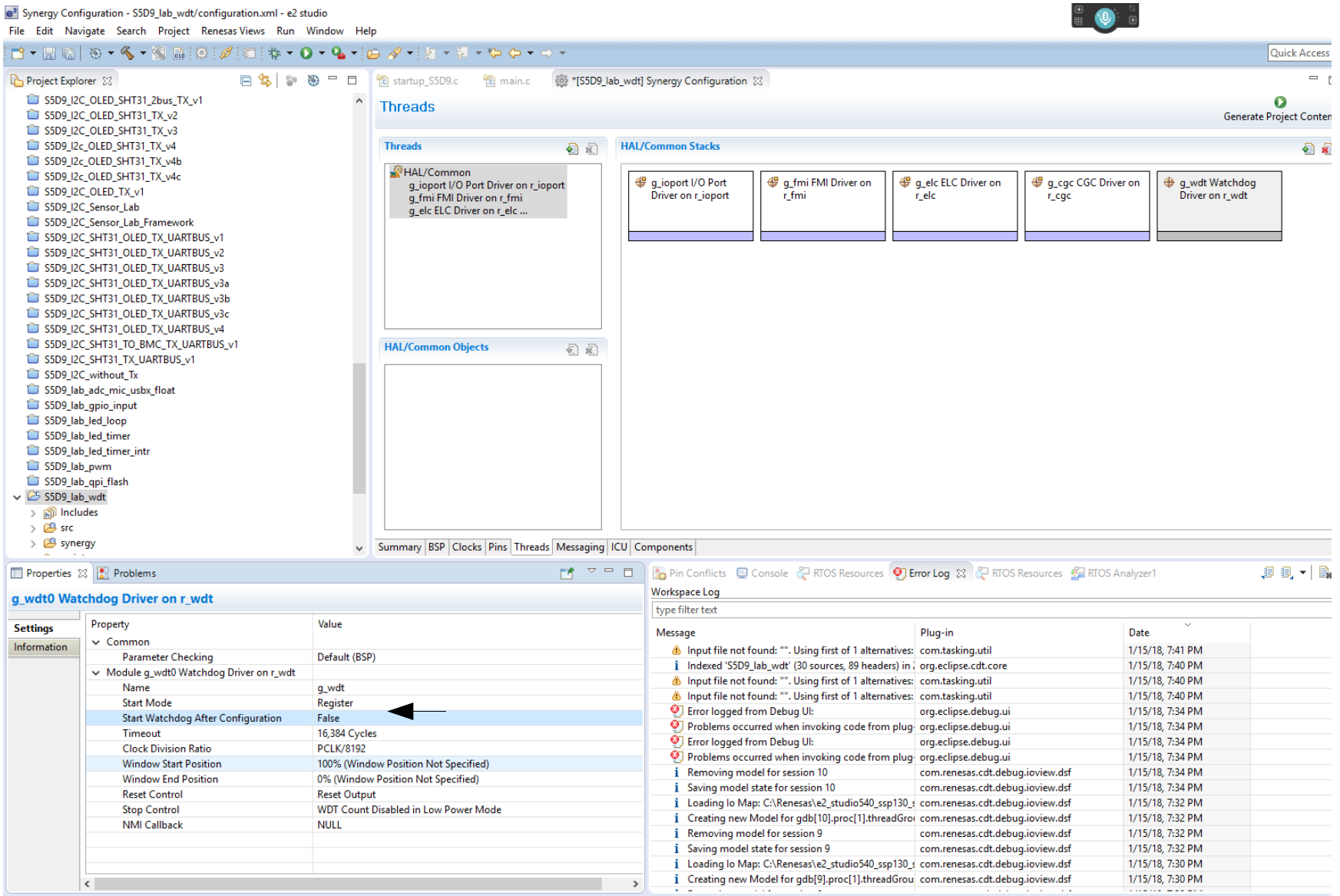
RTOS Resources
Error Log
RTOS Resources
RTOS Analyzer1

Properties


Synergy Configuration - S5D9_lab_wdt/configuration.xml - e2 studio
File Edit Navigate Search Project Renesas Views Run Window Help
Project Explorer
S5D9_I2C_OLED_SHT31_2bus_TX_v1
S5D9_I2C_OLED_SHT31_TX_v2
S5D9_I2C_OLED_SHT31_TX_v3
S5D9_I2c_OLED_SHT31_TX_v4
S5D9_I2c_OLED_SHT31_TX_v4b
S5D9_I2c_OLED_SHT31_TX_v4c
S5D9_I2C_OLED_TX_v1
S5D9_I2C_Sensor_Lab
S5D9_I2C_Sensor_Lab_Framework
S5D9_I2C_SHT31_OLED_TX_UARTBUS_v1
S5D9_I2C_SHT31_OLED_TX_UARTBUS_v2
S5D9_I2C_SHT31_OLED_TX_UARTBUS_v3
S5D9_I2C_SHT31_OLED_TX_UARTBUS_v3a
S5D9_I2C_SHT31_OLED_TX_UARTBUS_v3b
S5D9_I2C_SHT31_OLED_TX_UARTBUS_v3c
S5D9_I2C_SHT31_OLED_TX_UARTBUS_v4
S5D9_I2C_SHT31_TO_BMC_TX_UARTBUS_v1
S5D9_I2C_SHT31_TX_UARTBUS_v1
S5D9_I2C_without_Tx
S5D9_lab_adc_mic_usbx_float
S5D9_lab_gpio_input
S5D9_lab_led_loop
S5D9_lab_led_timer
S5D9_lab_led_timer_intr
S5D9_lab_pwm
S5D9_lab_qpi_flash
S5D9_lab_wdt
Includes
src
synergy
startup_S5D9.c
main.c
*[S5D9_lab_wdt] Synergy Configuration
Threads
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HAL/Common
g_ioport I/O Port Driver on r_ioport
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g_elc ELC Driver on r_elc
g_cgc CGC Driver on r_cgc
g_wdt Watchdog Driver on r_wdt
Summary BSP Clocks Pins Threads Messaging ICU Components
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g_wdt0 Watchdog Driver on r_wdt
Settings
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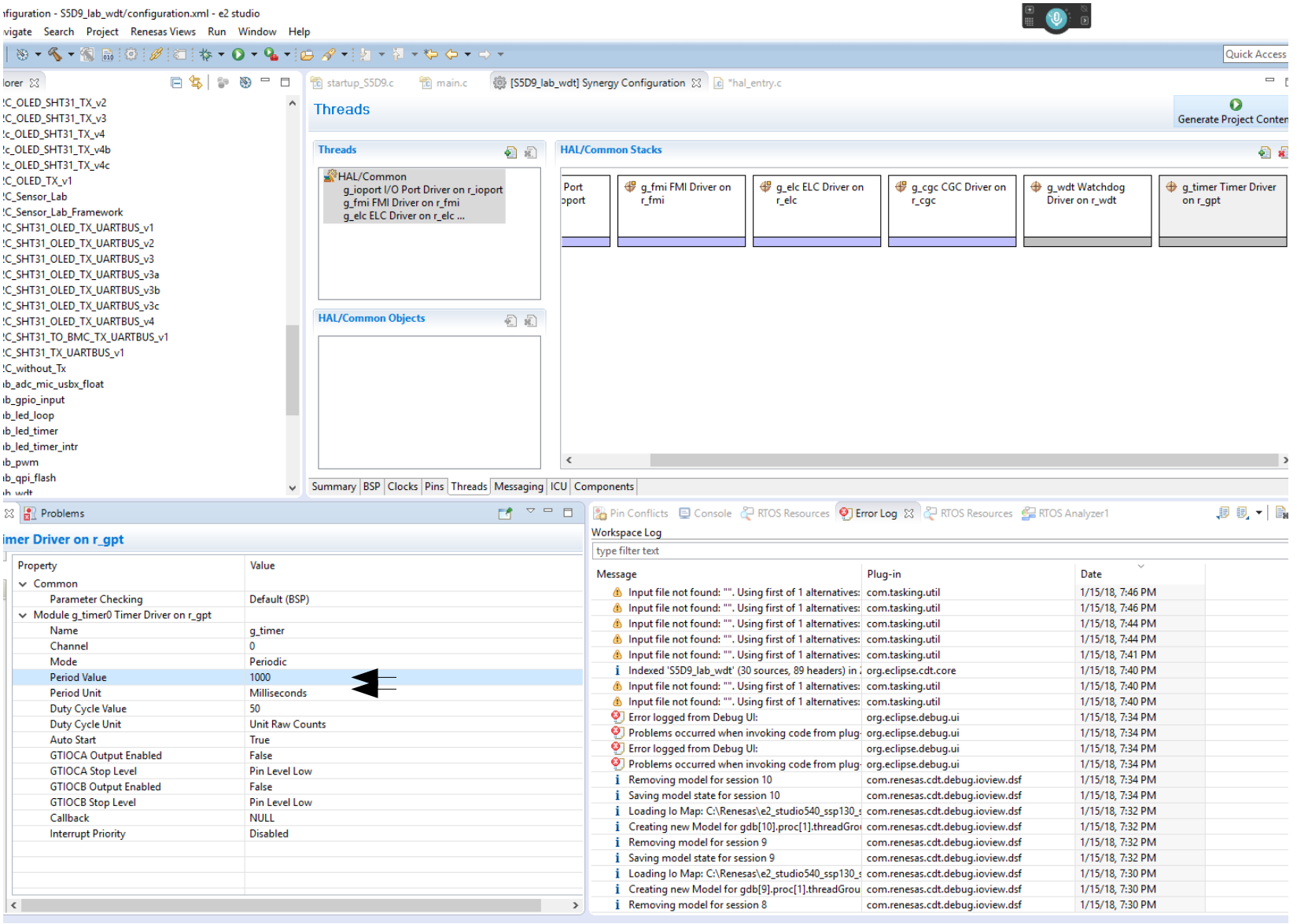
Value
Common
Parameter Checking
Default (BSP)
Module g_wdt0 Watchdog Driver on r_wdt
Name
g_wdt
Start Mode
Register
Start Watchdog After Configuration
False
Timeout
16,384 Cycles
Clock Division Ratio
PCLK/8192
Window Start Position
100% (Window Position Not Specified)
Window End Position
0% (Window Position Not Specified)
Reset Control
Reset Output
Stop Control
WDT Count Disabled in Low Power Mode
NMI Callback
NULL
Pin Conflicts
Console
RTOS Resources
Error Log
RTOS Resources
RTOS Analyzer1
Workspace Log
type filter text
Message
Plug-in
Date
Input file not found: "". Using first of 1 alternatives:
com.tasking.util
1/15/18, 7:41 PM
Indexed 'S5D9_lab_wdt' (30 sources, 89 headers) in
org.eclipse.cdt.core
1/15/18, 7:40 PM
Input file not found: "". Using first of 1 alternatives:
com.tasking.util
1/15/18, 7:40 PM
Input file not found: "". Using first of 1 alternatives:
com.tasking.util
1/15/18, 7:40 PM
Error logged from Debug UI:
org.eclipse.debug.ui
1/15/18, 7:34 PM
Problems occurred when invoking code from plug-
org.eclipse.debug.ui
1/15/18, 7:34 PM
Error logged from Debug UI:
org.eclipse.debug.ui
1/15/18, 7:34 PM
Problems occurred when invoking code from plug-
org.eclipse.debug.ui
1/15/18, 7:34 PM
Removing model for session 10
com.renesas.cdt.debug.ioview.dsf
1/15/18, 7:34 PM
Saving model state for session 10
com.renesas.cdt.debug.ioview.dsf
1/15/18, 7:34 PM
Loading Io Map: C:\Renesas\e2_studio540_ssp130_
com.renesas.cdt.debug.ioview.dsf
1/15/18, 7:32 PM
Creating new Model for gdb[10].proc[1].threadGro
com.renesas.cdt.debug.ioview.dsf
1/15/18, 7:32 PM
Removing model for session 9
com.renesas.cdt.debug.ioview.dsf
1/15/18, 7:32 PM
Saving model state for session 9
com.renesas.cdt.debug.ioview.dsf
1/15/18, 7:32 PM
Loading Io Map: C:\Renesas\e2_studio540_ssp130_
com.renesas.cdt.debug.ioview.dsf
1/15/18, 7:30 PM
Creating new Model for gdb[9].proc[1].threadGrou
com.renesas.cdt.debug.ioview.dsf
1/15/18, 7:30 PM

Add a 32 bit timer to refresh WDT



Main Code Description

For testing, load the firmware into the ROM. Then stop e2 studio debug mode. Turn power off/on.

WDT will not be enabled if you use the e2studio debug mode.