*Preferred Devices

Amplifier Transistors

Voltage and Current are Negative for PNP Transistors

Features

• Pb-Free Packages are Available*

MAXIMUM RATINGS

Rating	Symbol	Value	Unit
Collector – Emitter Voltage MPSA05, MPSA55 MPSA06, MPSA56	V _{CEO}	60 80	Vdc
Collector – Base Voltage MPSA05, MPSA55 MPSA06, MPSA56	V _{CBO}	60 80	Vdc
Emitter-Base Voltage	V _{EBO}	4.0	Vdc
Collector Current – Continuous	Ic	500	mAdc
Total Device Dissipation @ T _A = 25°C Derate above 25°C	P _D	625 5.0	W mW/°C
Total Device Dissipation @ T _C = 25°C Derate above 25°C	P _D	1.5 12	W mW/°C
Operating and Storage Junction Temperature Range	T _J , T _{stg}	-55 to +150	°C

THERMAL CHARACTERISTICS

Characteristic	Symbol	Max	Unit
Thermal Resistance, Junction-to-Ambient (Note 1)	$R_{\theta JA}$	200	°C/W
Thermal Resistance, Junction-to-Case	$R_{\theta JC}$	83.3	°C/W

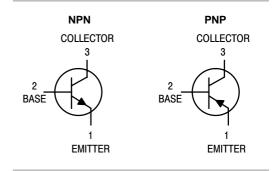
Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.

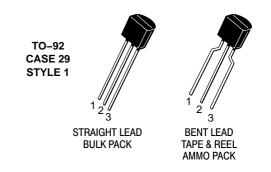
1. $R_{\theta JA}$ is measured with the device soldered into a typical printed circuit board.



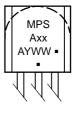
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MARKING DIAGRAM



xx = 05, 06, 55, or 56 A = Assembly Location

Y = Year

WW = Work Week

■ = Pb-Free Package

(Note: Microdot may be in either location)

ORDERING INFORMATION

See detailed ordering and shipping information in the package dimensions section on page 6 of this data sheet.

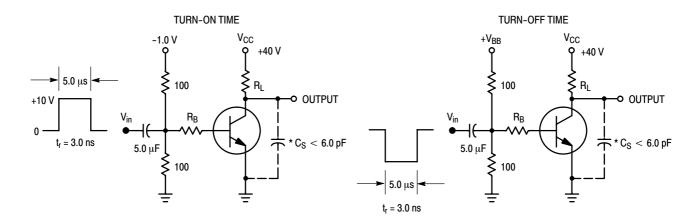
Preferred devices are recommended choices for future use and best overall value.

^{*}For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

ELECTRICAL CHARACTERISTICS (T_A = 25°C unless otherwise noted)

Characteristic		Symbol	Min	Max	Unit
OFF CHARACTERISTICS			1		I
Collector – Emitter Breakdown Voltage (Note 2) (I _C = 1.0 mAdc, I _B = 0)	MPSA05, MPSA55 MPSA06, MPSA56	V _{(BR)CEO}	60 80	- -	Vdc
Emitter – Base Breakdown Voltage ($I_E = 100 \mu Adc, I_C = 0$)		V _{(BR)EBO}	4.0	_	Vdc
Collector Cutoff Current (V _{CE} = 60 Vdc, I _B = 0)		I _{CES}	-	0.1	μAdc
Collector Cutoff Current $(V_{CB} = 60 \text{ Vdc}, I_E = 0)$ $(V_{CB} = 80 \text{ Vdc}, I_E = 0)$	MPSA05, MPSA55 MPSA06, MPSA56	Ісво	- -	0.1 0.1	μAdc
ON CHARACTERISTICS			•		
DC Current Gain (I _C = 10 mAdc, V_{CE} = 1.0 Vdc) (I _C = 100 mAdc, V_{CE} = 1.0 Vdc)		h _{FE}	100 100	- -	-
Collector – Emitter Saturation Voltage (I _C = 100 mAdc, I _B = 10 mAdc)		V _{CE(sat)}	-	0.25	Vdc
Base–Emitter On Voltage (I _C = 100 mAdc, V _{CE} = 1.0 Vdc)		V _{BE(on)}	-	1.2	Vdc
SMALL-SIGNAL CHARACTERISTICS			•	•	
Current – Gain – Bandwidth Product (Note 3) (I _C = 10 mA, V _{CE} = 2.0 V, f = 100 MHz)	MPSA05 MPSA06	f _T	100	_	MHz
$(I_C = 100 \text{ mAdc}, V_{CE} = 1.0 \text{ Vdc}, f = 100 \text{ MHz})$	MPSA55 MPSA56		50	_	

^{2.} Pulse Test: Pulse Width $\leq 300~\mu s$, Duty Cycle $\leq 2\%$. 3. f_T is defined as the frequency at which $|h_{fe}|$ extrapolates to unity.



^{*}Total Shunt Capacitance of Test Jig and Connectors For PNP Test Circuits, Reverse All Voltage Polarities

Figure 1. Switching Time Test Circuits

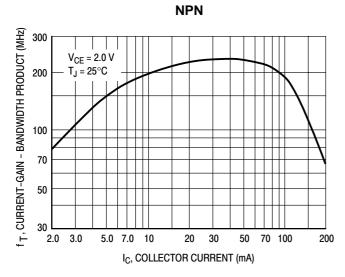


Figure 2. MPSA05/06 Current-Gain — Bandwidth Product

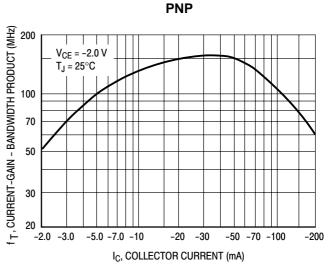


Figure 3. MPSA55/56 Current-Gain — Bandwidth Product

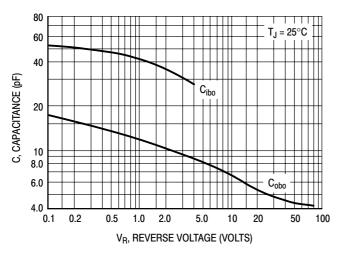


Figure 4. MPSA05/06 Capacitance

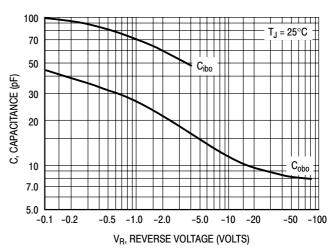


Figure 5. MPSA55/56 Capacitance

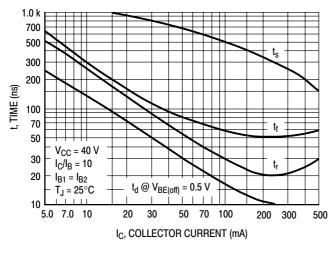


Figure 6. MPSA05/06 Switching Time

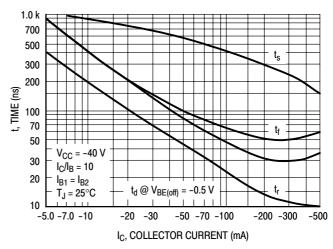
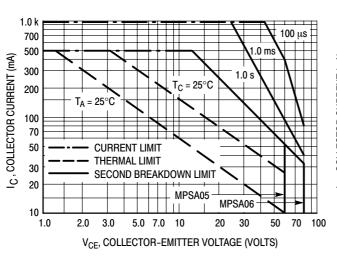


Figure 7. MPSA55/56 Switching Time



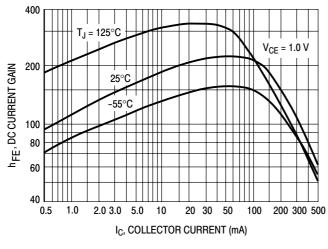
NPN

-1.0 k 100 μs -700 -500 IC, COLLECTOR CURRENT (mA) -300 .0 s $T_C = 25^{\circ}C$ -200 -100 -70 -50 **CURRENT LIMIT** THERMAL LIMIT -30 SECOND BREAKDOWN LIMIT -20 MPSA55 MPSA56 -10 -5.0 -7.0 -10 -1.0V_{CE}, COLLECTOR-EMITTER VOLTAGE (VOLTS)

PNP

Figure 8. MPSA05/06 Active-Region Safe Operating Area

Figure 9. MPSA55/56 Active-Region Safe Operating Area



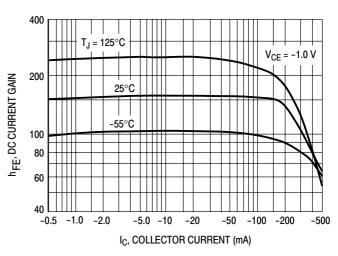
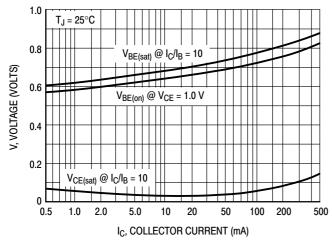


Figure 10. MPSA05/06 DC Current Gain

Figure 11. MPSA55/56 DC Current Gain



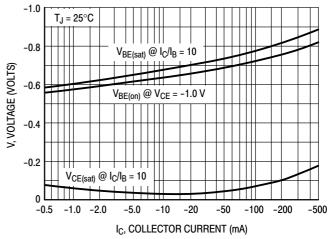
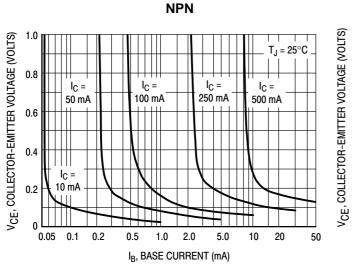


Figure 12. MPSA05/06 "ON" Voltages

Figure 13. MPSA55/56 "ON" Voltages

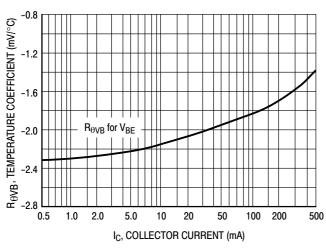


-1.0 $T_J = 25^{\circ}C$ -0.8 I_C = I_C = I_C = I_C = -250 mA -50 mA 100 mA 500 mA -0.6 -0.4 10 mA -0.2 0 -0.05 -0.1 -0.2 -0.5 -2.0 -5.0 -10 -20 -1.0 -50 IB, BASE CURRENT (mA)

PNP

Figure 14. MPSA05/06 Collector Saturation Region

Figure 15. MPSA55/56 Collector Saturation Region



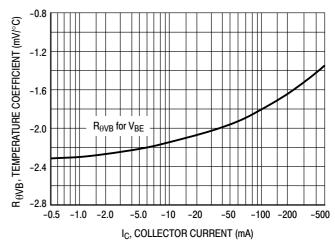


Figure 16. MPSA05/06 Base–Emitter Temperature Coefficient

Figure 17. MPSA55/56 Base–Emitter Temperature Coefficient

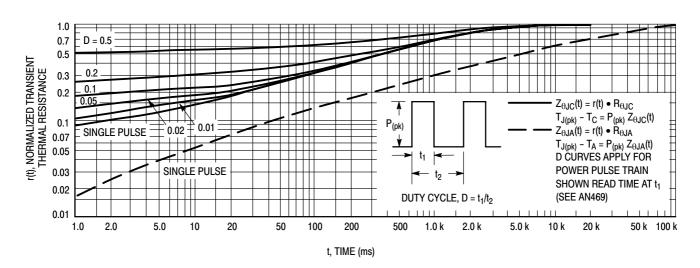


Figure 18. MPSA05, MPSA06, MPSA55 and MPSA56 Thermal Response

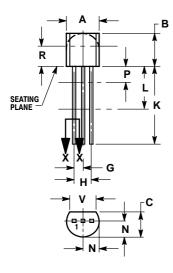
ORDERING INFORMATION

Device	Package	Shipping [†]
MPSA05	TO-92	5000 Units / Bulk
MPSA05G	TO-92 (Pb-Free)	5000 Units / Bulk
MPSA05RLRA	TO-92	2000 / Tape & Reel
MPSA05RLRAG	TO-92 (Pb-Free)	2000 / Tape & Reel
MPSA05RLRM	TO-92	2000 / Ammo Pack
MPSA05RLRMG	TO-92 (Pb-Free)	2000 / Ammo Pack
MPSA06	TO-92	5000 Units / Bulk
MPSA06G	TO-92 (Pb-Free)	5000 Units / Bulk
MPSA06RL1G	TO-92 (Pb-Free)	2000 / Tape & Reel
MPSA06RLG	TO-92 (Pb-Free)	2000 / Tape & Reel
MPSA06RLRA	TO-92	2000 / Tape & Reel
MPSA06RLRAG	TO-92 (Pb-Free)	2000 / Tape & Reel
MPSA06RLRM	TO-92	2000 / Ammo Pack
MPSA06RLRMG	TO-92 (Pb-Free)	2000 / Ammo Pack
MPSA06RLRP	TO-92	2000 / Ammo Pack
MPSA06RLRPG	TO-92 (Pb-Free)	2000 / Ammo Pack
MPSA55G	TO-92 (Pb-Free)	5000 Units / Bulk
MPSA55RLRA	TO-92	2000 / Tape & Reel
MPSA55RLRAG	TO-92 (Pb-Free)	2000 / Tape & Reel
MPSA56	TO-92	5000 Units / Bulk
MPSA56G	TO-92 (Pb-Free)	5000 Units / Bulk
MPSA56RLRA	TO-92	2000 / Tape & Reel
MPSA56RLRAG	TO-92 (Pb-Free)	2000 / Tape & Reel
MPSA56RLRM	TO-92	2000 / Ammo Pack
MPSA56RLRMG	TO-92 (Pb-Free)	2000 / Ammo Pack
MPSA56RLRP	TO-92	2000 / Ammo Pack
MPSA56RLRPG	TO-92 (Pb-Free)	2000 / Ammo Pack
MPSA56ZL1	TO-92	2000 / Ammo Pack
MPSA56ZL1G	TO-92 (Pb-Free)	2000 / Ammo Pack

[†]For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specification Brochure, BRD8011/D.

PACKAGE DIMENSIONS

TO-92 (TO-226) CASE 29-11 **ISSUE AM**

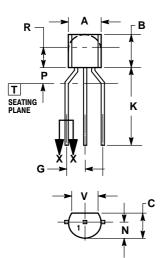


STRAIGHT LEAD **BULK PACK**



- 1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982. CONTROLLING DIMENSION: INCH.
- CONTOUR OF PACKAGE BEYOND DIMENSION R IS UNCONTROLLED
- LEAD DIMENSION IS UNCONTROLLED IN P AND BEYOND DIMENSION K MINIMUM.

	INCHES		MILLIN	IETERS
DIM	MIN	MAX	MIN	MAX
Α	0.175	0.205	4.45	5.20
В	0.170	0.210	4.32	5.33
С	0.125	0.165	3.18	4.19
D	0.016	0.021	0.407	0.533
G	0.045	0.055	1.15	1.39
Н	0.095	0.105	2.42	2.66
J	0.015	0.020	0.39	0.50
K	0.500		12.70	
L	0.250		6.35	
N	0.080	0.105	2.04	2.66
P		0.100		2.54
R	0.115		2.93	
V	0 135		3 43	



BENT LEAD TAPE & REEL AMMO PACK



NOTES:

- DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 1994.
- CONTROLLING DIMENSION: MILLIMETERS.
 CONTOUR OF PACKAGE BEYOND
- DIMENSION R IS UNCONTROLLED
- LEAD DIMENSION IS UNCONTROLLED IN PAND BEYOND DIMENSION K MINIMUM.

	MILLIMETERS		
DIM	MIN MAX		
Α	4.45	5.20	
В	4.32	5.33	
С	3.18	4.19	
D	0.40	0.54	
G	2.40	2.80	
J	0.39	0.50	
K	12.70		
N	2.04	2.66	
Р	1.50	4.00	
R	2.93		
٧	3.43		

PIN 1 FMITTER

BASE

COLLECTOR

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