

# Semester Project Report

Your Name

March 7, 2024

# Notes

- Chiplicity = efabless.com methodology for building up complete chips ready for fabrication from component parts pulled from the Efabless IP catalog
- 2 types of IP = Clone Hard IP (open-source), Try Hard IP (proprietary)
- The virtual desktop already provides all tools required for design and verification including the OpenLane design flow, Magic, Klayout, Netgen, Ngspice, and Xschem. These tools do not need to ( and can not ) be installed in the desktop by users. The desktop also include an installation of the SKY130 pdk.