

# Filtro de Média Móvel

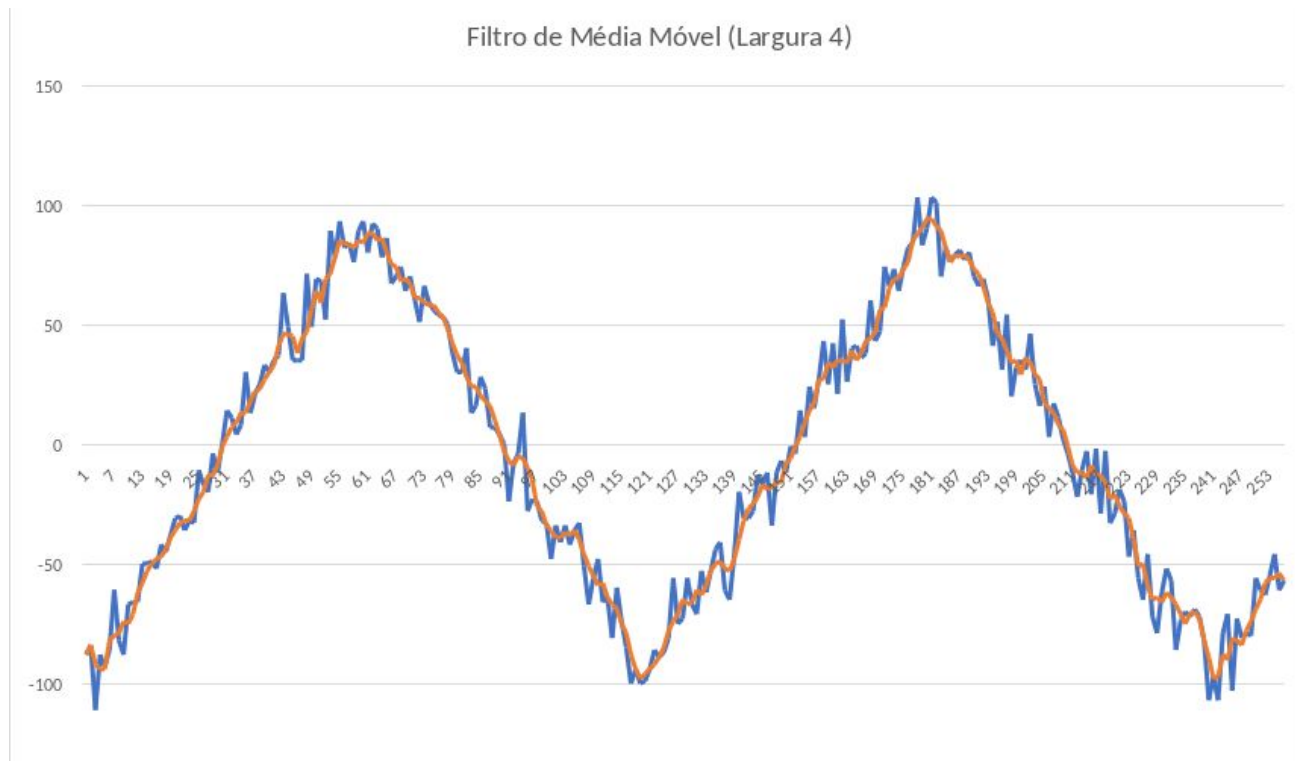
Projeto 5 - Laboratório de Sistemas Digitais

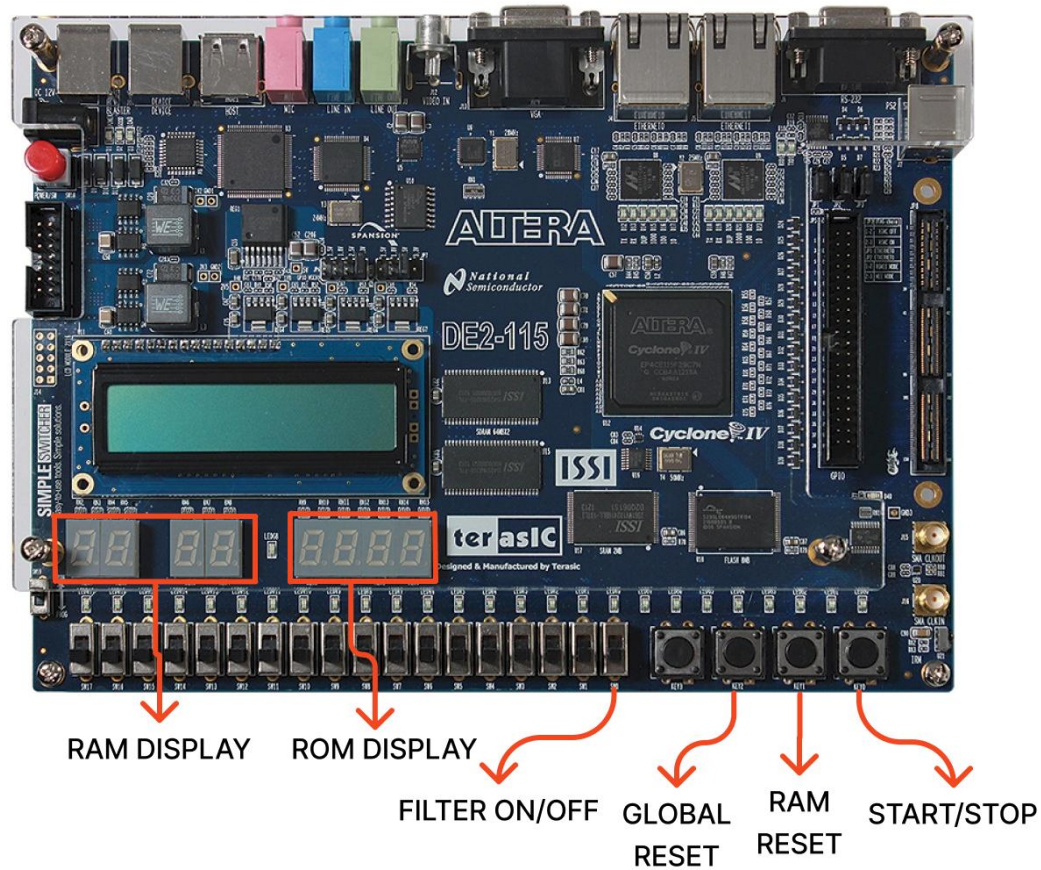


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Luíz Fernando (107535)  
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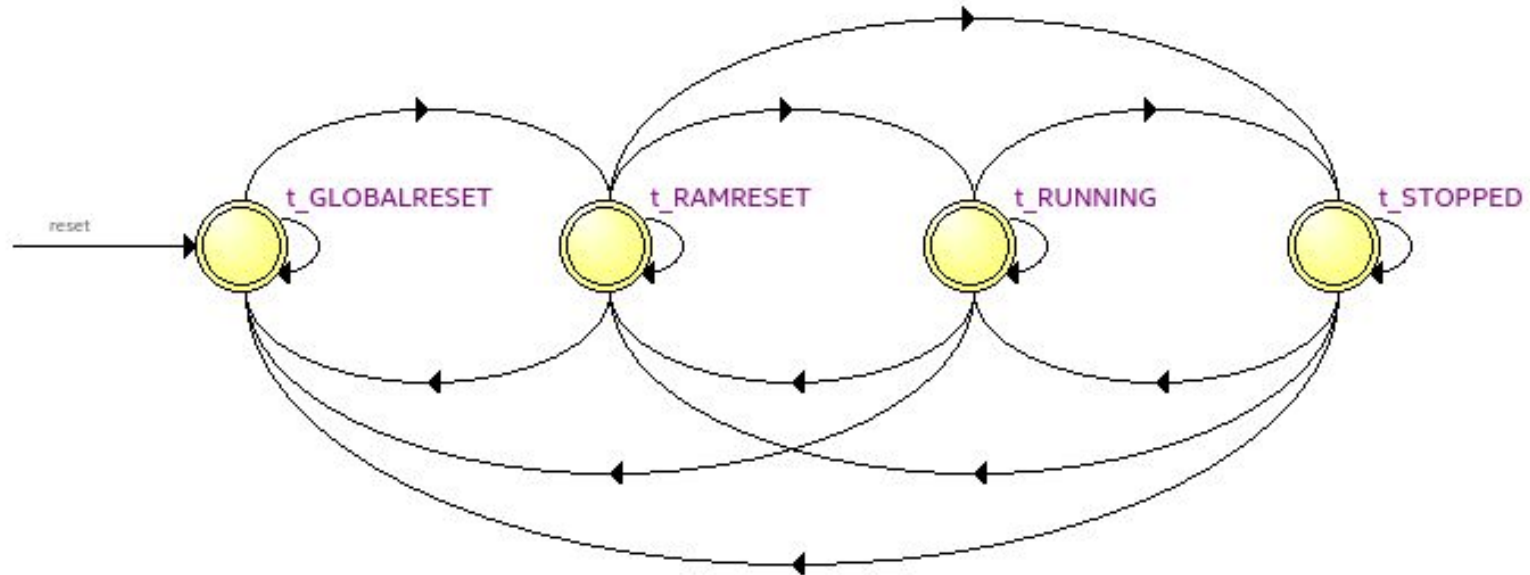
# Em que consiste?

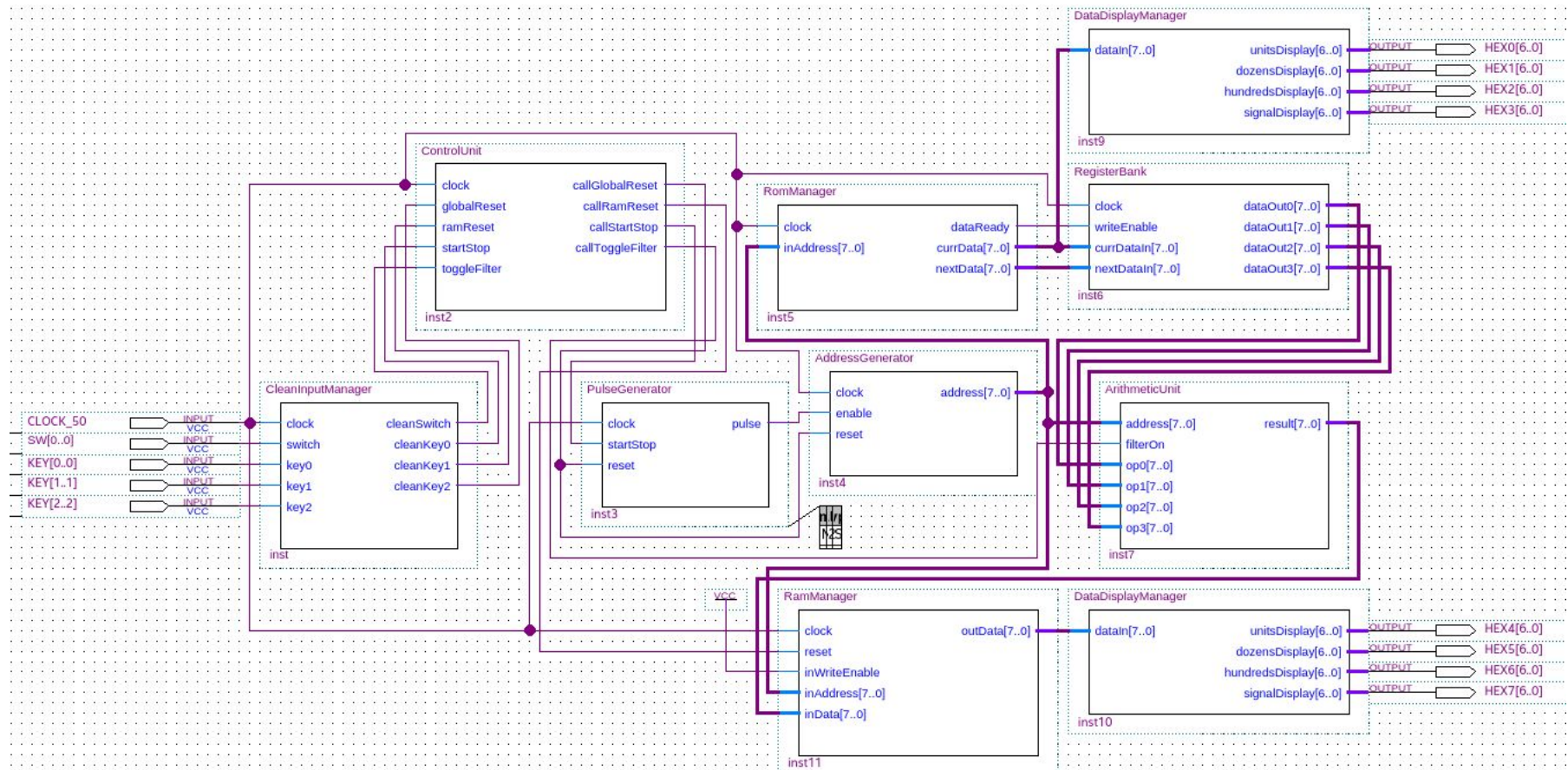




Interface de utilização do filtro móvel na FPGA Terasic DE2-115

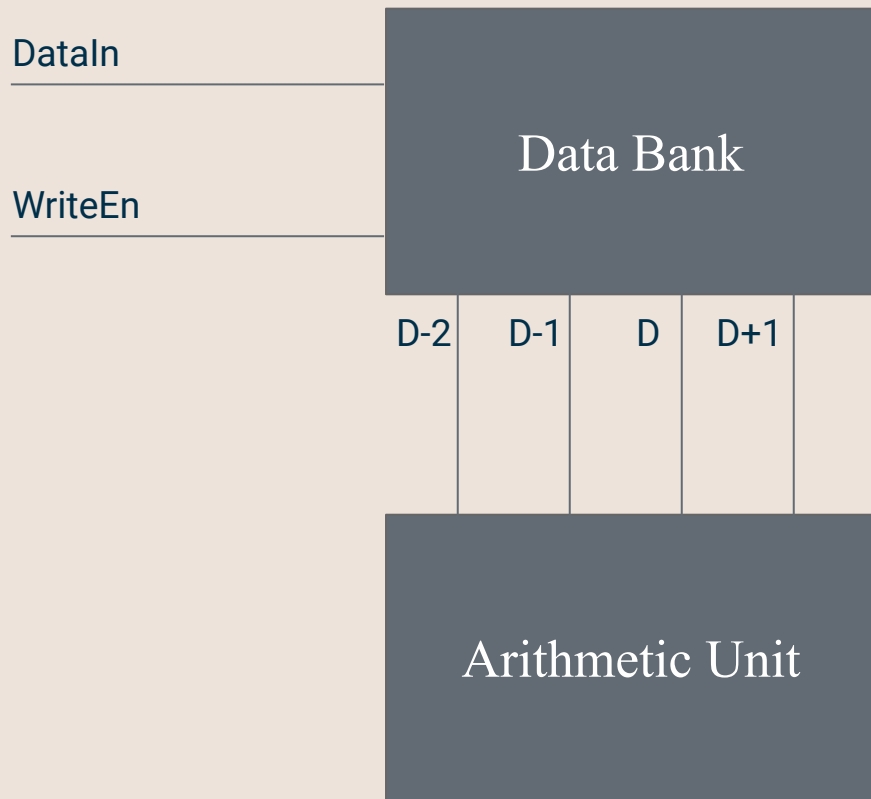
# State Machine - ControlUnit



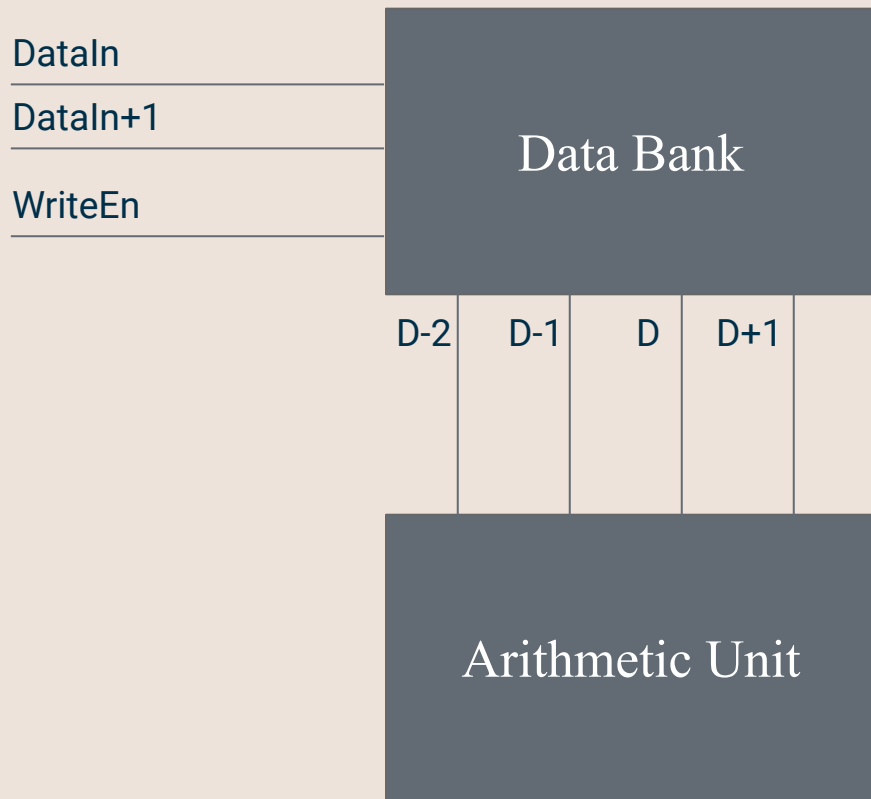


## Arquitetura do Projeto

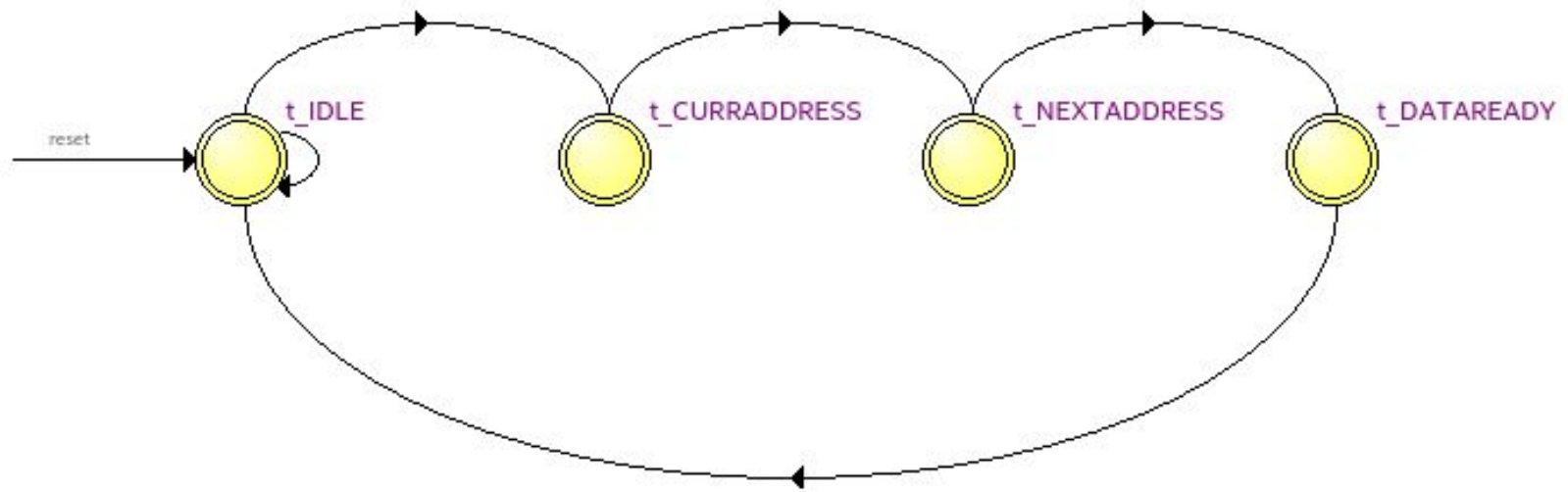
# Maior Dificuldade:



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# State Machine - RomManager





# Validação de Dados



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RAW	-87	-83	-110	-87	-92	-85	-60	-81	-87	-66	-65	-65	-49	-49	-48	-51	-41	-44	-37
FILTER	ON	ON	ON	ON	ON	ON	OFF	OFF	ON	ON	ON	ON	ON	OFF	OFF	ON	OFF	ON	ON
OUT	-87	-83	-91	-93	-93	-81	-60	-81	-73	-74	-70	-61	-57	-49	-48	-47	-41	-37	-38

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