# DISK SELF-GRAVITY CALCULATIONS ON A LEVEL 1 GRID WITH LEVEL 0 REFINEMENT

Computational Science II - University of Zurich

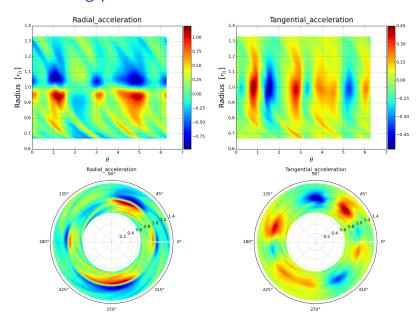
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4th June 2015

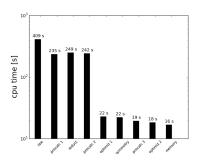
## Contents

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- New approach;
- Code schema;
- Results;
- Time measurements;
- Towards a faster code;

# March starting point



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- reduction of the number of calculations;
- a priori calculation of 1D and 2D arrays;
- use of trigonometric relations;

# New approach

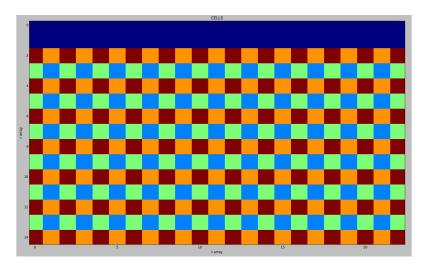


Figure : Cells coloured according to the parity of their indices.

# New approach

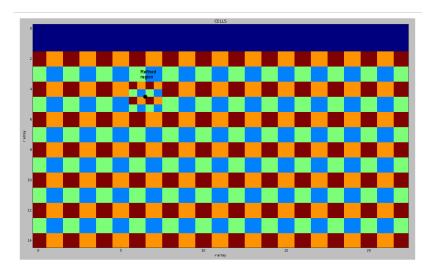
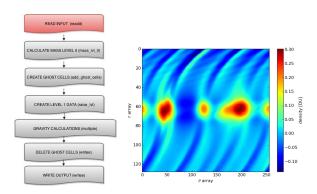
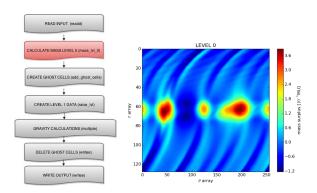
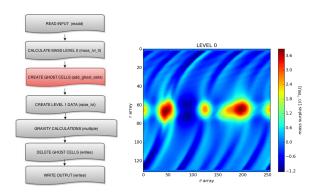
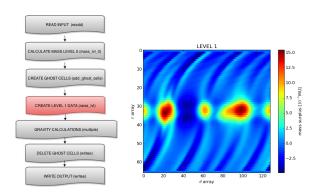


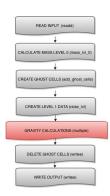
Figure: Refinement to the next lower level

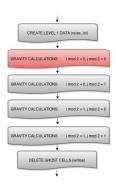


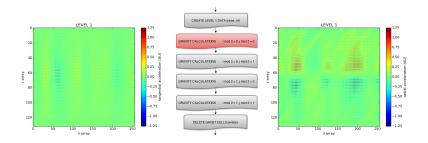


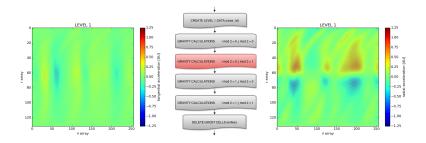


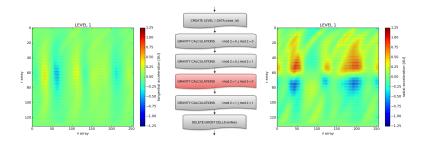


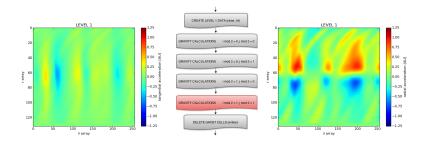




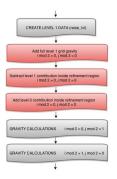






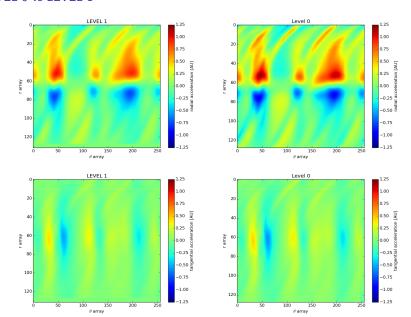


#### Version with refinement

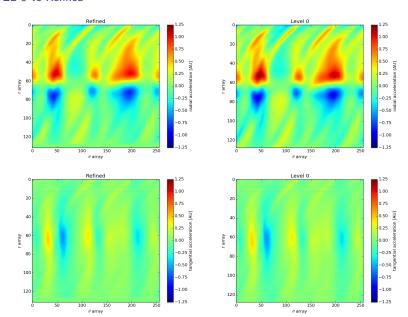


- avoids a series of "if" statements
- main cpu-time usage due to the level1 full calculations
- the larger the number of cells the smaller the share of the other two

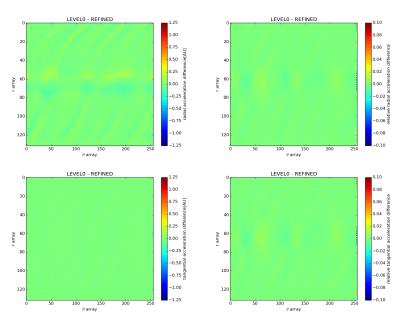
#### LEVEL 0 vs LEVEL 1



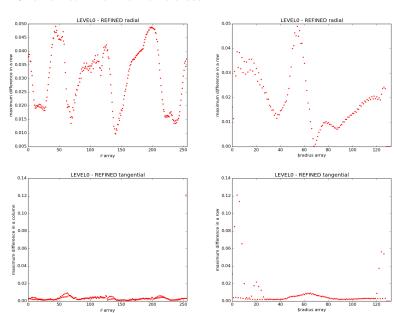
#### LEVEL 0 vs Refined



#### LEVEL 0 vs Refined : Differences



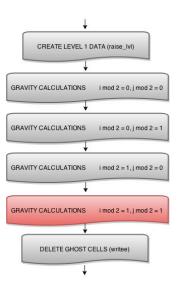
#### LEVEL 0 vs Refined: Maximum differences



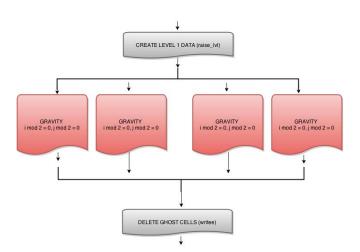
#### Time measurements

- ▶ 16 s level 0 optimization (March results) ;
- ▶ 61 s level 0 constrained for refinement method;
- ▶ 14 s level 1 with no refinement;
- 15 s level 1 with level 0 refinement;

#### Using intra-node parallelism



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- run 4 independent tasks on shared memory ;
- could use i.e. OpenMP;
- simpler distribution of tasks;
- the inter-node MPI structure would not change;
- no MPI communication during one step calculations.
- reduce needed time by 4 to 3-4 s

How about using GPUs?

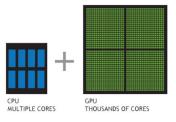


Figure: CPU vs GPU. (Source: NVIDIA)

- rewrite code in GPU languages (i.e. CUDA);
- the gravity calculation part would stay the same;
- the outer loops would need to be replaced to take advantage of the GPU;
- the inter-node MPI would not change;
- ▶ intra-node communication problems...



Movement of the industry and scientific community

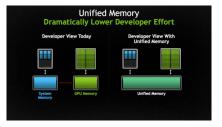


Figure: Future direction for hardware design. (Source: NVIDIA)

- avoid intra-node communication time-loss
- could reduce the needed time to under 1 s

## The fortran code

```
program gravity
! Variable section
implicit none
INTEGER i. i. k. l
INTEGER, parameter :: nr = 128, nt = 256, |v| = 1, ng = 2
REAL ag, a, G, dr, dt, rad, den ,den_inv, comm, prod, nothing, c00, c01, c10, c11, massa
double precision t_init, t_end, timed
REAL, dimension(nr) :: radius
REAL, dimension(nt) :: theta, cos_center, cos_corner, sin_center, sin_corner
REAL. dimension(nr,nt) :: density, acc_r, acc_t
REAL, dimension(:,:), allocatable :: acc_rg, acc_tg
REAL. dimension(nr.nt) :: mass
REAL :: cos_diff, sin_diff
REAL:: ratio, ratio_f
REAL. dimension (1:2) :: acc
INTEGER :: nr_l, nt_l, nrg, ii, jj, jjj, lll
REAL, dimension(:), allocatable:: radius_l, radius_g
REAL, dimension(:) , allocatable:: theta_l
REAL, dimension(:,:), allocatable :: mass_l, mass_g
REAL theta_ad . radius_ad . valu . radius_corn . radius_corn_2_inv . tl1 . tl2
REAL c1, c2, c3, c4, unity, contr1, contr2, thetap, thetam
logical don
call readd(nr. nt. radius, theta, density)
call mass_lvl_0 (nr. nt. radius. theta. density. mass)
call add_ghost_cells(nr, nt, radius, mass, ng, nrg, radius_g, mass_g)
call raise_lvl(0,0,lvl, nrg, nt, radius_g, theta, mass_g, radius_l, theta_l, mass_l, nr_
```