

@(#)SCDS: LVS version 6.1.7-64b 09/27/2016 19:41 (sjfhw305) \$

Command line: /software/cadence/installs/IC617/tools.lnx86/dfll/bin/64bit/LVS -dir
/gaia/class/student/mahajanm/EEE_234_Project1/LVS -l -s -t
/gaia/class/student/mahajanm/EEE_234_Project1/LVS/layout
/gaia/class/student/mahajanm/EEE_234_Project1/LVS/schematic

Like matching is enabled.

Net swapping is enabled.

Using terminal names as correspondence points.

Compiling Diva LVS rules...

Net-list summary for /gaia/class/student/mahajanm/EEE_234_Project1/LVS/layout/netlist

count

143	nets
16	terminals
132	pmos
132	nmos

Net-list summary for /gaia/class/student/mahajanm/EEE_234_Project1/LVS/schematic/netlist

count

143	nets
16	terminals
132	pmos
132	nmos

Terminal correspondence points

N133	N0	A0
N132	N22	A1
N131	N6	A2
N130	N7	A3
N141	N8	B0
N140	N10	B1

N139	N12	B2
N137	N14	B3
N136	N15	CarryOut
N129	N19	S0
N128	N17	S1
N127	N11	S2
N142	N18	S3
N135	N1	Sel
N138	N21	VDD
N134	N9	VSS

Devices in the rules but not in the netlist:

cap nfet pfet nmos4 pmos4

The net-lists match.

	layout	schematic
	instances	
un-matched	0	0
rewired	0	0
size errors	0	0
pruned	0	0
active	264	264
total	264	264

	nets	
un-matched	0	0
merged	0	0
pruned	0	0
active	143	143
total	143	143

	terminals	
un-matched	0	0
matched but		
different type	0	0
total	16	16

Probe files from /gaia/class/student/mahajanm/EEE_234_Project1/LVS/schematic

devbad.out:

netbad.out:

mergenet.out:

termbad.out:

prunenet.out:

prunedev.out:

audit.out:

Probe files from /gaia/class/student/mahajanm/EEE_234_Project1/LVS/layout

devbad.out:

netbad.out:

mergenet.out:

termbad.out:

prunenet.out:

prunedev.out:

audit.out: