

@(#)SCDS: LVS version 6.1.7-64b 09/27/2016 19:41 (sjfhw305) \$

Command line: /software/cadence/installs/IC617/tools.lnx86/dfll/bin/64bit/LVS -dir
/gaia/class/student/mahajanm/EEE_234_Project1/LVS -l -s -t
/gaia/class/student/mahajanm/EEE_234_Project1/LVS/layout
/gaia/class/student/mahajanm/EEE_234_Project1/LVS/schematic

Like matching is enabled.

Net swapping is enabled.

Using terminal names as correspondence points.

Compiling Diva LVS rules...

Net-list summary for /gaia/class/student/mahajanm/EEE_234_Project1/LVS/layout/netlist

count

27	nets
14	terminals
21	pmos
21	nmos

Net-list summary for /gaia/class/student/mahajanm/EEE_234_Project1/LVS/schematic/netlist

count

27	nets
14	terminals
21	pmos
21	nmos

Terminal correspondence points

N25	N7	D0
N24	N4	D1
N23	N1	D2
N22	N5	D3
N21	N18	D4
N20	N8	D5

N19	N3	D6
N18	N19	D7
N15	N10	S0
N14	N13	S1
N13	N14	S2
N26	N2	VDD
N16	N15	VSS
N17	N0	Y

Devices in the rules but not in the netlist:

cap nfet pfet nmos4 pmos4

The net-lists match.

	layout	schematic
	instances	
un-matched	0	0
rewired	0	0
size errors	0	0
pruned	0	0
active	42	42
total	42	42

	nets	
un-matched	0	0
merged	0	0
pruned	0	0
active	27	27
total	27	27

	terminals	
un-matched	0	0

matched but		
different type	0	0
total	14	14

Probe files from /gaia/class/student/mahajanm/EEE_234_Project1/LVS/schematic

devbad.out:

netbad.out:

mergenet.out:

termbad.out:

prunenet.out:

prunedev.out:

audit.out:

Probe files from /gaia/class/student/mahajanm/EEE_234_Project1/LVS/layout

devbad.out:

netbad.out:

mergenet.out:

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