

@(#)SCDS: LVS version 6.1.7-64b 09/27/2016 19:41 (sjfhw305) \$

Command line: /software/cadence/installs/IC617/tools.lnx86/dfl/bin/64bit/LVS -dir
/gaia/class/student/mahajanm/EEE_234_Project1/LVS -l -s -t
/gaia/class/student/mahajanm/EEE_234_Project1/LVS/layout
/gaia/class/student/mahajanm/EEE_234_Project1/LVS/schematic

Like matching is enabled.

Net swapping is enabled.

Using terminal names as correspondence points.

Compiling Diva LVS rules...

Net-list summary for /gaia/class/student/mahajanm/EEE_234_Project1/LVS/layout/netlist

count

454	nets
18	terminals
444	pmos
444	nmos

Net-list summary for /gaia/class/student/mahajanm/EEE_234_Project1/LVS/schematic/netlist

count

454	nets
18	terminals
444	pmos
444	nmos

Terminal correspondence points

N439	N17	A0
N438	N18	A1
N437	N19	A2
N436	N20	A3
N453	N15	B0
N452	N33	B1

N451	N36	B2
N449	N32	B3
N448	N35	P0
N447	N39	P1
N446	N10	P2
N445	N13	P3
N444	N38	P4
N443	N37	P5
N442	N34	P6
N441	N40	P7
N450	N25	VDD
N440	N1	VSS

Devices in the rules but not in the netlist:

cap nfet pfet nmos4 pmos4

The net-lists match.

	layout	schematic
	instances	
un-matched	0	0
rewired	0	0
size errors	0	0
pruned	0	0
active	888	888
total	888	888

	nets	
un-matched	0	0
merged	0	0
pruned	0	0
active	454	454

total	454	454
-------	-----	-----

terminals		
-----------	--	--

un-matched	0	0
------------	---	---

matched but		
-------------	--	--

different type	0	0
----------------	---	---

total	18	18
-------	----	----

Probe files from /gaia/class/student/mahajanm/EEE_234_Project1/LVS/schematic

devbad.out:

netbad.out:

mergenet.out:

termbad.out:

prunenet.out:

prunedev.out:

audit.out:

Probe files from /gaia/class/student/mahajanm/EEE_234_Project1/LVS/layout

devbad.out:

netbad.out:

mergenet.out:

termbad.out:

prunenet.out:

prunedev.out:

audit.out: