(wafer2,0,7,-1,-1,1,3) Characteristics

• Cell Size: 10um

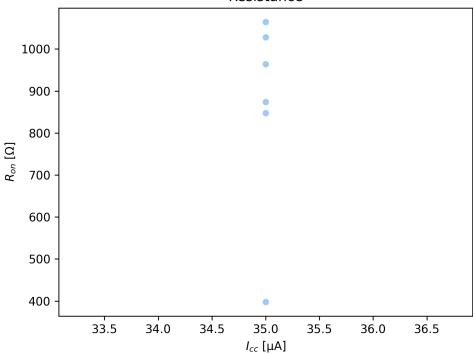
Times Accessed: 13

Last Measurement: 2022/March/28 at 04:34:10PM

Summary

Cycle #	Set Icc (μA)	Set Voltage (V)	$R_on (\Omega)$	R2
1	35.0	4.80	397.61	1.000
2	35.0	4.80	964.10	1.000
3	35.0	4.70	847.16	1.000
4	35.0	2.30	1028.23	1.000
5	35.0	1.55	1064.23	1.000
6	35.0	4.00	873.64	1.000

Resistance



form

• Time: 04:29:06PM

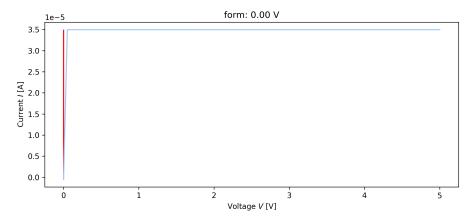
• **Icc**: 35.0uA

Voltage Range: 0V → 5V
Target Ramp Rate: 1V/s
True Ramp Rate: 1.837 V/s*

• Cycle: 1

• Error: Set failed

Set quickly, not actual form for this cell



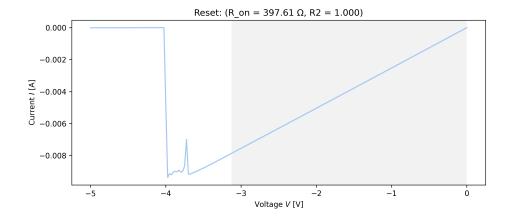
• Time: 04:29:26PM

• **Icc:** 10.0mA

Voltage Range: 0V → -5V
Target Ramp Rate: 1V/s
True Ramp Rate: -0.788 V/s*

• Cycle: 1

Resistance: 397.61 Ω
Linear Fit R2: 1.000



form

• Time: 04:29:58PM

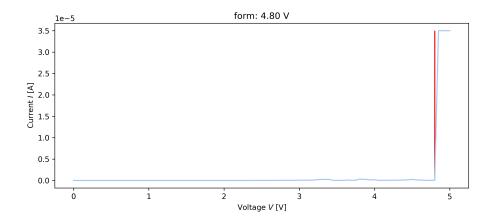
• **Icc**: 35.0uA

Voltage Range: 0V → 5V
Target Ramp Rate: 1V/s
True Ramp Rate: 1.069 V/s*

• **Cycle:** 1

• Set Voltage: 4.80 V

Formed at 4.85V



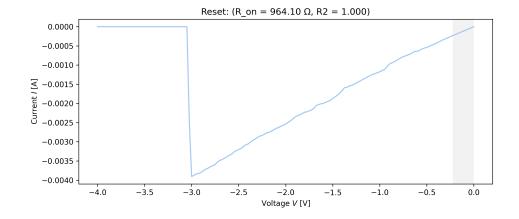
• Time: 04:30:36PM

• **Icc:** 8.0mA

Voltage Range: 0V → -4V
Target Ramp Rate: 1V/s
True Ramp Rate: -0.619 V/s*

• **Cycle:** 2

Resistance: 964.10 Ω
Linear Fit R2: 1.000



• Time: 04:31:06PM

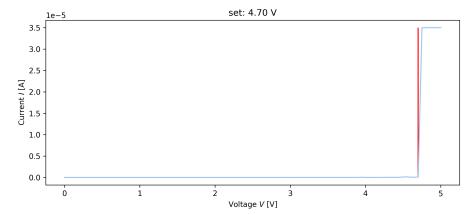
• **Icc**: 35.0uA

Voltage Range: 0V → 5V
Target Ramp Rate: 1V/s
True Ramp Rate: 1.069 V/s*

• **Cycle:** 3

• Set Voltage: 4.70 V

Set at 4.75V, high voltage but lower than form



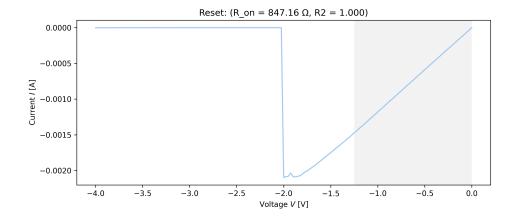
• Time: 04:31:29PM

• **Icc:** 8.0mA

Voltage Range: 0V → -4V
Target Ramp Rate: 1V/s
True Ramp Rate: -0.642 V/s*

• **Cycle:** 3

Resistance: 847.16 Ω
Linear Fit R2: 1.000



• Time: 04:31:50PM

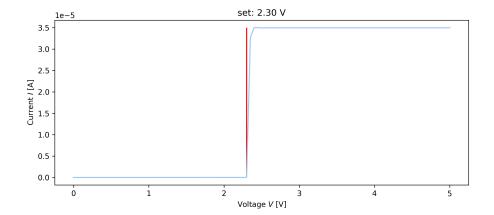
• **Icc:** 35.0uA

Voltage Range: 0V → 5V
Target Ramp Rate: 1V/s
True Ramp Rate: 1.069 V/s*

• Cycle: 4

• Set Voltage: 2.30 V

Set at 2.4V



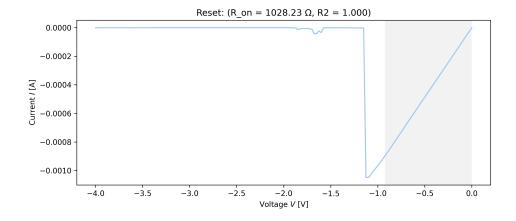
• Time: 04:32:15PM

• **Icc:** 8.0mA

Voltage Range: 0V → -4V
Target Ramp Rate: 1V/s
True Ramp Rate: -0.605 V/s*

• Cycle: 4

Resistance: 1028.23 Ω
Linear Fit R2: 1.000



• **Time:** 04:32:38PM

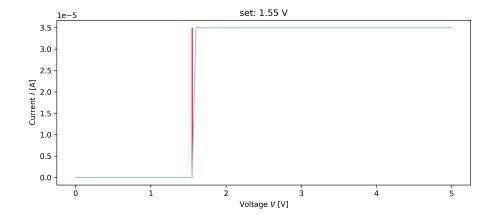
• **Icc**: 35.0uA

Voltage Range: 0V → 5V
Target Ramp Rate: 1V/s
True Ramp Rate: 1.069 V/s*

• **Cycle:** 5

• Set Voltage: 1.55 V

Set at 1.6V



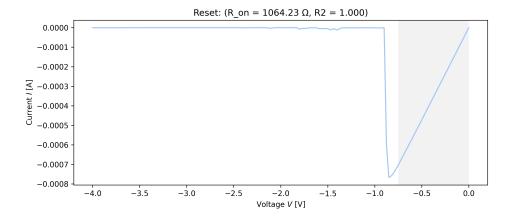
• Time: 04:32:58PM

• **Icc:** 8.0mA

Voltage Range: 0V → -4V
Target Ramp Rate: 1V/s
True Ramp Rate: -0.558 V/s*

• **Cycle:** 5

Resistance: 1064.23 Ω
Linear Fit R2: 1.000
Did Reset, very low voltage



• Time: 04:33:33PM

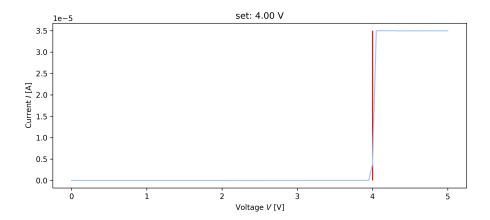
• **Icc:** 35.0uA

Voltage Range: 0V → 5V
Target Ramp Rate: 1V/s
True Ramp Rate: 1.069 V/s*

• **Cycle:** 6

• Set Voltage: 4.00 V

Set at 4.05V



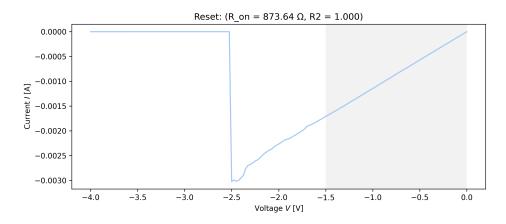
• Time: 04:33:57PM

• **Icc:** 8.0mA

Voltage Range: 0V → -4V
Target Ramp Rate: 1V/s
True Ramp Rate: -0.635 V/s*

• **Cycle**: 6

Resistance: 873.64 Ω
Linear Fit R2: 1.000
Did Reset, good voltage



• **Time:** 04:34:10PM

• **Icc**: 35.0uA

Voltage Range: 0V → 5V
Target Ramp Rate: 1V/s
True Ramp Rate: 1.069 V/s*

• Cycle: 7

• Set Voltage: 3.10 V

Set at 3.15V

