ECE4580 Final Project: Cartoonification Application

December 6, 2021

Mihir Savadi
The Bradley Department of
Electrical and Computer Engineering
Virginia Tech
Blacksburg, VA, USA
mihirsavadi1@vt.edu

I. THE PROBLEM
II. APPROACH
III. EXPERIMENTAL RESULTS
REFERENCES

[1] M. Savadi, "ECE4540: VLSI Circuit Design, Lab 2: CMOS Combinational Circuits", Blacksburg, VA, 2021.