


```
sonic_cli_stp.del_stp  
_intf_edge_port
```



```
graph LR; A[sonic_cli_stp.del_stp  
_intf_edge_port] --> B[sonic_cli_stp.set_stp  
_intf_edge_port];
```

```
sonic_cli_stp.set_stp  
_intf_edge_port
```