

# MISHAT HASSAN

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## RELEVANT EXPERIENCES

### Senior Design Verification Engineer – Marvell Technology

Ottawa, ON, Canada | June 2023 – Present

- Worked extensively on Ethernet IP and SoC verification, including VIP integrations, PHY bringups, and MACsec verification.
- Built scalable, reusable UVM testbenches for block-level and SoC-level verification.
- Implemented functional coverage models and assertions (SVA) to ensure verification completeness and improve quality.
- Owned the end-to-end regression verification process, from automation and execution to failure analysis and debugging, ensuring consistent design quality through continuous testing and improvements.
- Collaborated with design teams to review design specifications and define thorough test plans.
- Maintained and upgraded existing verification infrastructure to support evolving design complexity.

### Verification Engineer Intern – Marvell Technology

Ottawa, ON, Canada | September 2022 – March 2023

- Developed Python, Perl, C, and C++ test applications, scripts, and automation tools to verify design correctness and performance.
- Executed regression and unit tests, ensuring code coverage and proper test assertions.
- Designed and implemented SystemVerilog UVM testbenches to verify functional designs.

### Electrical Engineering Co-op – Newfoundland and Labrador Hydro

St. John's, NL, Canada | January 2022 – May 2022

- Used Bluebeam to design and modify electrical circuits.
- Created templates for SJB, Panels, DFR, and Relays.
- Developed a VBA + Excel-based lookup tool for schematic retrieval.
- Assisted with design, construction, and demolition of protection circuits.
- Investigated breakers and generators for drawing updates.

## EDUCATION

### Memorial University of Newfoundland (2017–2023)

Bachelor of Engineering Co-op Program, Electrical Engineering

St. John's, Newfoundland, Canada

### Rajuk Uttara Model College (2015–2017)

HSC (Higher Secondary Certificate)

Uttara Model Town, Dhaka, Bangladesh

## TECHNICAL SKILLS

- Scripting and Automation: Python, Bash
- Verification and Simulation Tools: SystemVerilog, UVM, SVA, VCS
- Debugging Tools: DVE, Verdi
- Other Tools: Git, NeoVim, Jira, Confluence

## RELEVANT PROJECTS

- 8-bit Computer on Breadboard (Personal)
- Clocking-related VHDL Projects on FPGA (Course Work)
- ALU and FIFO Design and Verification (Course Work)
- UART Implementation on FPGA (Personal + Course Work)

## SCHOLARSHIPS

- Outstanding Co-op Performance (2021)
- Memorial University of Newfoundland Entrance Scholarship (2017)
- Higher Secondary Certificate Scholarship (2017)
- Secondary School Certificate Scholarship (2015)

## ACTIVITIES

- Instructed public speaking workshops as a member of Bangladesh Debate Federation (2015–2017).
- Founded BCPSCDC and served as Founding President (2013–2015).
- Organized Rajuk Nationals 2016, Naturenligh 2016, and Apex RCPC Photography Exhibition II.
- Served as Vice President of RCDC and trained debate teams at ACSDC and BCPSCDC (2015–2017).