



MPLAB® XC8 User's Guide for Embedded Engineers - AVR MCUs

Introduction

This document presents five code examples for 8-bit AVR MCU devices and the MPLAB® XC8 C compiler using the Common Code Interface (CCI). For more on CCI, see the “*MPLAB® XC8 C Compiler User’s Guide for AVR MCU*” (DS50002750).

Some knowledge of microcontrollers and the C programming language is necessary.

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1. Turn LED On or Off

This example will light the User LED on the ATmega4809 Curiosity Nano board. For more information, see section [7. Get Hardware and Software](#).

```
/*
 * File:    main.c
 * Author: Microchip Technology Inc.
 *
 * Created on July 28, 2020 9:55 AM
 */

// ATmega4809 Configuration Bit Settings
// 'C' source line config statements

#include <xc.h>

FUSES = {
    .WDTCFG = 0x00, // WDTCFG {PERIOD=OFF, WINDOW=OFF}
    .BODCFG = 0x00, // BODCFG {SLEEP=DIS, ACTIVE=DIS, SAMPFREQ=1KHZ, LVL=BODLEVEL0}
    .OSCCFG = 0x02, // OSCCFG {FREQSEL=20MHZ, OSCLOCK=CLEAR}
    .SYSCFG0 = 0xC0, // SYSCFG0 {EESAVE=CLEAR, RSTPINCFG=GPIO, CRCSRC=NOCRC}
    .SYSCFG1 = 0x07, // SYSCFG1 {SUT=64MS}
    .APPEND = 0x00, // APPEND
    .BOOTEND = 0x00, // BOOTEND
};

LOCKBITS = 0xC5; // {LB=NOLOCK}

int main(void) {
    PORTF.DIRSET = PIN5_bm; // set PF5 to be output

    PORTF.OUTCLR = PIN5_bm; // clear PF5 - LED on

    //PORTF.OUTSET = PIN5_bm; // set PF5 - LED off

    while (1) {
    }

    return(0);
}
```

1.1 Configuration Bits

Microchip devices have configuration bits, or fuses, that enable and/or set up device features.

Note: If you do not set Configuration bits correctly, your device will not operate at all, or at least not as expected.

Which Configuration Bits to Set

In particular, be aware of the followings settings:

- Oscillator configuration - This must match your hardware's oscillator circuitry. If this is not correct, the device clock may not run. Typically, development boards use high-speed crystal oscillators. From the example code:

```
FUSES = {
    ...
    .OSCCFG = 0x02, // OSCCFG {FREQSEL=20MHZ, OSCLOCK=CLEAR}
    ...
}
```

- Watchdog timer configuration- It is recommended that you disable this timer until it is required. This prevents unexpected Resets. From the example code:

```
FUSES = {
    .WDTCFG = 0x00, // WDTCFG {PERIOD=OFF, WINDOW=OFF}
    ...
}
```

- Code protection - Turn off code protection until it is required. This ensures that device memory is fully accessible. From the example code:

```
LOCKBITS = 0xC5; // {LB=NOLOCK}
```

Note: You may also set configuration bits with `#pragma config`. For details, see the “MPLAB® XC8 C Compiler User’s Guide for AVR® MCU” (DS50002750).

See your device data sheet for the name and function of corresponding configuration bits. Use the part number to search www.microchip.com for the appropriate data sheet.

For more information about configuration bits that are available for each device, see the following file in the location where MPLAB XC8 was installed:

MPLAB XC8 Installation Directory/docs/avr_chipinfo.html

How to Set Configuration Bits

In MPLAB X IDE, you can use the Configuration Bits window to view and set these bits. Select **Window>Target Memory Views>Configuration Bits** to open this window.


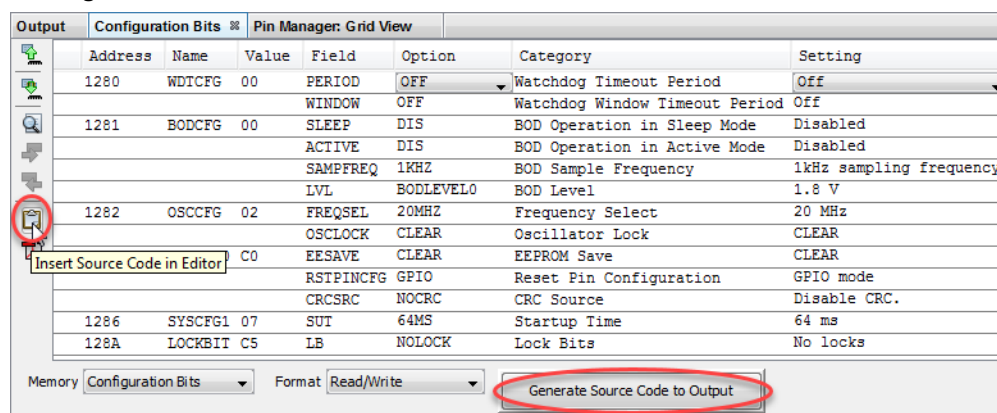
For AVR devices, click  to read configuration memory, i.e., FUSES and LOCKBITS values.

Figure 1-1. Configuration Bits Window



Address	Name	Value	Field	Option	Category	Setting
1280	WDTCFG	00	PERIOD	OFF	Watchdog Timeout Period	Off
			WINDOW	OFF	Watchdog Window Timeout Period	Off
1281	BODCFG	00	SLEEP	DIS	BOD Operation in Sleep Mode	Disabled
			ACTIVE	DIS	BOD Operation in Active Mode	Disabled
			SAMPFREQ	1KHZ	BOD Sample Frequency	1kHz sampling frequency
			LVL	BODLEVEL0	BOD Level	1.8 V
1282	OSCCFG	02	FREQSEL	20MHZ	Frequency Select	20 MHz
			OSCCLOCK	CLEAR	Oscillator Lock	CLEAR
			EESAVE	CLEAR	EEPROM Save	CLEAR
			RSTPINCFG	GPIO	Reset Pin Configuration	GPIO mode
			CRCSRC	NOCRC	CRC Source	Disable CRC.
1286	SYSCFG1	07	SUT	64MS	Startup Time	64 ms
128A	LOCKBIT	C5	LB	NOLOCK	Lock Bits	No locks

Memory: Configuration Bits Format: Read/Write **Generate Source Code to Output**

Once the settings are selected, click in code where you want this information placed and then click the **Insert Source Code in Editor** icon, as shown in the example code. Alternatively click the **Generate Source Code to Output** button to copy and paste into code.

See MPLAB X IDE documentation for more information on this window.

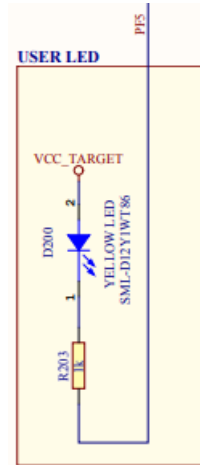
1.2 Included Header File

The `xc.h` header file allows code in the source file to access compiler-specific or device-specific features. Based on your selected device, the compiler sets macros that allow `avr/io.h` to vector to the correct device-specific header file. Do not include a device-specific header in your code or your code will not be portable.

This and other header files can be found in the MPLAB XC8 installation directory in the `avr/avr/include/avr` or `dfp/xc8/avr/include/avr` subdirectories, or from paths specified via the `-mdfp` option.

1.3 Port Access for LED

Each I/O pin Pxn can be controlled by the registers in PORTx. Each pin group x has its own set of PORT registers. For this example, PF5 of PORTF is used to turn the User LED on or off. Examining the board schematics shows that PF5 must be low for the LED to be lit and high for the LED to be off. Find the schematics link in the Kit Window.



Digital I/O device pins may be multiplexed with peripheral I/O pins. To ensure that you are using digital I/O only, disable the other peripheral(s). Do this by using the predefined C variables that represent the peripheral registers and bits. These variables are listed in the device-specific header file in the compiler include directory. To determine which peripherals share which pins, refer to your device data sheet.

To use PF5 as an output only pin, write bit 5 in the PORTF.DIRSET register to '1'. To do this without disturbing the value of the other bits, masks are provided for each register bit. In this case, PIN5_bm = 00001000.

```
PORTF.DIRSET = PIN5_bm; // set PF5 to be output
```

Writing bit 5 in PORTF.OUTCLR to '1' will clear that bit (set to '0'). This will turn the LED on.

```
PORTF.OUTCLR = PIN5_bm; // clear PF5 - LED on
```

Alternately, writing bit 5 in PORTF.OUTSET to '1' will set that bit (set to '1'). To turn the LED off, comment out the above instruction and uncomment the following instruction.

```
PORTF.OUTSET = PIN5_bm; // set PF5 - LED off
```

To view information on PORT registers, highlight a register name in the Editor, right click, and select [Navigate>Online Datasheet](#).

2. Flash LED Using Delay Function

This example is a modification of the previous code. Instead of just turning on or off the User LED, this code will make the LED flash.

```
/*
 * File:    main.c
 * Author: Microchip Technology Inc.
 *
 * Created on July 28, 2020 10:34 AM
 */

// ATmega4809 Configuration Bit Settings

// 'C' source line config statements

// After any reset, CLR_PER = CLK_MAIN/Prescaler = 20MHz / 6 = 3.3MHz
#define F_CPU (3300000UL)

#include <xc.h>
#include <util/delay.h>

FUSES = {
    .WDTCFG = 0x00, // WDTCFG {PERIOD=OFF, WINDOW=OFF}
    .BODCFG = 0x00, // BODCFG {SLEEP=DIS, ACTIVE=DIS, SAMPFREQ=1KHZ, LVL=BODLEVEL0}
    .OSCCFG = 0x02, // OSCCFG {FREQSEL=20MHZ, OSCLOCK=CLEAR}
    .SYSCFG0 = 0xC0, // SYSCFG0 {EESAVE=CLEAR, RSTPINCFG=GPIO, CRCSRC=NOCRC}
    .SYSCFG1 = 0x07, // SYSCFG1 {SUT=64MS}
    .APPEND = 0x00, // APPEND
    .BOOTEND = 0x00, // BOOTEND
};

LOCKBITS = 0xC5; // {LB=NOLOCK}

int main(void) {

    PORTF.DIRSET = PIN5_bm; // set PF5 to be output

    while (1) {
        PORTF.OUTTGL = PIN5_bm; // toggle PF5
        _delay_ms(500);
    }

    return(0);
}
```

2.1 The while() Loop and Toggle Function

Writing a bit in PORTF.OUTTGL to '1' will toggle that bit. For PF5:

```
PORTF.OUTTGL = PIN5_bm; // toggle PF5
```

To continually toggle the pin, and flash the LED, the `while(1)` loop is used.

2.2 The Delay Function

Because the speed of execution will, in most cases, cause the LED to flash faster than the eye can see, execution needs to be slowed. `_delay_ms()` is a built-in function of the compiler. To use this function, the header `util/delay.h` must be included. Also, the speed of the processor must be specified:

```
#define F_CPU (3300000UL)
```

For more details on the delay built-in, see the “*MPLAB XC8 C Compiler User’s Guide for AVR MCU*” (DS50002750).

3. Toggle LED Using Button Press and Interrupts

This example is a modification of the previous code. This time the User LED will be turned on or off by clicking the User button. When the button is clicked, interrupts will be used to toggle the LED state.

```

/*
 * File:    main.c
 * Author:  Microchip Technology Inc.
 *
 * Created on August 3, 2020 10:12 AM
 */

// ATmega4809 Configuration Bit Settings

// 'C' source line config statements

#include <xc.h>

FUSES = {
    .WDTCFG = 0x00, // WDTCFG {PERIOD=OFF, WINDOW=OFF}
    .BODCFG = 0x00, // BODCFG {SLEEP=DIS, ACTIVE=DIS, SAMPFREQ=1KHZ, LVL=BODLEVEL0}
    .OSCCFG = 0x02, // OSCCFG {FREQSEL=20MHZ, OSCLOCK=CLEAR}
    .SYSCFG0 = 0xC0, // SYSCFG0 {EESAVE=CLEAR, RSTPINCFG=GPIO, CRCSRC=NOCRC}
    .SYSCFG1 = 0x07, // SYSCFG1 {SUT=64MS}
    .APPEND = 0x00, // APPEND
    .BOOTEND = 0x00, // BOOTEND
};

LOCKBITS = 0xC5; // {LB=NOLOCK}

// Interrupt function
void __interrupt(PORTF_PORT_vect_num) btnInt(void)
{
    if(PORTF.INTFLAGS == PIN6_bm) // check PF6 interrupt
    {
        PORTF.OUTTGL = PIN5_bm; // toggle LED

        PORTF.INTFLAGS = PIN6_bm; // clear interrupt
    }
}

int main(void)
{
    //LED init
    PORTF.DIRSET = PIN5_bm; // set PF5 to be output
    PORTF.OUTSET = PIN5_bm; // set PF5 - LED off

    //BUTTON init
    //Reset value of all PORTF pins is '0', which is input
    PORTF.PIN6CTRL = PORT_PULLUPEN_bm | PORT_ISC_FALLING_gc; //enable pullups on PF6, IRQ on falling edge

    ei(); //enable global interrupts

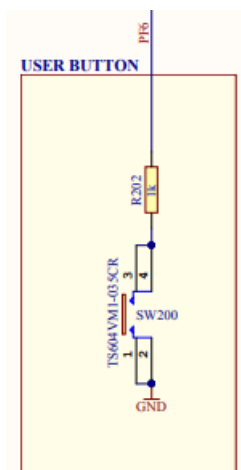
    while (1) {
        //wait for button press
    }

    return 0;
}

```

3.1 Port Access for Button

For this example, PF6 of PORTF is used to sense if a button press has occurred. Examining the board schematics shows that the internal port pull-up will need to be enabled so that the PF6 will go from '1' to '0' when the button is pressed.



While you could use `DIRCLR` to set PF6 as an input pin, the reset value of `PORTx` is 0x00 which sets all pins to inputs.

`PINCTRL` enables the pull-up and configures the input/sense on PF6. The sense configuration determines how a port interrupt can be triggered.

```
PORTF.PIN6CTRL = PORT_PULLUPEN_bm | PORT_ISC_FALLING_gc;
```

3.2 Interrupt on Pin Change

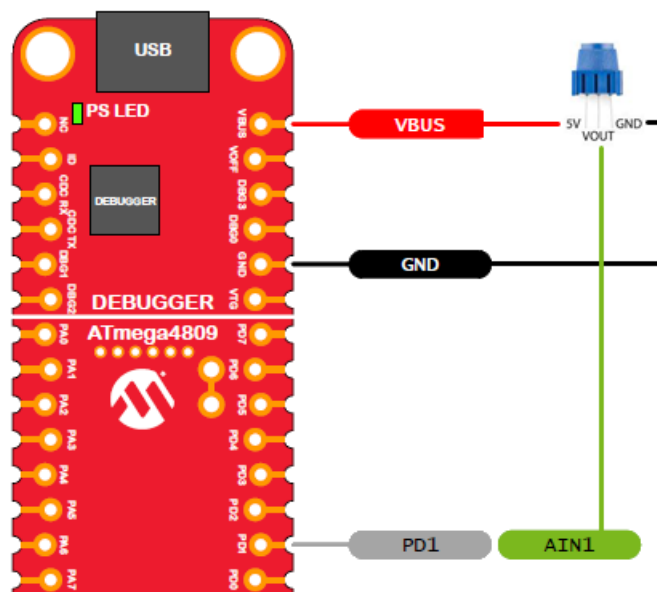
In `main()` code, `ei()` enables global interrupts.

`__interrupt(vector)` specifies `btnInt()` as the interrupt function. Port interrupt vectors are of the form `PORTx_Port_vect_num` (see `iom4809.h`.) To ensure only the PF6 change triggers an interrupt, the interrupt flag is checked. Then the LED is toggled, and the interrupt flag is cleared. Writing a '1' to a flag's bit location will clear the flag.

4. Light LED if Potentiometer Value Below ADC Value

This ADC Window Comparator example will demonstrate how to initialize the ADC, set the conversion window comparator low threshold, enable the conversion Window mode, enable the Free Running mode, start the conversion, and then wait until the conversion is done to turn on the LED if the ADC result is below the set threshold or turn off the LED if the result is above the threshold. A potentiometer was used as the analog source.

Figure 4-1. ATmega4809 Curiosity Nano ADC Connections



For more on this and other ADC examples, see TB3209: “Getting Started with ADC” (DS90003209).

Instead of generating code by hand, the MPLAB Code Configurator (MCC) is used. The MCC is a plug-in available for installation under the MPLAB X IDE menu *Tools>Plugins*, **Available Plugins** tab. See MPLAB X IDE Help for more on how to install plugins.

For information on the MCC, including the “MPLAB® Code Configurator 3.xx User’s Guide” (DS40001829), go to the MPLAB Code Configurator web page at:

www.microchip.com/mplab/mplab-code-configurator

For this example, the MCC UI was set up as shown in the following sections.

4.1 MCC System Resource Configuration

Figure 4-2. Project Resources - System Module

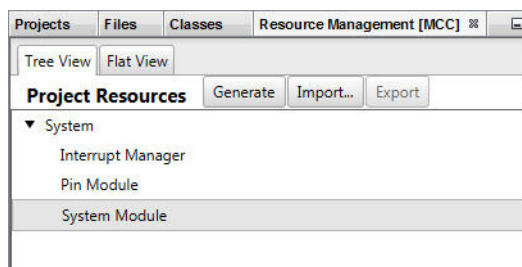


Figure 4-3. System Module Configuration

The screenshot shows the 'System Module' configuration window with the 'Easy Setup' tab selected. The window is divided into four main sections:

- Clock Control:**
 - Main Clock(Hz): 3333333
 - Clock Source: Internal Oscillator
 - Internal Oscillator Frequency: 20 MHz
 - External Clock(Hz): 1 ≤ 1000000 ≤ 20000000
 - Prescaler Enable: ☒
 - Prescaler: 6X
 - Clock Out Enable: ☐
- Watchdog Timer:**
 - WDT Period: Off
 - WDT Window: Off
- Brown-out Detector:**
 - BOD Operation Mode: Disabled
 - BOD Level: 1.8 V
 - BOD Sampling Frequency: 1kHz sampling frequency
 - BOD Operation in Sleep Mode: Disabled
- Voltage Level Monitor:**
 - VLM configuration: Interrupt when supply goes below VLM level
 - Interrupt Enable: ☐
 - VLM Level: VLM threshold 5% above BOD level

4.2 MCC ADC Resource Configuration

Although only the “Easy Setup” tab for ADC Resource Configuration is shown, you should also review the “Registers” tab for setups not shown (MUXPOS) and for alternate data entry.

Figure 4-4. ADC Resource Selection

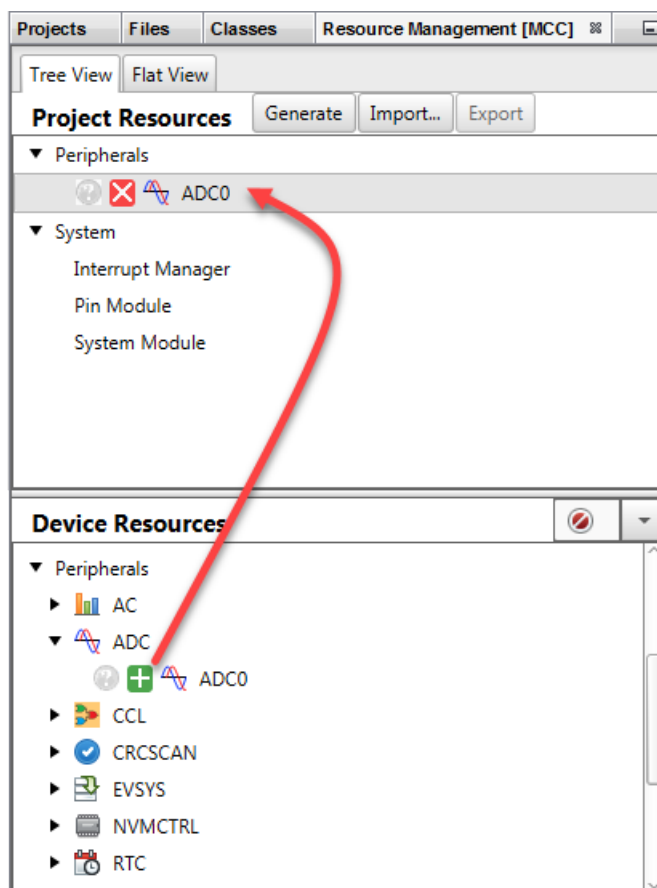


Figure 4-5. ADC Resource Configuration

...age Available Resources Pin Module Interrupt Manager System Module ADC0

ADC0

Easy Setup Registers

Software Settings

API Prefix: ADC0

Result Selection : 10-bit mode

Hardware Settings

Enable ADC: ☒

Sampling Frequency(Hz): 18939 ≤ 59523 ≤ 64102

ADC Clock(Hz): 833333

Sample Accumulation Number: 1 ADC sample

Sample Length (# of ADC Clock) : 0 ≤ 1 ≤ 31

Voltage Reference : Internal reference

Interrupt Settings

Result Ready Interrupt Enable: ☒

WCMP Interrupt Enable: ☒

Select Channels

Pin	Channel	Custom Name
PIN_AIN1	PIN_AIN1	channel_PIN_AIN1
Internal Channel	0V_(GND)	channel_0V (GND)
Internal Channel	Temperature_sensor	channel_Temperature sensor
Internal Channel	AC_DAC_Reference	channel_AC DAC Reference

Window Settings

Window Comparator Mode: Below Window

Enable IRQ on conversion complete: ☒

Enable IRQ on conversion satisfying window criteria : ☒

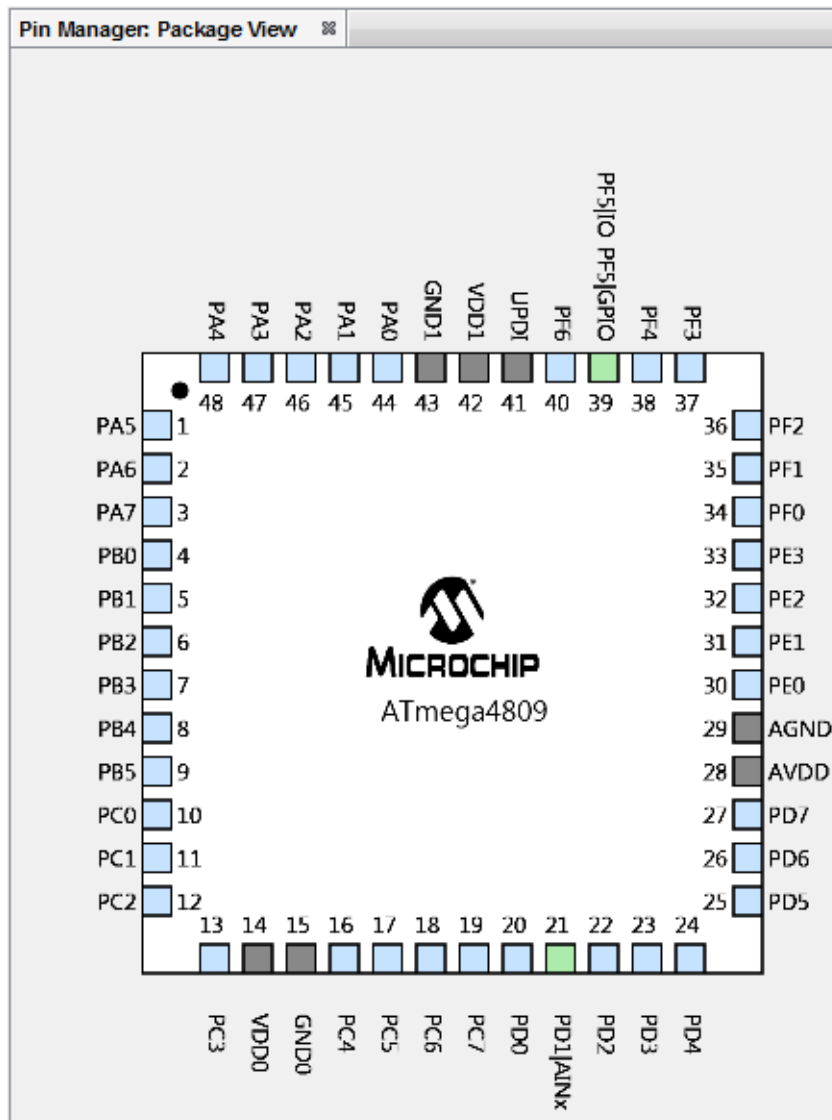
Window Comparator Low Threshold(V): 0 ≤ 512 ≤ 65535

4.3 MCC GPIO Pin Resource Configuration

Figure 4-6. GPIO Pin Resource - Grid

Search Results				Output				Variables				C Stack				Breakpoints				Notifications [ACC]				Pin Manager: Grid View																							
Package:	QFN48		Pin No:	44	45	46	47	48	1	2	3	4	5	6	7	8	9	10	11	12	13	16	17	18	19	20	21	22	23	24	25	26	27	30	31	32	33	34	35	36	37	38	39	40			
				Port A ▼							Port B ▼							Port C ▼							Port D ▼							Port E ▼							Port F ▼								
Module	Function	Direction	0	1	2	3	4	5	6	7	0	1	2	3	4	5	0	1	2	3	4	5	6	7	0	1	2	3	4	5	6	7	0	1	2	3	4	5	6	7	0	1	2	3	4	5	6
ADC0	AINx	input																																													
	CLKI	input																																													
CLKCTRL ▼	CLKO	output																																													
	TOSC1	input																																													
	TOSC2	input																																													
Pin Module ▼	GPIO	input																																													
	GPIO	output																																													
RSTCTRL	RESET	input																																													

Figure 4-7. GPIO Pin Resource - Package



4.4 MCC Pin Resource Configuration

Figure 4-8. Project Resources - Pin Module

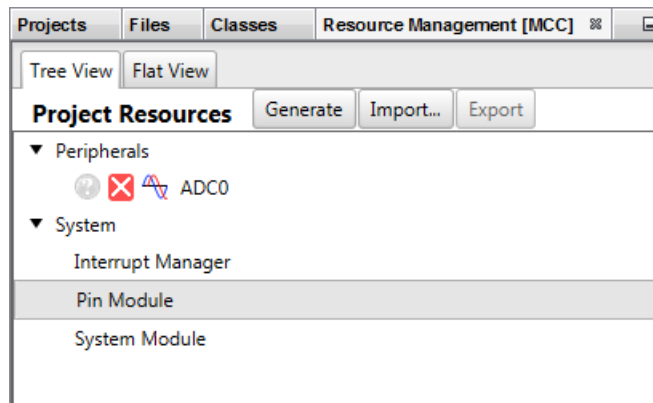


Figure 4-9. Pin Module Configuration

Pin Module									
Selected Package : QFP48									
Pin Name ^	Module	Function	Custom Name	OUTPUT	START HIGH	INVEN	PULLUPEN	ISC	
PD1	ADC0	AINx		<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	Digital Input Buffer disabled	
PF5	Pin Module	GPIO	IO_PF5	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	Interrupt disabled but input buffer enabled	

4.5 MCC Interrupt Manager Resource Configuration

Figure 4-10. Project Resources - Interrupt Manager

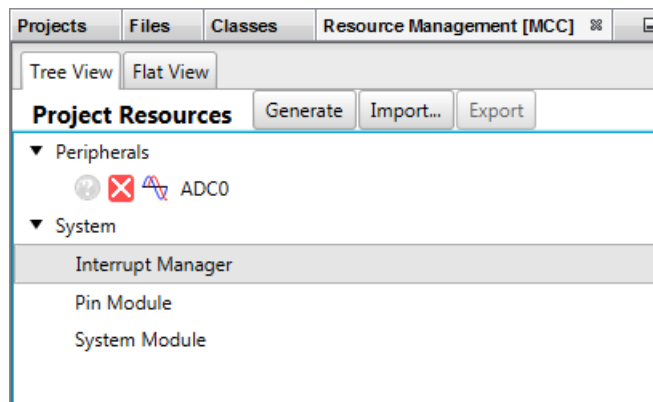


Figure 4-11. Interrupt Manager Configuration

The screenshot shows the 'Interrupt Manager' configuration window. The 'Easy Setup' tab is selected. The configuration is for the 'ADC0' module. The 'Interrupt Setting' section has 'Global Interrupt Enable' disabled. The 'Interrupt Priority' section has 'Round-robin Scheduling Enable' disabled, 'Interrupt Level Priority' set to 0, and 'Interrupt Vector with High Priority' set to 0. The 'Interrupt Vector' section has 'Compact Vector Table Enable' disabled and 'Interrupt Vector Select Enable' disabled. The 'Module Interrupts' table shows the following configuration:

Module	Interrupt	Enable
BOD	VLM	<input type="checkbox"/>
ADC0	RESRDY	<input checked="" type="checkbox"/>
ADC0	WCMP	<input checked="" type="checkbox"/>

4.6 MCC Code Generation

When the code is configured (as shown in the previous figures), click the **Generate** button on the “Project Resources” window. Code generated by the MCC is modular. Therefore main, system, and peripheral code are all in individual files. Also, each peripheral has its own header file.

Editing of `main.c` is always required to add functionality to your program. Review the generated files to find any functions or macros you may need in your code.

Figure 4-12. Code Generated in Project Tree

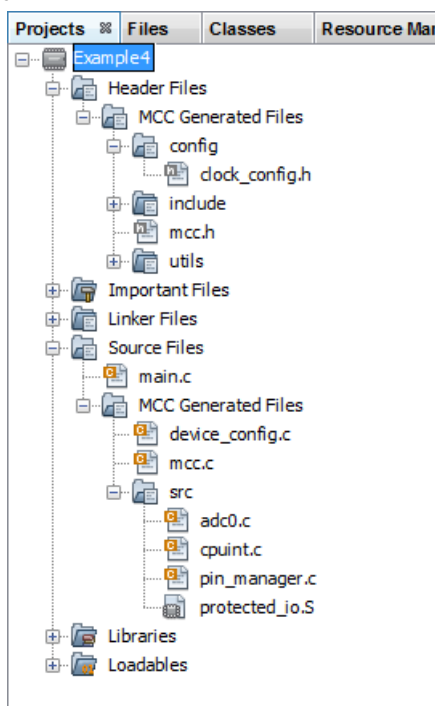


Figure 4-13. Code Generation Progress in Output Window

```

Kits MPLAB® Code Configurator PKOB nano-ADC_MCC_NANO ADC_MCC_NANO (Build, Load, ...)
18:59:43.259 INFO: .....
18:59:43.260 INFO: Generation Results
18:59:43.260 INFO: .....
18:59:43.277 INFO: main.c Success.
18:59:43.277 INFO: mcc_generated_files\config\clock_config.h Success.
18:59:43.279 INFO: mcc_generated_files\device_config.c Success.
18:59:43.279 INFO: mcc_generated_files\include\adc0.h Success.
18:59:43.279 INFO: mcc_generated_files\include\ccp.h Success.
18:59:43.280 INFO: mcc_generated_files\include\cpuint.h Success.
18:59:43.280 INFO: mcc_generated_files\include\pin_manager.h Success.
18:59:43.280 INFO: mcc_generated_files\include\port.h Success.
18:59:43.280 INFO: mcc_generated_files\include\protected_io.h Success.
18:59:43.281 INFO: mcc_generated_files\include\rstctrl.h Success.
18:59:43.281 INFO: mcc_generated_files\mcc.c Success. Auto-merged.
18:59:43.281 INFO: mcc_generated_files\mcc.h Success. Auto-merged.
18:59:43.281 INFO: mcc_generated_files\src\adc0.c Success.
18:59:43.282 INFO: mcc_generated_files\src\cpuint.c Success.
18:59:43.282 INFO: mcc_generated_files\src\pin_manager.c Success.
18:59:43.282 INFO: mcc_generated_files\src\protected_io.S Success.
18:59:43.282 INFO: mcc_generated_files\utils\assembler.h Success.
18:59:43.283 INFO: mcc_generated_files\utils\assembler\gas.h Success.
18:59:43.283 INFO: mcc_generated_files\utils\assembler\iar.h Success.
18:59:43.283 INFO: mcc_generated_files\utils\atomic.h Success.
18:59:43.283 INFO: mcc_generated_files\utils\compiler.h Success.
18:59:43.284 INFO: mcc_generated_files\utils\interrupt_avr8.h Success.
18:59:43.284 INFO: mcc_generated_files\utils\utils.h Success.
18:59:43.284 INFO: mcc_generated_files\utils\utils_assert.h Success.
18:59:43.332 INFO: .....
18:59:43.332 INFO: Generation complete (total time: 1771 milliseconds)
18:59:43.332 INFO: .....

```

4.7 main.c Modified Code

The `main.c` template file has been edited as shown below. Some comments have been removed.

Note: `<xc.h>` is automatically included by `"mcc_generated_files/mcc.h"`.

```

/*
(c) 2018 Microchip Technology Inc. and its subsidiaries.

```

```

/*
    <See generated main.c file for additional copyright information.>

#include "mcc_generated_files/mcc.h"
adc_0_channel_t channel = ADC_MUXPOS_AIN1_gc;

/*
    Main application
*/
int main(void)
{
    /* Initializes MCU, drivers and middleware */
    SYSTEM_Initialize();

    //Enable ADC and start conversion
    ADC0_Enable();
    ADC0_StartConversion(channel);

    while (1){
        if (ADC0_IsConversionDone())
        {
            if(ADC0_GetWindowResult())
            {
                PORTF.OUTCLR = PIN5_bm; // clear PF5 - LED on

            }
            else
            {
                PORTF.OUTSET = PIN5_bm; // set PF5 - LED off
            }
        }
    }
}
/**
    End of File
*/

```

4.7.1 ADC Associated Variables

The channel variable is needed for the `ADC0_StartConversion()` function.

```
adc_0_channel_t channel = ADC_MUXPOS_AIN1_gc;
```

In `adc0.h`, `adc_0_channel_t` is defined as type `ADC_MUXPOS_t`.

```
typedef ADC_MUXPOS_t adc_0_channel_t;
```

In the device-specific `io.h` file (in this case `iom4809.h`), `ADC_MUXPOS_t` is declared (`ADC_MUXPOS_AIN2_gc` through `ADC_MUXPOS_AIN14_gc` removed for brevity).

```

/* Analog Channel Selection Bits select */
typedef enum ADC_MUXPOS_enum
{
    ADC_MUXPOS_AIN0_gc = (0x00<<0), /* ADC input pin 0 */
    ADC_MUXPOS_AIN1_gc = (0x01<<0), /* ADC input pin 1 */
    :
    ADC_MUXPOS_AIN15_gc = (0x0F<<0), /* ADC input pin 15 */
    ADC_MUXPOS_DACREF_gc = (0x1C<<0), /* AC DAC Reference */
    ADC_MUXPOS_TEMPSENSE_gc = (0x1E<<0), /* Temperature sensor */
    ADC_MUXPOS_GND_gc = (0x1F<<0), /* 0V (GND) */
} ADC_MUXPOS_t;

```

4.7.2 ADC Window Comparison

The functions used to execute ADC operation and comparison may be found in the `adc0.c` file.

- `ADC0_Enable()` - Enable the ADC module.
- `ADC0_StartConversion(channel)` - Start the ADC conversion.
- `ADC0_IsConversionDone()` - In the `while()` loop, check for when a conversion is complete.

Light LED if Potentiometer Value Below ADC ...

- `ADC0_GetWindowResult()` - If conversion result is below threshold the value is true (turn on LED); if not, the value is false (turn off LED).

5. Flash LED after EEData Write and Read

This example demonstrates how to write to and read from EEPROM Data (EE Data) memory. After writing and reading complete successfully, the LED is flashed. To view EEPROM memory before and after writing, open

Window>Target Memory Views>EEPROM Memory and then Read Device Memory .

Again, MPLAB Code Configurator (MCC) is used to generate most of the code. To find out how to install and get the user's guide for MCC, see:

[4. Light LED if Potentiometer Value Below ADC Value](#)

For this example, the MCC GUI was set up as shown in the following sections.

5.1 MCC System Resource Configuration

Figure 5-1. Project Resources - System Module

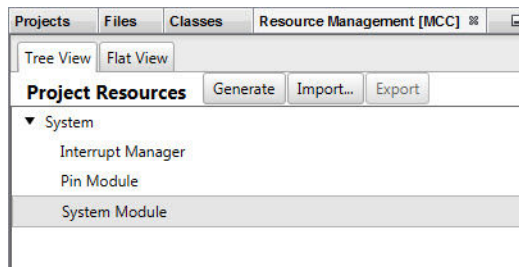


Figure 5-2. System Module Configuration

The screenshot shows the 'System Module' configuration window. The 'Easy Setup' tab is active. The configuration is organized into four expandable sections:

- Clock Control:**
 - Main Clock(Hz): 3333333
 - Clock Source: Internal Oscillator
 - Internal Oscillator Frequency: 20 MHz
 - External Clock(Hz): $1 \leq 1000000 \leq 20000000$
 - Prescaler Enable: ☒
 - Prescaler: 6X
 - Clock Out Enable: ☐
- Watchdog Timer:**
 - WDT Period: Off
 - WDT Window: Off
- Brown-out Detector:**
 - BOD Operation Mode: Disabled
 - BOD Level: 1.8 V
 - BOD Sampling Frequency: 1kHz sampling frequency
 - BOD Operation in Sleep Mode: Disabled
- Voltage Level Monitor:**
 - VLM configuration: Interrupt when supply goes below VLM level
 - Interrupt Enable: ☐
 - VLM Level: VLM threshold 5% above BOD level

5.2 MCC Memory Resource Configuration

To add EE Data to Project Resources:

1. Under Device Resources, find and expand NVMCTRL (Non-Volatile Memory Control).
2. Click on the green plus sign to add under Project Resources.
3. Click on NVMCTRL to view resource configuration settings. For this example, no changes will be made.

Figure 5-3. NVMCTRL (EE Data) Resource Selection

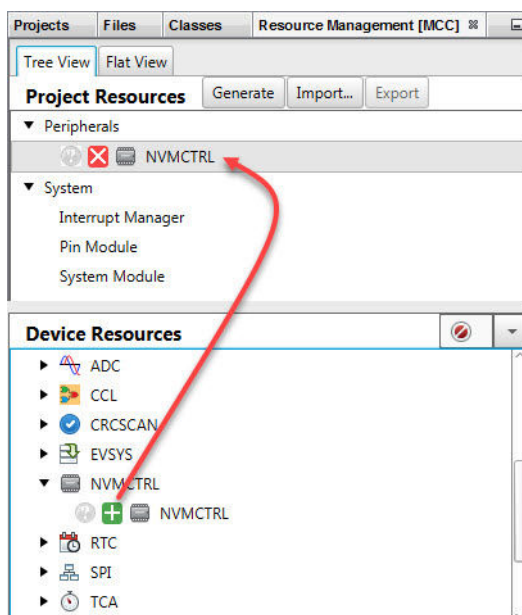
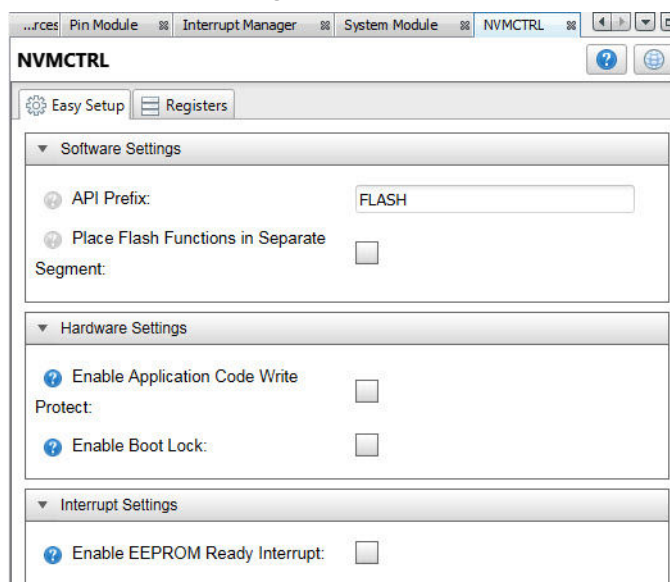


Figure 5-4. NVMCTRL (EE Data) Resource Configuration



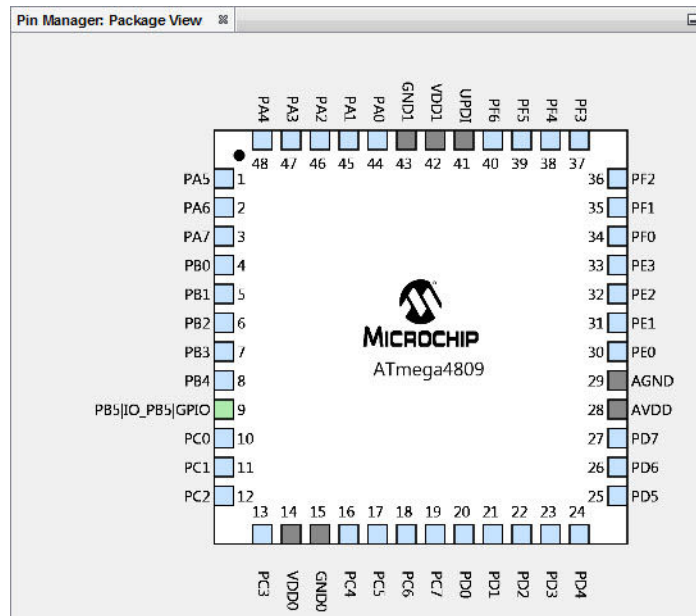
5.3 MCC GPIO Pin Resource Configuration

In order to flash the LED, Port B pin 5 must be set as an output.

Figure 5-5. GPIO Pin Resource - Grid

Output	Configuration Bits		Pin Manager: Grid View ⓘ																			
Package:	QFP48	▼	Pin No:	44	45	46	47	48	1	2	3	4	5	6	7	8	9	10	11	12	13	16
			Port A ▼								Port B ▼								Port C ▼			
Module	Function	Direction	0	1	2	3	4	5	6	7	0	1	2	3	4	5	0	1	2	3	4	
CLKCTRL ▼	CLKI	input	🔒																			
	CLKO	output								🔒												
	TOSC1	input																				
	TOSC2	input																				
Pin Module ▼	GPIO	input	🔒	🔒	🔒	🔒	🔒	🔒	🔒	🔒	🔒	🔒	🔒	🔒	🔒	🔒	🔒	🔒	🔒	🔒	🔒	
	GPIO	output	🔒	🔒	🔒	🔒	🔒	🔒	🔒	🔒	🔒	🔒	🔒	🔒	🔒	🔒	🔒	🔒	🔒	🔒	🔒	
RSTCTRL	RESET	input																				

Figure 5-6. GPIO Pin Resource - Package



5.4 MCC Pin Resource Configuration

Under Project Resources, click on “Pin Module” to view Pin Module configuration settings.

PB5 appears in the Pin Module window because it was selected in the Pin Manager: Grid View window. No changes to the pin configuration will be made for this example.

Figure 5-7. Project Resources - Pin Module

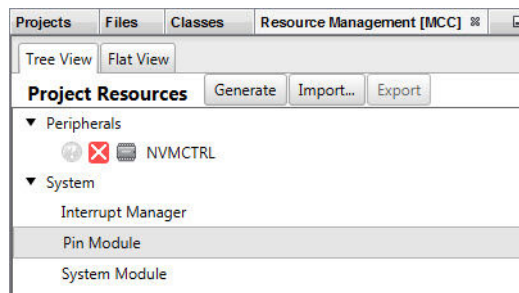
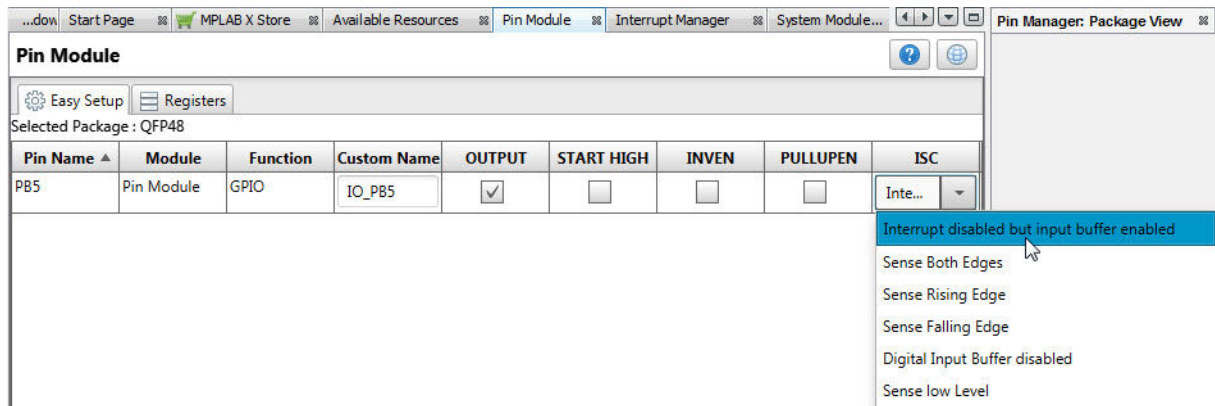


Figure 5-8. Pin Module Configuration



5.5 MCC Code Generation

When the code is configured (as shown in the previous figures), click the **Generate** button on the “Project Resources” window. Code generated by the MCC is modular. Therefore main, system, and peripheral code are all in individual files. Also, each peripheral has its own header file.

Editing of `main.c` is always required to add functionality to your program. Review the generated files to find any functions or macros you may need in your code.

Figure 5-9. Code Generated in Project Tree

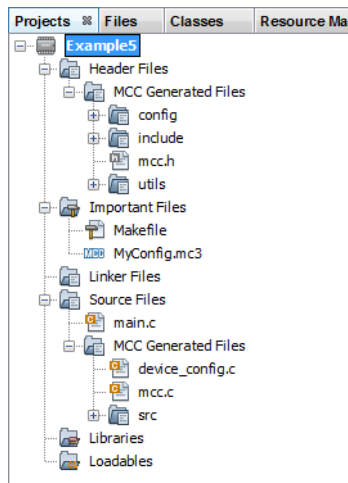


Figure 5-10. Code Generation in the Output Window

Out...	Configuration Bits	Pin Manager: Grid View
Kits	MPLAB® Code Configurator	
11:22:35.780	INFO: *****	
11:22:35.781	INFO: Generation Results	
11:22:35.781	INFO: *****	
11:22:35.798	INFO: main.c	Success. New file.
11:22:35.799	INFO: mcc_generated_files\config\clock_config.h	Success. New file.
11:22:35.799	INFO: mcc_generated_files\device_config.c	Success. New file.
11:22:35.799	INFO: mcc_generated_files\include\ccp.h	Success. New file.
11:22:35.800	INFO: mcc_generated_files\include\cpuint.h	Success. New file.
11:22:35.800	INFO: mcc_generated_files\include\nvmctrl.h	Success. New file.
11:22:35.800	INFO: mcc_generated_files\include\pin_manager.h	Success. New file.
11:22:35.801	INFO: mcc_generated_files\include\port.h	Success. New file.
11:22:35.801	INFO: mcc_generated_files\include\protected_io.h	Success. New file.
11:22:35.801	INFO: mcc_generated_files\include\rstctrl.h	Success. New file.
11:22:35.802	INFO: mcc_generated_files\mcc.c	Success. New file.
11:22:35.802	INFO: mcc_generated_files\mcc.h	Success. New file.
11:22:35.802	INFO: mcc_generated_files\src\cpuint.c	Success. New file.
11:22:35.803	INFO: mcc_generated_files\src\nvmctrl.c	Success. New file.
11:22:35.803	INFO: mcc_generated_files\src\pin_manager.c	Success. New file.
11:22:35.803	INFO: mcc_generated_files\src\protected_io.S	Success. New file.
11:22:35.803	INFO: mcc_generated_files\utils\assembler.h	Success. New file.
11:22:35.804	INFO: mcc_generated_files\utils\assembler\gas.h	Success. New file.
11:22:35.804	INFO: mcc_generated_files\utils\assembler\iar.h	Success. New file.
11:22:35.804	INFO: mcc_generated_files\utils\atomic.h	Success. New file.
11:22:35.805	INFO: mcc_generated_files\utils\compiler.h	Success. New file.
11:22:35.805	INFO: mcc_generated_files\utils\interrupt_avr8.h	Success. New file.
11:22:35.805	INFO: mcc_generated_files\utils\utils.h	Success. New file.
11:22:35.806	INFO: mcc_generated_files\utils\utils_assert.h	Success. New file.
11:22:35.840	INFO: *****	
11:22:35.858	INFO: Generation complete (total time: 1951 milliseconds)	
11:22:35.858	INFO: *****	

5.6 main.c Modified Code

The main.c template file has been edited as shown below. Some comments have been removed.

```

/*
    (c) 2018 Microchip Technology Inc. and its subsidiaries.

    <See generated main.c file for additional copyright information.>
*/

#include "mcc_generated_files/mcc.h"
#include <util/delay.h>

#define LED_ON_OFF_DELAY 500
#define NUM_EE_VALUES 8
#define EE_ADR_START 8

eeprom_adr_t ee_address;
nvmctrl_status_t status;
volatile uint8_t RAMArray[NUM_EE_VALUES];

/*
    Main application
*/
int main(void)
{
    /* Initializes MCU, drivers and middleware */
    SYSTEM_Initialize();

    /* Declare loop variable */
    uint8_t i;

    if (!FLASH_Initialize()) {

        ee_address = EE_ADR_START;

        // Write EEPROM Data
        for(i=0; i<NUM_EE_VALUES; i++){
            status = FLASH_WriteEepromByte(ee_address, i);
            ee_address++;
        }
    }
}

```

```

    ee_address = EE_ADR_START;

    // Read EEPROM Data
    for(i=0; i<NUM_EE_VALUES; i++){
        RAMArray[i] = FLASH_ReadEepromByte(ee_address);
        ee_address++;
    }

}

while (1){
    PORTF.OUTTGL = PIN5_bm; // toggle PB5
    _delay_ms(LED_ON_OFF_DELAY);
}
}
/**
End of File
*/

```

5.6.1 EE Data Associated Variables

Variables used to store data from an EE Data read or write must match the types specified in the read/write function prototype, referenced from `mcc.h`, and found in `nvmctrl.h`:

```

uint8_t FLASH_ReadEepromByte(eeprom_adr_t eeprom_adr);
nvmctrl_status_t FLASH_WriteEepromByte(eeprom_adr_t eeprom_adr, uint8_t data);

```

From `stdint.h` (also referenced), `uint8_t` is the same as `unsigned char`.

5.6.2 Write to EE Data

In this example, data is written to EE Data and then read back.

The function to write one byte of data to EE Data, `FLASH_WriteEepromByte()`, may be found in `nvmctrl.c`.

Within this function is a loop that waits until any previous writes have finished before starting the next write. So there is no need to check if a write is complete, i.e., using `FLASH_IsEepromReady()`.

5.6.3 Read from EE Data

After EE Data is written, memory values are read into a RAM array. The function to read one byte of data to EE Data, `FLASH_ReadEepromByte()`, may be found in `nvmctrl.c`.

Once the values are read, a while loop flashes the LED to indicate successful program completion.

6. Run Code in MPLAB X IDE

Follow the instructions below to execute example code in MPLAB X IDE.

6.1 Create a Project

1. Launch MPLAB X IDE.

2. From the IDE, launch the New Project Wizard



Follow the screens to create a new project:

1. **Choose Project:** Select “Microchip Embedded” and then select “Standalone Project.”
2. **Select Device and Tool:** Select the example device. Select your hardware debug tool, SNxxxxxx. If you do not see a serial number (SN) under your debug tool name, ensure that your debug tool is correctly installed. See your debug tool documentation for details.
3. Select Header: None.
4. Select Plugin Board: None.
5. **Select Compiler:** Select XC8 (latest version number) [bin location]. If you do not see a compiler under XC8, ensure the compiler is correctly installed and that MPLAB X IDE is aware of it (*Tools>Options>Embedded>Build Tools*). See MPLAB XC8 and MPLAB X IDE documentation for details.
6. **Select Project Name and Folder:** Name the project.

6.2 Select the Common Compiler Interface (CCI)

After your project is created, right click on the project name in the Projects window and select Properties. In the dialog box, click on the “XC8 Compiler” category, select the “Preprocessing and messages” option category, and check “Use CCI syntax.” Click the **OK** button.

6.3 Debug the Examples

Do one of the following, based on the example you are using:

1. For examples 1, 2, and 3, create a file to hold the example code:
 - 1.1. Right click on the “Source Files” folder in the Projects window. Select *New>main.c*. The “New main.c” dialog opens.
 - 1.2. Under “File name,” enter a name (e.g., *examplen*), where *n* is the example number.
 - 1.3. Click **Finish**. The file opens in an editor window.
 - 1.4. Delete the template code in the file. Then cut and paste the example code from this user’s guide into the empty editor window and select *File>Save*.
2. For examples 4 and 5, follow the instructions in each section to generate code using MCC and then edit the *main.c* file with the code shown.



Finally, select Debug Project to build, download to a device, and execute the code. View the demo board



LEDs for output. Click Finish Debug Session to end execution.

7. Get Hardware and Software

For the MPLAB XC8 projects in this document, the ATmega4809 Curiosity Nano development board is powered from and communicates with the PC using a USB connection. MPLAB X IDE was used for development.

Get MPLAB X IDE and MPLAB XC8 C Compiler

MPLAB X IDE v5.45 and later can be found at:

www.microchip.com/mplab/mplab-x-ide

The MPLAB XC8 C compiler v2.31 and later can be found at:

www.microchip.com/mplab/compilers

Get the MPLAB Code Configurator (MCC)

In MPLAB X IDE, go to *Tools>Plugins>Available Plugins* and install “MPLAB Code Configurator”.

More on MCC can be found at:

www.microchip.com/mplab/mplab-code-configurator

Get AVR® MCUs

The AVR MCU used in the examples are available at:

www.microchip.com/ATmega4809

Get the ATmega4809 Curiosity Nano

The ATmega4809 Curiosity Nano board is available at:

www.microchip.com/DevelopmentTools/ProductDetails/DM320115

About the Potentiometer in Example 4

SparkFun Trimmer 10KΩ 0.5W PC Pin Top

8. Additional Information

Some videos with further information on using AVR devices in MPLAB X IDE.

[Import Studio 7 Project into MPLAB X IDE](#)

[Create a New Project/Project Dashboard](#)

[Context Datasheet Help & AVR® Interrupts](#)

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PART NO. **[X]⁽¹⁾** **-** **X** **/XX** **XXX**

Device Tape and Reel Option Temperature Range Package Pattern

Device:	PIC16F18313, PIC16LF18313, PIC16F18323, PIC16LF18323	
Tape and Reel Option:	Blank	= Standard packaging (tube or tray)
	T	= Tape and Reel ⁽¹⁾
Temperature Range:	I	= -40°C to +85°C (Industrial)
	E	= -40°C to +125°C (Extended)
Package: ⁽²⁾	JQ	= UQFN
	P	= PDIP
	ST	= TSSOP
	SL	= SOIC-14
	SN	= SOIC-8
	RF	= UDFN
Pattern:	QTP, SQTP, Code or Special Requirements (blank otherwise)	

Examples:

- PIC16LF18313- I/P Industrial temperature, PDIP package
- PIC16F18313- E/SS Extended temperature, SSOP package

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