

## **CUDA Math API**

API Reference Manual

## Table of Contents

Chapter 1. Modules	1
1.1. Half Precision Intrinsics	2
Half Arithmetic Functions	2
Half2 Arithmetic Functions	2
Half Comparison Functions	2
Half2 Comparison Functions	2
Half Precision Conversion and Data Movement	2
Half Math Functions	2
Half2 Math Functions	2
1.1.1. Half Arithmetic Functions	2
habs	2
hadd	3
hadd_sat	3
hdiv	3
hfma	4
hfma_relu	4
hfma_sat	4
hmul	5
hmul_sat	5
hneg	6
hsub	6
hsub_sat	6
1.1.2. Half2 Arithmetic Functions	6
h2div	7
habs2	7
hadd2	
hadd2_sat	7
hcmadd	8
hfma2	9
hfma2_relu	
hfma2_sat	10
hmul2	10
hmul2_sat	11
hneg2	
hsub2	11
hsub2 sat	12

1.1.3. Half Comparison Functions	12
heq	12
hequ	13
hge	13
hgeu	14
hgt	14
hgtu	15
hisinf	15
hisnan	16
hle	16
hleu	16
hlt	17
hltu	17
hmax	18
hmax_nan	18
hmin	18
hmin_nan	19
hne	19
hneu	19
1.1.4. Half2 Comparison Functions	20
hbeq2	20
hbequ2	21
hbge2	21
hbgeu2	22
hbgt2	22
hbgtu2	23
hble2	24
hbleu2	24
hblt2	25
hbltu2	25
hbne2	26
hbneu2	27
heq2	27
hequ2	28
hge2	28
hgeu2	29
hatu?	30

hisnan2	30
hle2	31
hleu2	31
hlt2	32
hltu2	32
hmax2	33
hmax2_nan	33
hmin2	33
hmin2_nan	33
hne2	34
hneu2	34
1.1.5. Half Precision Conversion and Data Movement	35
double2half	35
float22half2_rn	35
float2half	36
float2half2_rn	36
float2half_rd	37
float2half_rn	37
float2half_ru	38
float2half_rz	38
floats2half2_rn	39
half22float2	39
half2float	40
half2half2	4C
half2int_rd	4C
half2int_rn	41
half2int_ru	41
half2int_rz	42
half2ll_rd	42
half2ll_rn	43
half2ll_ru	43
half2ll_rz	44
half2short_rd	44
half2short_rn	45
half2short_ru	45
half2short_rz	46
half2uint_rd	46
half2uint_rn	47

half2uint_ru	47
half2uint_rz	
half2ull_rd	48
half2ull_rn	49
half2ull_ru	49
half2ull_rz	50
half2ushort_rd	50
half2ushort_rn	51
half2ushort_ru	51
half2ushort_rz	52
half_as_short	52
half_as_ushort	53
halves2half2	53
high2float	54
high2half	54
high2half2	54
highs2half2	55
int2half_rd	55
int2half_rn	56
int2half_ru	56
int2half_rz	57
ldca	57
ldca	57
ldcg	58
ldcg	58
ldcs	58
ldcs	58
ldcv	59
ldcv	59
ldg	59
ldg	59
ldlu	60
ldlu	60
ll2half_rd	60
ll2half_rn	61
ll2half_ru	61
ll2half_rz	
low2float	62

low2half	62
low2half2	63
lowhigh2highlow	63
lows2half2	64
shfl_down_sync	64
shfl_down_sync	65
shfl_sync	66
shfl_sync	66
shfl_up_sync	67
shfl_up_sync	68
shfl_xor_sync	68
shfl_xor_sync	69
short2half_rd	70
short2half_rn	70
short2half_ru	71
short2half_rz	71
short_as_half	72
stcg	72
stcg	72
stcs	73
stcs	73
stwb	73
stwb	73
stwt	74
stwt	74
uint2half_rd	74
uint2half_rn	75
uint2half_ru	75
uint2half_rz	76
ull2half_rd	76
ull2half_rn	77
ull2half_ru	77
ull2half_rz	78
ushort2half_rd	78
ushort2half_rn	
ushort2half_ru	
ushort2half_rz	
	QΩ

1.1.6. Half Math Functions	80
hceil	8´
hcos	8′
hexp	8′
hexp10	82
hexp2	82
hfloor	83
hlog	83
hlog10	84
hlog2	84
hrcp	84
hrint	85
hrsqrt	85
hsin	86
hsqrt	86
htrunc	87
1.1.7. Half2 Math Functions	87
h2ceil	87
h2cos	88
h2exp	88
h2exp10	88
h2exp2	89
h2floor	89
h2log	90
h2log10	90
h2log2	9 <sup>^</sup>
h2rcp	9 <sup>^</sup>
h2rint	9′
h2rsqrt	92
h2sin	
h2sqrt	
h2trunch	
.2. Bfloat16 Precision Intrinsics	
Bfloat16 Arithmetic Functions	
Bfloat162 Arithmetic Functions	
Bfloat16 Comparison Functions	
Bfloat162 Comparison Functions	
Bfloat16 Precision Conversion and Data Movement	

Bfloat16 Math Functions	94
Bfloat162 Math Functions	94
1.2.1. Bfloat16 Arithmetic Functions	94
h2div	94
habs	95
hadd	95
hadd_sat	
hdiv	96
hfma	96
hfma_relu	96
hfma_sat	97
hmul	97
hmul_sat	98
hneg	98
hsub	98
hsub_sat	99
1.2.2. Bfloat162 Arithmetic Functions	99
habs2	99
hadd2	100
hadd2_sat	100
hcmadd	101
hfma2	101
hfma2_relu	101
hfma2_sat	102
hmul2	103
hmul2_sat	103
hneg2	104
hsub2	104
hsub2_sat	104
1.2.3. Bfloat16 Comparison Functions	104
heq	105
hequ	
hge	106
hgeu	
hgt	
bgtu	
hisinf	
— hisnan	108

hle	109
hleu	
hlt	110
hltu	110
hmax	111
hmax_nan	111
hmin	111
hmin_nan	111
hne	112
hneu	112
1.2.4. Bfloat162 Comparison Functions	113
hbeq2	113
hbequ2	113
hbge2	114
hbgeu2	115
hbgt2	115
hbgtu2	116
hble2	117
hbleu2	117
hblt2	118
hbltu2	119
hbne2	119
hbneu2	120
heq2	121
hequ2	121
hge2	122
hgeu2	122
hgt2	
hgtu2	123
hisnan2	124
hle2	124
hleu2	125
hlt2	125
hltu2	126
hmax2	126
hmax2_nan	
hmin2	127
hmin? nan	127

hne2	128
hneu2	128
1.2.5. Bfloat16 Precision Conversion and Data Movement	129
bfloat1622float2	129
bfloat162bfloat162	129
bfloat162float	130
bfloat162int_rd	130
bfloat162int_rn	131
bfloat162int_ru	131
bfloat162int_rz	132
bfloat162ll_rd	132
bfloat162ll_rn	133
bfloat162ll_ru	133
bfloat162ll_rz	134
bfloat162short_rd	134
bfloat162short_rn	135
bfloat162short_ru	135
bfloat162short_rz	136
bfloat162uint_rd	136
bfloat162uint_rn	137
bfloat162uint_ru	137
bfloat162uint_rz	138
bfloat162ull_rd	138
bfloat162ull_rn	139
bfloat162ull_ru	139
bfloat162ull_rz	140
bfloat162ushort_rd	140
bfloat162ushort_rn	141
bfloat162ushort_ru	141
bfloat162ushort_rz	142
bfloat16_as_short	142
bfloat16_as_ushort	143
double2bfloat16	143
float22bfloat162_rn	144
float2bfloat16	144
float2bfloat162_rn	145
float2bfloat16_rd	145
float2hfloat16 rn	146

_	_float2bfloat16_ru	146
_	_float2bfloat16_rz	.147
_	_floats2bfloat162_rn	.147
_	_halves2bfloat162	.148
_	_high2bfloat16	. 148
_	_high2bfloat162	. 149
_	_high2float	. 149
_	_highs2bfloat162	150
_	_int2bfloat16_rd	.150
_	_int2bfloat16_rn	.151
_	_int2bfloat16_ru	.151
_	_int2bfloat16_rz	. 152
_	_ldca	.152
_	_ldca	.152
_	_ldcg	.153
_	_ldcg	.153
_	_ldcs	. 153
_	_ldcs	. 153
_	_ldcv	. 154
_	_ldcv	. 154
	_ldg	.154
_	_ldg	.154
	_ldlu	155
_	_ldlu	155
_	_ll2bfloat16_rd	.155
_	_ll2bfloat16_rn	.156
	_ll2bfloat16_ru	.156
_	_ll2bfloat16_rz	. 157
	_low2bfloat16	.157
	_low2bfloat162	.158
	_low2float	.158
	_lowhigh2highlow	. 159
	_lows2bfloat162	.159
_	_shfl_down_sync	. 160
	_shfl_down_sync	. 160
	_shfl_sync	. 161
	_shfl_sync	. 162
_	_shfl_up_sync	. 163

shfl_up_sync	163
shfl_xor_sync	
shfl_xor_sync	165
short2bfloat16_rd	166
short2bfloat16_rn	166
short2bfloat16_ru	167
short2bfloat16_rz	167
short_as_bfloat16	168
stcg	168
stcg	168
stcs	169
stcs	169
stwb	169
stwb	169
stwt	170
stwt	170
uint2bfloat16_rd	170
uint2bfloat16_rn	171
uint2bfloat16_ru	171
uint2bfloat16_rz	172
ull2bfloat16_rd	172
ull2bfloat16_rn	173
ull2bfloat16_ru	173
ull2bfloat16_rz	174
ushort2bfloat16_rd	174
ushort2bfloat16_rn	175
ushort2bfloat16_ru	175
ushort2bfloat16_rz	176
ushort_as_bfloat16	176
1.2.6. Bfloat16 Math Functions	176
hceil	177
hcos	177
hexp	177
hexp10	178
hexp2	178
hfloor	179
hlog	179
hlog10	180

hlog2	
hrcp	180
hrint	181
hrsqrt	181
hsin	182
hsqrt	182
htrunc	183
1.2.7. Bfloat162 Math Functions	183
h2ceil	183
h2cos	184
h2exp	184
h2exp10	184
h2exp2	185
h2floor	185
h2log	186
h2log10	186
h2log2	187
h2rcp	187
h2rint	187
h2rsqrt	
h2sin	188
h2sqrt	189
h2trunc	189
1.3. Mathematical Functions	
1.4. Single Precision Mathematical Functions	190
acosf	190
acoshf	191
asinf	191
asinhf	192
atan2f	192
atanf	192
atanhf	193
cbrtf	193
ceilf	194
copysignf	
cosf	
coshf	
cospif	

cyl_bessel_i0f	196
cyl_bessel_i1f	196
erfcf	196
erfcinvf	197
erfcxf	197
erff	198
erfinvf	198
exp10f	199
exp2f	199
expf	199
expm1f	200
fabsf	200
fdimf	201
fdividef	201
floorf	202
fmaf	202
fmaxf	203
fminf	203
fmodf	204
frexpf	204
hypotf	205
ilogbf	205
isfinite	206
sinf	206
isnan	
Of	207
1f	
nf	208
ldexpf	
lgammaf	
Urintf	
llroundf	
log10f	
log1pf	
log2f	
logbf	
logf	
lrintf	212

roundf	212
max	213
min	213
modff	213
nanf	214
nearbyintf	214
nextafterf	214
norm3df	215
norm4df	215
normcdff	216
normcdfinvf	216
normf	217
oowf	217
rcbrtf	218
remainderf	218
remquof	219
- hypotf	219
rintf	220
norm3df	220
norm4df	221
normf	221
roundf	222
rsqrtf	222
scalblnf	222
scalbnf	223
signbit	223
sincosf	
sincospif	224
sinf	
sinhf	
sinpif	226
sartf	
anf	
anhf	227
gammaf	
truncf	
/Of	
,1f	

ynf	229
1.5. Double Precision Mathematical Functions	230
acos	230
acosh	230
asin	231
asinh	231
atan	232
atan2	232
atanh	233
cbrt	233
ceil	233
copysign	234
COS	234
cosh	234
cospi	235
cyl_bessel_i0	235
cyl_bessel_i1	236
erf	236
erfc	237
erfcinv	237
erfcx	237
erfinv	238
exp	238
exp10	239
exp2	239
expm1	239
fabs	240
fdim	240
floor	241
fma	241
fmax	242
fmin	242
fmod	243
frexp	243
hypot	
ilogb	
isfinite	
isinf	245

isnan	245
j0	246
j1	246
jn	247
ldexp	247
lgamma	248
llrint	248
llround	248
log	249
log10	249
log1p	250
log2	250
logb	251
lrint	251
lround	251
max	252
max	252
max	252
min	252
min	253
min	253
modf	253
nan	254
nearbyint	254
nextafter	
norm	255
norm3d	
norm4d	256
normcdf	256
normcdfinv	
pow	257
rcbrt	
remainder	
remquo	
rhypot	
rint	
rnorm	
rnorm3d	261

rnorm4d	261
round	262
rsqrt	262
scalbln	
scalbn	
signbit	
sin	264
sincos	264
sincospi	265
sinh	265
sinpi	
sqrt	266
tan	267
tanh	267
tgamma	267
trunc	
y0	268
y1	269
yn	269
1.6. Integer Mathematical Functions	270
abs	270
labs	270
llabs	270
llmax	270
llmin	271
max	272
max	272
max	
max	
max	0.50
max	273
max	273
max	
max	
min	

min	274
min	274
min	274
min	274
min	275
min	276
min	276
ullmax	276
ullmin	276
umax	276
umin	277
1.7. Single Precision Intrinsics	277
cosf	
exp10f	277
expf	278
fadd_rd	278
fadd_rn	279
fadd_ru	279
fadd_rz	279
fdiv_rd	280
fdiv_rn	280
fdiv_ru	281
	281
 fdividef	281
fmaf_ieee_rd	
fmaf_ieee_rn	
fmaf_ieee_ru	
fmaf_ieee_rz	
fmaf_rd	
fmaf_rn	
 fmaf_ru	
 fmaf_rz	
 fmul_rd	
— – fmul rn	

fmul_ru	286
fmul_rz	286
frcp_rd	
frcp_rn	287
frcp_ru	287
frcp_rz	288
frsqrt_rn	
fsqrt_rd	288
fsqrt_rn	289
fsqrt_ru	289
fsqrt_rz	290
fsub_rd	
fsub_rn	290
fsub_ru	291
fsub_rz	291
log10f	292
log2f	
logf	292
powf	293
saturatef	293
sincosf	294
sinf	294
tanf	295
1.8. Double Precision Intrinsics	
dadd_rd	
dadd_rn	296
dadd_ru	296
dadd_rz	
ddiv_rd	
ddiv_rn	
ddiv_ru	
ddiv_rz	
 dmul_rd	
 dmul_rn	
a	
dmul_rz	
drcp_rd	
dren rn	301

drcp_ru	301
drcp_rz	301
dsqrt_rd	
dsqrt_rn	302
dsqrt_ru	303
dsqrt_rz	303
dsub_rd	
dsub_rn	304
dsub_ru	304
dsub_rz	305
fma_rd	305
fma_rn	306
fma_ru	306
fma_rz	307
1.9. Integer Intrinsics	307
brev	308
brevll	308
byte_perm	308
clz	309
clzll	309
ffs	309
ffsll	309
funnelshift_l	310
funnelshift_lc	310
funnelshift_r	310
funnelshift_rc	311
hadd	311
mul24	311
mul64hi	312
mulhi	312
popc	312
popcll	313
rhadd	313
sad	313
uhadd	314
 umul24	314
 umul64hi	314
 umulhi	315

urhadd	315
usad	315
1.10. Type Casting Intrinsics	316
double2float_rd	316
double2float_rn	316
double2float_ru	316
double2float_rz	317
double2hiint	317
double2int_rd	317
double2int_rn	317
double2int_ru	318
double2int_rz	318
double2ll_rd	318
double2ll_rn	319
double2ll_ru	319
double2ll_rz	319
double2loint	319
double2uint_rd	320
double2uint_rn	320
double2uint_ru	320
double2uint_rz	321
double2ull_rd	321
double2ull_rn	321
double2ull_ru	322
double2ull_rz	322
double_as_longlong	322
float2int_rd	323
float2int_rn	323
float2int_ru	323
float2int_rz	323
float2ll_rd	324
float2ll_rn	324
float2ll_ru	324
float2ll_rz	325
float2uint_rd	325
float2uint_rn	325
float2uint_ru	325
float2uint_rz	326

float2ull_rd	
float2ull_rn	326
float2ull_ru	327
float2ull_rz	
float_as_int	327
float_as_uint	327
hiloint2double	
int2double_rn	
int2float_rd	328
int2float_rn	329
int2float_ru	329
int2float_rz	329
int_as_float	329
ll2double_rd	330
ll2double_rn	
ll2double_ru	
ll2double_rz	331
ll2float_rd	331
ll2float_rn	331
ll2float_ru	331
ll2float_rz	332
longlong_as_double	332
uint2double_rn	332
uint2float_rd	333
uint2float_rn	333
uint2float_ru	333
uint2float_rz	333
uint_as_float	334
ull2double_rd	334
ull2double_rn	
ull2double_ru	335
ull2double_rz	335
ull2float_rd	335
ull2float_rn	336
ull2float_ru	336
ull2float_rz	336
1.11. SIMD Intrinsics	
vahe?	227

vabs4	337
vabsdiffs2	337
vabsdiffs4	338
vabsdiffu2	338
vabsdiffu4	338
vabsss2	339
vabsss4	339
vadd2	
vadd4	340
vaddss2	340
vaddss4	
vaddus2	341
vaddus4	341
vavgs2	
vavgs4	
vavgu2	
vavgu4	
vcmpeq2	
vcmpeq4	
vcmpges2	
vcmpges4	
vcmpgeu2	
vcmpgeu4	
vcmpgts2	
vcmpgtu2	
vcmpqtu4	
vcmples2	
vcmples4	
vcmpleu2	
vcmpleu4	
vcmplts2	
vcmplts4	
vcmpltu2	
vcmpltu4	
vcmpne2	
vcmpne4	
	3/,0

vhaddu4	350
vmaxs2	350
vmaxs4	350
vmaxu2	351
vmaxu4	351
vmins2	351
vmins4	
vminu2	352
vminu4	352
vneg2	
vneg4	353
vnegss2	353
vnegss4	353
vsads2	354
vsads4	354
vsadu2	354
vsadu4	355
vseteq2	355
vseteq4	
vsetges2	356
vsetges4	356
vsetgeu2	
vsetgeu4	357
vsetgts2	
vsetgts4	
vsetgtu2	358
vsetgtu4	
vsetles2	358
vsetles4	359
vsetleu2	359
vsetleu4	359
vsetlts2	360
vsetlts4	360
vsetltu2	360
vsetltu4	
vsetne2	361
vsetne4	
vsuh2	362

vsub4	362
 vsubss2	
vsubss4	
vsubus2	
vsubus4	

## Chapter 1. Modules

#### Here is a list of all modules:

- ► Half Precision Intrinsics
  - ► Half Arithmetic Functions
  - ► Half2 Arithmetic Functions
  - Half Comparison Functions
  - ► Half2 Comparison Functions
  - Half Precision Conversion and Data Movement
  - Half Math Functions
  - Half2 Math Functions
- Bfloat16 Precision Intrinsics
  - ► Bfloat16 Arithmetic Functions
  - ► Bfloat162 Arithmetic Functions
  - Bfloat16 Comparison Functions
  - Bfloat162 Comparison Functions
  - Bfloat16 Precision Conversion and Data Movement
  - ► Bfloat16 Math Functions
  - ► Bfloat162 Math Functions
- Mathematical Functions
- Single Precision Mathematical Functions
- Double Precision Mathematical Functions
- Integer Mathematical Functions
- Single Precision Intrinsics
- Double Precision Intrinsics
- Integer Intrinsics
- Type Casting Intrinsics
- SIMD Intrinsics

## 1.1. Half Precision Intrinsics

This section describes half precision intrinsic functions that are only supported in device code. To use these functions, include the header file cuda fp16.h in your program.

Half Arithmetic Functions

Half2 Arithmetic Functions

Half Comparison Functions

Half2 Comparison Functions

Half Precision Conversion and Data Movement

Half Math Functions

Half2 Math Functions

## 1.1.1. Half Arithmetic Functions

Half Precision Intrinsics

To use these functions, include the header file cuda fp16.h in your program.

\_\_device\_\_ \_half \_\_habs (const \_\_half a)

Calculates the absolute value of input half number and returns the result.

#### **Parameters**

а

- half. Is only being read.

#### Returns

half

The

absolute value of a.

#### Description

Calculates the absolute value of input half number and returns the result.

\_\_device\_\_ \_half \_\_hadd (const \_\_half a, const \_\_half b)

Performs half addition in round-to-nearest-even mode.

#### Description

Performs half addition of inputs a and b, in round-to-nearest-even mode.

\_\_device\_\_ \_half \_\_hadd\_sat (const \_\_half a, const \_\_half b)

Performs half addition in round-to-nearest-even mode, with saturation to [0.0, 1.0].

#### **Parameters**

a

- half. Is only being read.

b

- half. Is only being read.

#### Returns

half

▶ The

sum of a and b, with respect to saturation.

## Description

Performs half add of inputs a and b, in round-to-nearest-even mode, and clamps the result to range [0.0, 1.0]. NaN results are flushed to +0.0.

\_\_device\_\_ \_half \_\_hdiv (const \_\_half a, const \_\_half b)

Performs half division in round-to-nearest-even mode.

## Description

Divides half input a by input b in round-to-nearest mode.

# \_\_device\_\_ \_ half \_\_hfma (const \_\_half a, const \_\_half b, const half c)

Performs half fused multiply-add in round-to-nearest-even mode.

## Description

Performs half multiply on inputs a and b, then performs a half add of the result with c, rounding the result once in round-to-nearest-even mode.

Performs half fused multiply-add in round-to-nearest-even mode with relu saturation.

#### **Parameters**

- а
- half. Is only being read.
- b
- half. Is only being read.
- C
- half. Is only being read.

#### Returns

half

▶ The

result of fused multiply-add operation on a, b, and c with relu saturation.

## Description

Performs half multiply on inputs a and b, then performs a half add of the result with c, rounding the result once in round-to-nearest-even mode. Then negative result is clamped to 0. NaN result is converted to canonical NaN.

Performs half fused multiply-add in round-to-nearest-even mode, with saturation to [0.0, 1.0].

#### **Parameters**

a

- half. Is only being read.

b

- half. Is only being read.

c

- half. Is only being read.

#### Returns

half

► The

result of fused multiply-add operation on a, b, and c, with respect to saturation.

## Description

Performs half multiply on inputs a and b, then performs a half add of the result with c, rounding the result once in round-to-nearest-even mode, and clamps the result to range [0.0, 1.0]. NaN results are flushed to +0.0.

\_\_device\_\_ \_half \_\_hmul (const \_\_half a, const \_\_half b)

Performs half multiplication in round-to-nearest-even mode.

## Description

Performs half multiplication of inputs a and b, in round-to-nearest mode.

\_\_device\_\_ \_half \_\_hmul\_sat (const \_\_half a, const \_\_half b)

Performs half multiplication in round-to-nearest-even mode, with saturation to [0.0, 1.0].

#### **Parameters**

а

- half. Is only being read.

b

- half. Is only being read.

#### Returns

half

► The

result of multiplying a and b, with respect to saturation.

## Description

Performs half multiplication of inputs a and b, in round-to-nearest mode, and clamps the result to range [0.0, 1.0]. NaN results are flushed to +0.0.

## 

Negates input half number and returns the result.

### Description

Negates input half number and returns the result.

\_\_device\_\_ \_half \_\_hsub (const \_\_half a, const \_\_half b)

Performs half subtraction in round-to-nearest-even mode.

## Description

Subtracts half input b from input a in round-to-nearest mode.

\_\_device\_\_ \_half \_\_hsub\_sat (const \_\_half a, const \_\_half b)

Performs half subtraction in round-to-nearest-even mode, with saturation to [0.0, 1.0].

#### **Parameters**

a

- half. Is only being read.

b

- half. Is only being read.

#### Returns

half

The

result of subtraction of b from a, with respect to saturation.

## Description

Subtracts half input b from input a in round-to-nearest mode, and clamps the result to range [0.0, 1.0]. NaN results are flushed to +0.0.

## 1.1.2. Half2 Arithmetic Functions

Half Precision Intrinsics

To use these functions, include the header file cuda fp16.h in your program.



Performs half2 vector division in round-to-nearest-even mode.

## Description

Divides half2 input vector a by input vector b in round-to-nearest mode.

device half2 habs2 (const half2 a)

Calculates the absolute value of both halves of the input half2 number and returns the result.

#### **Parameters**

а

- half2. Is only being read.

#### Returns

half2

Returns

a with the absolute value of both halves.

## Description

Calculates the absolute value of both halves of the input half2 number and returns the result.

\_\_device\_\_ \_half2 \_\_hadd2 (const \_\_half2 a, const \_\_half2 b)

Performs half2 vector addition in round-to-nearest-even mode.

## Description

Performs half2 vector add of inputs a and b, in round-to-nearest mode.

\_\_device\_\_ \_half2 \_\_hadd2\_sat (const \_\_half2 a, const \_\_half2 b)

Performs half2 vector addition in round-to-nearest-even mode, with saturation to [0.0, 1.0].

#### **Parameters**

a

- half2. Is only being read.

#### b

- half2. Is only being read.

#### Returns

half2

The

sum of a and b, with respect to saturation.

## Description

Performs half2 vector add of inputs a and b, in round-to-nearest mode, and clamps the results to range [0.0, 1.0]. NaN results are flushed to +0.0.

```
__device__ _half2 __hcmadd (const __half2 a, const __half2 b, const __half2 c)
```

Performs fast complex multiply-accumulate.

#### **Parameters**

а

- half2. Is only being read.

b

- half2. Is only being read.

C

- half2. Is only being read.

#### Returns

half2

► The

result of complex multiply-accumulate operation on complex numbers a, b, and c

## Description

Interprets vector half2 input pairs a, b, and c as complex numbers in half precision and performs complex multiply-accumulate operation: a\*b+c

# \_\_device\_\_ \_half2 \_hfma2 (const \_half2 a, const \_half2 b, const half2 c)

Performs half2 vector fused multiply-add in round-to-nearest-even mode.

## Description

Performs half2 vector multiply on inputs a and b, then performs a half2 vector add of the result with c, rounding the result once in round-to-nearest-even mode.

Performs half2 vector fused multiply-add in round-to-nearest-even mode with relusaturation.

#### **Parameters**

а

- half2. Is only being read.

b

- half2. Is only being read.

C

- half2. Is only being read.

#### Returns

half2

The

result of elementwise fused multiply-add operation on vectors a, b, and c with relu saturation.

## Description

Performs half2 vector multiply on inputs a and b, then performs a half2 vector add of the result with c, rounding the result once in round-to-nearest-even mode. Then negative result is clamped to 0. NaN result is converted to canonical NaN.

# \_\_device\_\_ \_\_half2 \_\_hfma2\_sat (const \_\_half2 a, const half2 b, const half2 c)

Performs half2 vector fused multiply-add in round-to-nearest-even mode, with saturation to [0.0, 1.0].

#### **Parameters**

- а
- half2. Is only being read.
- b
- half2. Is only being read.
- C
- half2. Is only being read.

#### Returns

half2

The

result of elementwise fused multiply-add operation on vectors a, b, and c, with respect to saturation.

## Description

Performs half2 vector multiply on inputs a and b, then performs a half2 vector add of the result with c, rounding the result once in round-to-nearest-even mode, and clamps the results to range [0.0, 1.0]. NaN results are flushed to +0.0.

\_\_device\_\_ \_half2 \_\_hmul2 (const \_\_half2 a, const \_\_half2 b)

Performs half2 vector multiplication in round-to-nearest-even mode.

## Description

Performs half2 vector multiplication of inputs a and b, in round-to-nearest-even mode.

# \_\_device\_\_ \_half2 \_\_hmul2\_sat (const \_\_half2 a, const \_\_half2 b)

Performs half2 vector multiplication in round-to-nearest-even mode, with saturation to [0.0, 1.0].

#### **Parameters**

а

- half2. Is only being read.

b

- half2. Is only being read.

#### Returns

half2

The

result of elementwise multiplication of vectors a and b, with respect to saturation.

## Description

Performs half2 vector multiplication of inputs a and b, in round-to-nearest-even mode, and clamps the results to range [0.0, 1.0]. NaN results are flushed to +0.0.

# \_\_device\_\_ \_half2 \_hneg2 (const \_half2 a)

Negates both halves of the input half2 number and returns the result.

# Description

Negates both halves of the input half2 number a and returns the result.

# \_\_device\_\_ \_half2 \_\_hsub2 (const \_\_half2 a, const \_\_half2 b)

Performs half2 vector subtraction in round-to-nearest-even mode.

# Description

Subtracts half2 input vector b from input vector a in round-to-nearest-even mode.

# \_\_device\_\_ \_half2 \_\_hsub2\_sat (const \_\_half2 a, const half2 b)

Performs half2 vector subtraction in round-to-nearest-even mode, with saturation to [0.0, 1.0].

#### **Parameters**

а

- half2. Is only being read.

b

- half2. Is only being read.

#### Returns

half2

► The

subtraction of vector b from a, with respect to saturation.

## Description

Subtracts half2 input vector b from input vector a in round-to-nearest-even mode, and clamps the results to range [0.0, 1.0]. NaN results are flushed to +0.0.

# 1.1.3. Half Comparison Functions

Half Precision Intrinsics

To use these functions, include the header file cuda\_fp16.h in your program.

\_\_device\_\_ bool \_\_heq (const \_\_half a, const \_\_half b)

Performs half if-equal comparison.

#### **Parameters**

a

- half. Is only being read.

b

- half. Is only being read.

#### Returns

bool

▶ The

boolean result of if-equal comparison of a and b.

Performs half if-equal comparison of inputs a and b. NaN inputs generate false results.

\_\_device\_\_ bool \_\_hequ (const \_\_half a, const \_\_half b)

Performs half unordered if-equal comparison.

#### **Parameters**

- а
- half. Is only being read.
- b
- half. Is only being read.

#### Returns

bool

▶ The

boolean result of unordered if-equal comparison of a and b.

## Description

Performs half if-equal comparison of inputs a and b. NaN inputs generate true results.

\_\_device\_\_ bool \_\_hge (const \_\_half a, const \_\_half b)

Performs half greater-equal comparison.

#### **Parameters**

- а
- half. Is only being read.
- h
- half. Is only being read.

#### Returns

bool

► The

boolean result of greater-equal comparison of a and b.

# Description

Performs half greater-equal comparison of inputs a and b. NaN inputs generate false results.

# \_\_device\_\_ bool \_\_hgeu (const \_\_half a, const \_\_half b)

Performs half unordered greater-equal comparison.

### **Parameters**

а

- half. Is only being read.

b

- half. Is only being read.

#### Returns

bool

▶ The

boolean result of unordered greater-equal comparison of a and b.

## Description

Performs half greater-equal comparison of inputs a and b. NaN inputs generate true results.

# \_\_device\_\_ bool \_\_hgt (const \_\_half a, const \_\_half b)

Performs half greater-than comparison.

#### **Parameters**

а

- half. Is only being read.

b

- half. Is only being read.

#### Returns

bool

▶ The

boolean result of greater-than comparison of a and b.

## Description

Performs half greater-than comparison of inputs a and b. NaN inputs generate false results.

# \_\_device\_\_ bool \_\_hgtu (const \_\_half a, const \_\_half b)

Performs half unordered greater-than comparison.

#### **Parameters**

а

- half. Is only being read.

b

- half. Is only being read.

#### Returns

bool

▶ The

boolean result of unordered greater-than comparison of a and b.

## Description

Performs half greater-than comparison of inputs a and b. NaN inputs generate true results.

# \_\_device\_\_ int \_\_hisinf (const \_\_half a)

Checks if the input half number is infinite.

#### **Parameters**

а

- half. Is only being read.

#### Returns

int

\_ 1

iff a is equal to negative infinity,

• '

iff a is equal to positive infinity,

► ∩

otherwise.

# Description

Checks if the input half number a is infinite.

# device bool hisnan (const half a)

Determine whether half argument is a NaN.

### **Parameters**

a

- half. Is only being read.

#### Returns

bool

true

iff argument is NaN.

## Description

Determine whether half value a is a NaN.

\_\_device\_\_ bool \_\_hle (const \_\_half a, const \_\_half b)

Performs half less-equal comparison.

#### **Parameters**

а

- half. Is only being read.

h

- half. Is only being read.

#### Returns

bool

The

boolean result of less-equal comparison of a and b.

# Description

Performs half less-equal comparison of inputs a and b. NaN inputs generate false results.

\_\_device\_\_ bool \_\_hleu (const \_\_half a, const \_\_half b)

Performs half unordered less-equal comparison.

#### **Parameters**

a

- half. Is only being read.

b

- half. Is only being read.

#### Returns

bool

▶ The

boolean result of unordered less-equal comparison of a and b.

## Description

Performs half less-equal comparison of inputs a and b. NaN inputs generate true results.

\_\_device\_\_ bool \_\_hlt (const \_\_half a, const \_\_half b)

Performs half less-than comparison.

#### **Parameters**

а

- half. Is only being read.

b

- half. Is only being read.

#### Returns

bool

▶ The

boolean result of less-than comparison of a and b.

# Description

Performs half less-than comparison of inputs a and b. NaN inputs generate false results.

\_\_device\_\_ bool \_\_hltu (const \_\_half a, const \_\_half b)

Performs half unordered less-than comparison.

#### **Parameters**

а

- half. Is only being read.

b

- half. Is only being read.

#### Returns

bool

The

boolean result of unordered less-than comparison of a and b.

## Description

Performs half less-than comparison of inputs a and b. NaN inputs generate true results.

\_\_device\_\_ \_half \_\_hmax (const \_\_half a, const \_\_half b)
Calculates half maximum of two input values.

#### Description

Calculates half max(a, b) defined as (a > b)? a : b.

- ▶ If either of inputs is NaN, the other input is returned.
- If both inputs are NaNs, then canonical NaN is returned.
- ▶ If values of both inputs are 0.0, then +0.0 > -0.0

\_\_device\_\_ \_half \_\_hmax\_nan (const \_\_half a, const \_\_half b)

Calculates half maximum of two input values, NaNs pass through.

# Description

Calculates half max(a, b) defined as (a > b)? a:b.

- If either of inputs is NaN, then canonical NaN is returned.
- ▶ If values of both inputs are 0.0, then +0.0 > -0.0

\_\_device\_\_ \_half \_\_hmin (const \_\_half a, const \_\_half b)

Calculates half minimum of two input values.

# Description

Calculates half min(a, b) defined as (a < b)? a : b.

- If either of inputs is NaN, the other input is returned.
- If both inputs are NaNs, then canonical NaN is returned.
- ▶ If values of both inputs are 0.0, then +0.0 > -0.0

\_\_device\_\_ \_half \_\_hmin\_nan (const \_\_half a, const \_\_half b)

Calculates half minimum of two input values, NaNs pass through.

## Description

Calculates half min(a, b) defined as (a < b)? a : b.

- If either of inputs is NaN, then canonical NaN is returned.
- ► If values of both inputs are 0.0, then +0.0 > -0.0

\_\_device\_\_ bool \_\_hne (const \_\_half a, const \_\_half b)

Performs half not-equal comparison.

#### **Parameters**

а

- half. Is only being read.

b

- half. Is only being read.

#### Returns

bool

► The

boolean result of not-equal comparison of a and b.

# Description

Performs half not-equal comparison of inputs a and b. NaN inputs generate false results.

\_\_device\_\_ bool \_\_hneu (const \_\_half a, const \_\_half b)

Performs half unordered not-equal comparison.

#### **Parameters**

а

- half. Is only being read.

b

- half. Is only being read.

#### Returns

bool

▶ The

boolean result of unordered not-equal comparison of a and b.

# Description

Performs half not-equal comparison of inputs a and b. NaN inputs generate true results.

# 1.1.4. Half2 Comparison Functions

Half Precision Intrinsics

To use these functions, include the header file cuda fp16.h in your program.

# \_\_device\_\_ bool \_\_hbeq2 (const \_\_half2 a, const \_\_half2 b)

Performs half2 vector if-equal comparison and returns boolean true iff both half results are true, boolean false otherwise.

#### **Parameters**

а

- half2. Is only being read.

b

- half2. Is only being read.

#### Returns

bool

true

if both half results of if-equal comparison of vectors a and b are true;

false

otherwise.

# Description

Performs half2 vector if-equal comparison of inputs a and b. The bool result is set to true only if both half if-equal comparisons evaluate to true, or false otherwise. NaN inputs generate false results.

# \_\_device\_\_ bool \_\_hbequ2 (const \_\_half2 a, const \_\_half2 b)

Performs half2 vector unordered if-equal comparison and returns boolean true iff both half results are true, boolean false otherwise.

#### **Parameters**

а

- half2. Is only being read.

b

- half2. Is only being read.

#### Returns

bool

true

if both half results of unordered if-equal comparison of vectors a and b are true;

false

otherwise.

## Description

Performs half2 vector if-equal comparison of inputs a and b. The bool result is set to true only if both half if-equal comparisons evaluate to true, or false otherwise. NaN inputs generate true results.

# \_\_device\_\_ bool \_\_hbge2 (const \_\_half2 a, const \_\_half2 b)

Performs half2 vector greater-equal comparison and returns boolean true iff both half results are true, boolean false otherwise.

#### **Parameters**

а

- half2. Is only being read.

b

- half2. Is only being read.

#### Returns

bool

▶ true

if both half results of greater-equal comparison of vectors a and b are true;

false

otherwise.

Performs half2 vector greater-equal comparison of inputs a and b. The bool result is set to true only if both half greater-equal comparisons evaluate to true, or false otherwise. NaN inputs generate false results.

# \_\_device\_\_ bool \_\_hbgeu2 (const \_\_half2 a, const \_\_half2 b)

Performs half2 vector unordered greater-equal comparison and returns boolean true iff both half results are true, boolean false otherwise.

#### **Parameters**

а

- half2. Is only being read.

b

- half2. Is only being read.

#### Returns

bool

▶ true

if both half results of unordered greater-equal comparison of vectors a and b are true;

false

otherwise.

# Description

Performs half2 vector greater-equal comparison of inputs a and b. The bool result is set to true only if both half greater-equal comparisons evaluate to true, or false otherwise. NaN inputs generate true results.

# \_device\_\_ bool \_\_hbgt2 (const \_\_half2 a, const \_\_half2 b)

Performs half2 vector greater-than comparison and returns boolean true iff both half results are true, boolean false otherwise.

#### **Parameters**

а

- half2. Is only being read.

b

- half2. Is only being read.

#### Returns

bool

true

if both half results of greater-than comparison of vectors a and b are true;

false

otherwise.

## Description

Performs half2 vector greater-than comparison of inputs a and b. The bool result is set to true only if both half greater-than comparisons evaluate to true, or false otherwise. NaN inputs generate false results.

# \_\_device\_\_ bool \_\_hbgtu2 (const \_\_half2 a, const \_\_half2 b)

Performs half2 vector unordered greater-than comparison and returns boolean true iff both half results are true, boolean false otherwise.

#### **Parameters**

а

- half2. Is only being read.

b

- half2. Is only being read.

#### Returns

bool

true

if both half results of unordered greater-than comparison of vectors a and b are true;

false

otherwise.

# Description

Performs half2 vector greater-than comparison of inputs a and b. The bool result is set to true only if both half greater-than comparisons evaluate to true, or false otherwise. NaN inputs generate true results.

# device bool hble2 (const half2 a, const half2 b)

Performs half2 vector less-equal comparison and returns boolean true iff both half results are true, boolean false otherwise.

#### **Parameters**

а

- half2. Is only being read.

b

- half2. Is only being read.

#### Returns

bool

true

if both half results of less-equal comparison of vectors a and b are true;

false

otherwise.

## Description

Performs half2 vector less-equal comparison of inputs a and b. The bool result is set to true only if both half less-equal comparisons evaluate to true, or false otherwise. NaN inputs generate false results.

# \_\_device\_\_ bool \_\_hbleu2 (const \_\_half2 a, const \_\_half2 b)

Performs half2 vector unordered less-equal comparison and returns boolean true iff both half results are true, boolean false otherwise.

#### **Parameters**

а

- half2. Is only being read.

b

- half2. Is only being read.

#### Returns

bool

true

if both half results of unordered less-equal comparison of vectors a and b are true;

false

otherwise.

Performs half2 vector less-equal comparison of inputs a and b. The bool result is set to true only if both half less-equal comparisons evaluate to true, or false otherwise. NaN inputs generate true results.

# \_\_device\_\_ bool \_\_hblt2 (const \_\_half2 a, const \_\_half2 b)

Performs half2 vector less-than comparison and returns boolean true iff both half results are true, boolean false otherwise.

#### **Parameters**

а

- half2. Is only being read.

b

- half2. Is only being read.

#### Returns

bool

▶ true

if both half results of less-than comparison of vectors a and b are true;

false

otherwise.

# Description

Performs half2 vector less-than comparison of inputs a and b. The bool result is set to true only if both half less-than comparisons evaluate to true, or false otherwise. NaN inputs generate false results.

# \_\_device\_\_ bool \_\_hbltu2 (const \_\_half2 a, const \_\_half2 b)

Performs half2 vector unordered less-than comparison and returns boolean true iff both half results are true, boolean false otherwise.

#### **Parameters**

а

- half2. Is only being read.

b

- half2. Is only being read.

#### Returns

bool

true

if both half results of unordered less-than comparison of vectors a and b are true;

false

otherwise.

## Description

Performs half2 vector less-than comparison of inputs a and b. The bool result is set to true only if both half less-than comparisons evaluate to true, or false otherwise. NaN inputs generate true results.

# device bool hbne2 (const half2 a, const half2 b)

Performs half2 vector not-equal comparison and returns boolean true iff both half results are true, boolean false otherwise.

#### **Parameters**

а

- half2. Is only being read.

b

- half2. Is only being read.

#### Returns

bool

true

if both half results of not-equal comparison of vectors a and b are true,

false

otherwise.

# Description

Performs half2 vector not-equal comparison of inputs a and b. The bool result is set to true only if both half not-equal comparisons evaluate to true, or false otherwise. NaN inputs generate false results.

# \_\_device\_\_ bool \_\_hbneu2 (const \_\_half2 a, const \_\_half2 b)

Performs half2 vector unordered not-equal comparison and returns boolean true iff both half results are true, boolean false otherwise.

#### **Parameters**

а

- half2. Is only being read.

b

- half2. Is only being read.

#### Returns

bool

true

if both half results of unordered not-equal comparison of vectors a and b are true;

false

otherwise.

## Description

Performs half2 vector not-equal comparison of inputs a and b. The bool result is set to true only if both half not-equal comparisons evaluate to true, or false otherwise. NaN inputs generate true results.

\_\_device\_\_ \_half2 \_\_heq2 (const \_\_half2 a, const \_\_half2 b)

Performs half2 vector if-equal comparison.

#### **Parameters**

а

- half2. Is only being read.

b

- half2. Is only being read.

#### Returns

half2

► The

vector result of if-equal comparison of vectors a and b.

Performs half2 vector if-equal comparison of inputs a and b. The corresponding half results are set to 1.0 for true, or 0.0 for false. NaN inputs generate false results.

# \_\_device\_\_ \_half2 \_\_hequ2 (const \_\_half2 a, const \_\_half2 b)

Performs half2 vector unordered if-equal comparison.

#### **Parameters**

a

- half2. Is only being read.

b

- half2. Is only being read.

#### Returns

half2

The

vector result of unordered if-equal comparison of vectors a and b.

# Description

Performs half2 vector if-equal comparison of inputs a and b. The corresponding half results are set to 1.0 for true, or 0.0 for false. NaN inputs generate true results.

\_\_device\_\_ \_half2 \_\_hge2 (const \_\_half2 a, const \_\_half2 b)

Performs half2 vector greater-equal comparison.

#### **Parameters**

а

- half2. Is only being read.

b

- half2. Is only being read.

#### Returns

half2

The

vector result of greater-equal comparison of vectors a and b.

Performs half2 vector greater-equal comparison of inputs a and b. The corresponding half results are set to 1.0 for true, or 0.0 for false. NaN inputs generate false results.

# \_\_device\_\_ \_half2 \_\_hgeu2 (const \_\_half2 a, const \_\_half2 b)

Performs half2 vector unordered greater-equal comparison.

#### **Parameters**

a

- half2. Is only being read.

b

- half2. Is only being read.

#### Returns

half2

▶ The

half2 vector result of unordered greater-equal comparison of vectors a and b.

# Description

Performs half2 vector greater-equal comparison of inputs a and b. The corresponding half results are set to 1.0 for true, or 0.0 for false. NaN inputs generate true results.

\_\_device\_\_ \_half2 \_hgt2 (const \_half2 a, const \_half2 b)

Performs half2 vector greater-than comparison.

#### **Parameters**

а

- half2. Is only being read.

b

- half2. Is only being read.

#### Returns

half2

The

vector result of greater-than comparison of vectors a and b.

Performs half2 vector greater-than comparison of inputs a and b. The corresponding half results are set to 1.0 for true, or 0.0 for false. NaN inputs generate false results.

# \_\_device\_\_ \_half2 \_\_hgtu2 (const \_\_half2 a, const \_\_half2 b)

Performs half2 vector unordered greater-than comparison.

#### **Parameters**

a

- half2. Is only being read.

b

- half2. Is only being read.

#### Returns

half2

▶ The

half2 vector result of unordered greater-than comparison of vectors a and b.

# Description

Performs half2 vector greater-than comparison of inputs a and b. The corresponding half results are set to 1.0 for true, or 0.0 for false. NaN inputs generate true results.

# \_\_device\_\_ half2 \_\_hisnan2 (const \_\_half2 a)

Determine whether half2 argument is a NaN.

#### **Parameters**

а

- half2. Is only being read.

#### Returns

half2

The

half2 with the corresponding half results set to 1.0 for NaN, 0.0 otherwise.

## Description

Determine whether each half of input half2 number a is a NaN.

\_\_device\_\_ \_half2 \_\_hle2 (const \_\_half2 a, const \_\_half2 b)

Performs half2 vector less-equal comparison.

### **Parameters**

а

- half2. Is only being read.

b

- half2. Is only being read.

#### Returns

half2

▶ The

half2 result of less-equal comparison of vectors a and b.

## Description

Performs half2 vector less-equal comparison of inputs a and b. The corresponding half results are set to 1.0 for true, or 0.0 for false. NaN inputs generate false results.

\_\_device\_\_ \_half2 \_\_hleu2 (const \_\_half2 a, const \_\_half2 b)

Performs half2 vector unordered less-equal comparison.

#### **Parameters**

a

- half2. Is only being read.

b

- half2. Is only being read.

#### Returns

half2

The

vector result of unordered less-equal comparison of vectors a and b.

# Description

Performs half2 vector less-equal comparison of inputs a and b. The corresponding half results are set to 1.0 for true, or 0.0 for false. NaN inputs generate true results.

\_\_device\_\_ \_half2 \_\_hlt2 (const \_\_half2 a, const \_\_half2 b)

Performs half2 vector less-than comparison.

#### **Parameters**

а

- half2. Is only being read.

b

- half2. Is only being read.

#### Returns

half2

► The

half2 vector result of less-than comparison of vectors a and b.

## Description

Performs half2 vector less-than comparison of inputs a and b. The corresponding half results are set to 1.0 for true, or 0.0 for false. NaN inputs generate false results.

\_\_device\_\_ \_half2 \_\_hltu2 (const \_\_half2 a, const \_\_half2 b)

Performs half2 vector unordered less-than comparison.

#### **Parameters**

a

- half2. Is only being read.

b

- half2. Is only being read.

#### Returns

half2

The

vector result of unordered less-than comparison of vectors a and b.

# Description

Performs half2 vector less-than comparison of inputs a and b. The corresponding half results are set to 1.0 for true, or 0.0 for false. NaN inputs generate true results.

\_\_device\_\_ \_half2 \_\_hmax2 (const \_\_half2 a, const \_\_half2 b)

Calculates half2 vector maximum of two inputs.

## Description

Calculates half2 vector max(a, b). Elementwise half operation is defined as (a > b)? a : b.

- If either of inputs is NaN, the other input is returned.
- If both inputs are NaNs, then canonical NaN is returned.
- ▶ If values of both inputs are 0.0, then +0.0 > -0.0

Calculates half2 vector maximum of two inputs, NaNs pass through.

## Description

Calculates half2 vector max(a, b). Elementwise half operation is defined as (a > b)? a : b.

- If either of inputs is NaN, then canonical NaN is returned.
- ▶ If values of both inputs are 0.0, then +0.0 > -0.0

Calculates half2 vector minimum of two inputs.

## Description

Calculates half2 vector min(a, b). Elementwise half operation is defined as (a < b)? a : b.

- If either of inputs is NaN, the other input is returned.
- If both inputs are NaNs, then canonical NaN is returned.
- ▶ If values of both inputs are 0.0, then +0.0 > -0.0

Calculates half2 vector minimum of two inputs, NaNs pass through.

# Description

Calculates half2 vector min(a, b). Elementwise half operation is defined as (a < b)? a : b.

If either of inputs is NaN, then canonical NaN is returned.

▶ If values of both inputs are 0.0, then +0.0 > -0.0

\_\_device\_\_ \_half2 \_\_hne2 (const \_\_half2 a, const \_\_half2 b)

Performs half2 vector not-equal comparison.

#### **Parameters**

а

- half2. Is only being read.

b

- half2. Is only being read.

#### Returns

half2

The

vector result of not-equal comparison of vectors a and b.

## Description

Performs half2 vector not-equal comparison of inputs a and b. The corresponding half results are set to 1.0 for true, or 0.0 for false. NaN inputs generate false results.

\_\_device\_\_ \_half2 \_\_hneu2 (const \_\_half2 a, const \_\_half2 b)

Performs half2 vector unordered not-equal comparison.

#### **Parameters**

а

- half2. Is only being read.

b

- half2. Is only being read.

#### Returns

half2

▶ The

vector result of unordered not-equal comparison of vectors a and b.

## Description

Performs half2 vector not-equal comparison of inputs a and b. The corresponding half results are set to 1.0 for true, or 0.0 for false. NaN inputs generate true results.

# 1.1.5. Half Precision Conversion and Data Movement

Half Precision Intrinsics

To use these functions, include the header file cuda fp16.h in your program.

\_\_host\_\_\_device\_\_half \_\_double2half (const double a)

Converts double number to half precision in round-to-nearest-even mode and returns half with converted value.

#### **Parameters**

а

- double. Is only being read.

#### Returns

half

**>** 2

converted to half.

## Description

Converts double number a to half precision in round-to-nearest-even mode.

\_\_host\_\_\_\_device\_\_ \_half2 \_\_float22half2\_rn (const float2 a)

Converts both components of float2 number to half precision in round-to-nearest-even mode and returns half2 with converted values.

#### **Parameters**

а

- float2. Is only being read.

#### Returns

half2

▶ The

half2 which has corresponding halves equal to the converted float2 components.

Converts both components of float2 to half precision in round-to-nearest mode and combines the results into one half2 number. Low 16 bits of the return value correspond to a.x and high 16 bits of the return value correspond to a.y.

# \_\_host\_\_\_device\_\_ \_half \_\_float2half (const float a)

Converts float number to half precision in round-to-nearest-even mode and returns half with converted value.

#### **Parameters**

a

- float. Is only being read.

#### Returns

half

a

converted to half.

## Description

Converts float number a to half precision in round-to-nearest-even mode.

# \_\_host\_\_\_\_device\_\_ \_half2 \_\_float2half2\_rn (const float a)

Converts input to half precision in round-to-nearest-even mode and populates both halves of half2 with converted value.

### **Parameters**

a

- float. Is only being read.

#### Returns

half2

► The

half2 value with both halves equal to the converted half precision number.

# Description

Converts input a to half precision in round-to-nearest-even mode and populates both halves of half2 with converted value.

# \_\_host\_\_\_device\_\_ \_half \_\_float2half\_rd (const float a)

Converts float number to half precision in round-down mode and returns half with converted value.

#### **Parameters**

a

- float. Is only being read.

#### Returns

half

▶ a

converted to half.

## Description

Converts float number a to half precision in round-down mode.

# \_\_host\_\_\_device\_\_ \_half \_\_float2half\_rn (const float a)

Converts float number to half precision in round-to-nearest-even mode and returns half with converted value.

#### **Parameters**

a

- float. Is only being read.

#### Returns

half

a

converted to half.

# Description

Converts float number a to half precision in round-to-nearest-even mode.

# \_\_host\_\_\_device\_\_ \_half \_\_float2half\_ru (const float a)

Converts float number to half precision in round-up mode and returns half with converted value.

#### **Parameters**

а

- float. Is only being read.

#### Returns

half

▶ a

converted to half.

## Description

Converts float number a to half precision in round-up mode.

# \_\_host\_\_\_\_device\_\_ \_half \_\_float2half\_rz (const float a)

Converts float number to half precision in round-towards-zero mode and returns half with converted value.

#### **Parameters**

a

- float. Is only being read.

#### Returns

half

a

converted to half.

# Description

Converts float number a to half precision in round-towards-zero mode.

# \_\_host\_\_\_\_device\_\_ \_half2 \_\_floats2half2\_rn (const float a, const float b)

Converts both input floats to half precision in round-to-nearest-even mode and returns half2 with converted values.

#### **Parameters**

а

- float. Is only being read.

b

- float. Is only being read.

#### Returns

half2

► The

half2 value with corresponding halves equal to the converted input floats.

## Description

Converts both input floats to half precision in round-to-nearest-even mode and combines the results into one half2 number. Low 16 bits of the return value correspond to the input a, high 16 bits correspond to the input b.

\_\_host\_\_\_device\_\_float2 \_\_half22float2 (const \_\_half2 a)

Converts both halves of half2 to float2 and returns the result.

#### **Parameters**

a

- half2. Is only being read.

#### Returns

float2

▶ a

converted to float2.

# Description

Converts both halves of half2 input a to float2 and returns the result.

# \_\_host\_\_\_\_device\_\_ float \_\_half2float (const \_\_half a)

Converts half number to float.

### **Parameters**

a

- float. Is only being read.

#### Returns

float

a

converted to float.

## Description

Converts half number a to float.

# \_\_device\_\_ \_half2 \_\_half2half2 (const \_\_half a)

Returns half2 with both halves equal to the input value.

#### **Parameters**

а

- half. Is only being read.

#### Returns

half2

► The

vector which has both its halves equal to the input a.

# Description

Returns half2 number with both halves equal to the input a half number.

# \_\_device\_\_ int \_\_\_half2int\_rd (const \_\_\_half h)

Convert a half to a signed integer in round-down mode.

#### **Parameters**

h

- half. Is only being read.

#### Returns

int

h converted to a signed integer.

## Description

Convert the half-precision floating-point value h to a signed integer in round-down mode.

# \_\_device\_\_ int \_\_half2int\_rn (const \_\_half h)

Convert a half to a signed integer in round-to-nearest-even mode.

#### **Parameters**

h

- half. Is only being read.

#### Returns

int

► h converted to a signed integer.

# Description

Convert the half-precision floating-point value  ${\tt h}$  to a signed integer in round-to-nearest-even mode.

# \_\_device\_\_ int \_\_half2int\_ru (const \_\_half h)

Convert a half to a signed integer in round-up mode.

## **Parameters**

h

- half. Is only being read.

#### Returns

int

▶ h

converted to a signed integer.

Convert the half-precision floating-point value h to a signed integer in round-up mode.

# \_\_host\_\_\_device\_\_ int \_\_half2int\_rz (const \_\_half h)

Convert a half to a signed integer in round-towards-zero mode.

## **Parameters**

h

- half. Is only being read.

#### Returns

int

▶ h

converted to a signed integer.

## Description

Convert the half-precision floating-point value  ${\tt h}$  to a signed integer in round-towards-zero mode.

# \_\_device\_\_ long long int \_\_half2ll\_rd (const \_\_half h)

Convert a half to a signed 64-bit integer in round-down mode.

#### **Parameters**

h

- half. Is only being read.

#### Returns

long long int

▶ h

converted to a signed 64-bit integer.

## Description

Convert the half-precision floating-point value h to a signed 64-bit integer in round-down mode.

# \_\_device\_\_ long long int \_\_half2ll\_rn (const \_\_half h)

Convert a half to a signed 64-bit integer in round-to-nearest-even mode.

#### **Parameters**

h

- half. Is only being read.

#### Returns

long long int

▶ h

converted to a signed 64-bit integer.

# Description

Convert the half-precision floating-point value h to a signed 64-bit integer in round-to-nearest-even mode.

# \_\_device\_\_ long long int \_\_half2ll\_ru (const \_\_half h)

Convert a half to a signed 64-bit integer in round-up mode.

#### **Parameters**

h

- half. Is only being read.

#### Returns

long long int

► h

converted to a signed 64-bit integer.

# Description

Convert the half-precision floating-point value h to a signed 64-bit integer in round-up mode.

# \_\_host\_\_\_\_device\_\_ long long int \_\_half2ll\_rz (const \_\_half h)

Convert a half to a signed 64-bit integer in round-towards-zero mode.

#### **Parameters**

h

- half. Is only being read.

#### Returns

long long int

h

converted to a signed 64-bit integer.

## Description

Convert the half-precision floating-point value h to a signed 64-bit integer in round-towards-zero mode.

# \_\_device\_\_ short int \_\_half2short\_rd (const \_\_half h)

Convert a half to a signed short integer in round-down mode.

#### **Parameters**

h

- half. Is only being read.

#### Returns

short int

▶ h

converted to a signed short integer.

# Description

Convert the half-precision floating-point value  ${\tt h}$  to a signed short integer in round-down mode.

# \_\_device\_\_ short int \_\_half2short\_rn (const \_\_half h)

Convert a half to a signed short integer in round-to-nearest-even mode.

### **Parameters**

h

- half. Is only being read.

#### Returns

short int

► h

converted to a signed short integer.

# Description

Convert the half-precision floating-point value h to a signed short integer in round-to-nearest-even mode.

# \_\_device\_\_ short int \_\_half2short\_ru (const \_\_half h)

Convert a half to a signed short integer in round-up mode.

#### **Parameters**

h

- half. Is only being read.

#### Returns

short int

► h

converted to a signed short integer.

# Description

Convert the half-precision floating-point value h to a signed short integer in round-up mode.

# \_\_host\_\_\_\_device\_\_ short int \_\_half2short\_rz (const \_\_half h)

Convert a half to a signed short integer in round-towards-zero mode.

#### **Parameters**

h

- half. Is only being read.

#### Returns

short int

▶ h

converted to a signed short integer.

## Description

Convert the half-precision floating-point value h to a signed short integer in round-towards-zero mode.

# \_\_device\_\_ unsigned int \_\_half2uint\_rd (const \_\_half h)

Convert a half to an unsigned integer in round-down mode.

#### **Parameters**

h

- half. Is only being read.

#### Returns

unsigned int

▶ h

converted to an unsigned integer.

# Description

Convert the half-precision floating-point value h to an unsigned integer in round-down mode.

# \_\_device\_\_ unsigned int \_\_half2uint\_rn (const \_\_half h)

Convert a half to an unsigned integer in round-to-nearest-even mode.

# **Parameters**

h

- half. Is only being read.

# Returns

unsigned int

▶ h

converted to an unsigned integer.

# Description

Convert the half-precision floating-point value h to an unsigned integer in round-to-nearesteven mode.

# \_\_device\_\_ unsigned int \_\_half2uint\_ru (const \_\_half h)

Convert a half to an unsigned integer in round-up mode.

# **Parameters**

h

- half. Is only being read.

# Returns

unsigned int

► h

converted to an unsigned integer.

# Description

Convert the half-precision floating-point value h to an unsigned integer in round-up mode.

# \_\_host\_\_\_\_device\_\_ unsigned int \_\_half2uint\_rz (const \_\_half h)

Convert a half to an unsigned integer in round-towards-zero mode.

# **Parameters**

h

- half. Is only being read.

# Returns

unsigned int

▶ h

converted to an unsigned integer.

# Description

Convert the half-precision floating-point value h to an unsigned integer in round-towards-zero mode.

# \_\_device\_\_ unsigned long long int \_\_half2ull\_rd (const \_\_half h)

Convert a half to an unsigned 64-bit integer in round-down mode.

# **Parameters**

h

- half. Is only being read.

# Returns

unsigned long long int

► h

converted to an unsigned 64-bit integer.

# Description

Convert the half-precision floating-point value h to an unsigned 64-bit integer in round-down mode.

# \_\_device\_\_ unsigned long long int \_\_half2ull\_rn (const half h)

Convert a half to an unsigned 64-bit integer in round-to-nearest-even mode.

# **Parameters**

h

- half. Is only being read.

# Returns

unsigned long long int

h

converted to an unsigned 64-bit integer.

# Description

Convert the half-precision floating-point value h to an unsigned 64-bit integer in round-to-nearest-even mode.

# \_\_device\_\_ unsigned long long int \_\_half2ull\_ru (const \_\_half h)

Convert a half to an unsigned 64-bit integer in round-up mode.

# **Parameters**

h

- half. Is only being read.

# Returns

unsigned long long int

► h

converted to an unsigned 64-bit integer.

# Description

Convert the half-precision floating-point value h to an unsigned 64-bit integer in round-up mode.

# \_\_host\_\_\_\_device\_\_ unsigned long long int \_\_half2ull\_rz (const half h)

Convert a half to an unsigned 64-bit integer in round-towards-zero mode.

# **Parameters**

h

- half. Is only being read.

# Returns

unsigned long long int

h converted to an unsigned 64-bit integer.

# Description

Convert the half-precision floating-point value h to an unsigned 64-bit integer in round-towards-zero mode.

# \_\_device\_\_ unsigned short int \_\_half2ushort\_rd (const \_\_half h)

Convert a half to an unsigned short integer in round-down mode.

# **Parameters**

h

- half. Is only being read.

# Returns

unsigned short int

h converted to an unsigned short integer.

# Description

Convert the half-precision floating-point value h to an unsigned short integer in round-down mode.

# \_\_device\_\_ unsigned short int \_\_half2ushort\_rn (const \_\_half h)

Convert a half to an unsigned short integer in round-to-nearest-even mode.

# **Parameters**

h

- half. Is only being read.

# Returns

unsigned short int

▶ h

converted to an unsigned short integer.

# Description

Convert the half-precision floating-point value h to an unsigned short integer in round-to-nearest-even mode.

# \_\_device\_\_ unsigned short int \_\_half2ushort\_ru (const \_\_half h)

Convert a half to an unsigned short integer in round-up mode.

# **Parameters**

h

- half. Is only being read.

# Returns

unsigned short int

► h

converted to an unsigned short integer.

# Description

Convert the half-precision floating-point value h to an unsigned short integer in round-up mode.

# \_\_host\_\_\_\_device\_\_ unsigned short int \_\_half2ushort\_rz (const \_\_half h)

Convert a half to an unsigned short integer in round-towards-zero mode.

# **Parameters**

h

- half. Is only being read.

# Returns

unsigned short int

h converted to an unsigned short integer.

# Description

Convert the half-precision floating-point value  ${\tt h}$  to an unsigned short integer in round-towards-zero mode.

# \_\_device\_\_ short int \_\_half\_as\_short (const \_\_half h)

Reinterprets bits in a half as a signed short integer.

# **Parameters**

h

- half. Is only being read.

# Returns

short int

► The reinterpreted value.

# Description

Reinterprets the bits in the half-precision floating-point number h as a signed short integer.

# \_\_device\_\_ unsigned short int \_\_half\_as\_ushort (const half h)

Reinterprets bits in a half as an unsigned short integer.

# **Parameters**

h

- half. Is only being read.

# Returns

unsigned short int

The reinterpreted value.

# Description

Reinterprets the bits in the half-precision floating-point h as an unsigned short number.

# \_\_device\_\_ \_half2 \_\_halves2half2 (const \_\_half a, const half b)

Combines two half numbers into one half2 number.

# **Parameters**

а

- half. Is only being read.

b

- half. Is only being read.

## Returns

half2

The

half2 with one half equal to a and the other to b.

# Description

Combines two input half number a and b into one half2 number. Input a is stored in low 16 bits of the return value, input b is stored in high 16 bits of the return value.

# \_\_host\_\_\_device\_\_ float \_\_high2float (const \_\_half2 a)

Converts high 16 bits of half2 to float and returns the result.

# **Parameters**

a

- half2. Is only being read.

# Returns

float

▶ The

high 16 bits of a converted to float.

# Description

Converts high 16 bits of half2 input a to 32-bit floating-point number and returns the result.

\_\_device\_\_ \_half \_\_high2half (const \_\_half2 a)

Returns high 16 bits of half2 input.

# **Parameters**

а

- half2. Is only being read.

# Returns

half

▶ The

high 16 bits of the input.

# Description

Returns high 16 bits of half2 input a.

\_\_device\_\_ \_half2 \_\_high2half2 (const \_\_half2 a)

Extracts high 16 bits from half2 input.

# **Parameters**

a

- half2. Is only being read.

#### Returns

half2

► The

half2 with both halves equal to the high 16 bits of the input.

# Description

Extracts high 16 bits from half2 input a and returns a new half2 number which has both halves equal to the extracted bits.

# \_\_device\_\_ \_half2 \_\_highs2half2 (const \_\_half2 a, const half2 b)

Extracts high 16 bits from each of the two half2 inputs and combines into one half2 number.

# **Parameters**

а

- half2. Is only being read.

b

- half2. Is only being read.

# Returns

half2

► The

high 16 bits of a and of b.

# Description

Extracts high 16 bits from each of the two half2 inputs and combines into one half2 number. High 16 bits from input a is stored in low 16 bits of the return value, high 16 bits from input b is stored in high 16 bits of the return value.

# \_\_device\_\_ \_half \_\_int2half\_rd (const int i)

Convert a signed integer to a half in round-down mode.

# **Parameters**

i

- int. Is only being read.

# Returns

half

▶ j

converted to half.

# Description

Convert the signed integer value  $\mathtt{i}$  to a half-precision floating-point value in round-down mode.

\_\_host\_\_\_device\_\_ \_half \_\_int2half\_rn (const int i)

Convert a signed integer to a half in round-to-nearest-even mode.

# **Parameters**

i

- int. Is only being read.

#### Returns

half

▶ i

converted to half.

# Description

Convert the signed integer value i to a half-precision floating-point value in round-to-nearest-even mode.

# \_\_device\_\_ \_half \_\_int2half\_ru (const int i)

Convert a signed integer to a half in round-up mode.

# **Parameters**

i

- int. Is only being read.

# Returns

half

i

converted to half.

# Description

Convert the signed integer value i to a half-precision floating-point value in round-up mode.

# \_\_device\_\_ \_\_half \_\_int2half\_rz (const int i)

Convert a signed integer to a half in round-towards-zero mode.

# **Parameters**

i

- int. Is only being read.

# Returns

half

▶ į

converted to half.

# Description

Convert the signed integer value i to a half-precision floating-point value in round-towards-zero mode.

# \_\_device\_\_ \_half \_\_ldca (const \_\_half \*ptr)

Generates a `ld.global.ca` load instruction.

# **Parameters**

ptr

- memory location

#### Returns

The value pointed by `ptr`

# \_\_device\_\_ \_half2 \_\_ldca (const \_\_half2 \*ptr)

Generates a `ld.global.ca` load instruction.

# **Parameters**

ptr

- memory location

# Returns

The value pointed by `ptr`

# \_\_device\_\_ \_half \_\_ldcg (const \_\_half \*ptr) Generates a `ld.global.cg` load instruction. **Parameters** ptr - memory location Returns The value pointed by `ptr` \_\_device\_\_ \_half2 \_\_ldcg (const \_\_half2 \*ptr) Generates a `ld.global.cg` load instruction. **Parameters** ptr - memory location Returns The value pointed by `ptr` \_\_device\_\_ \_half \_\_ldcs (const \_\_half \*ptr) Generates a `ld.global.cs` load instruction.

# **Parameters**

ptr

- memory location

# Returns

The value pointed by `ptr`

\_\_device\_\_ \_half2 \_\_ldcs (const \_\_half2 \*ptr)

Generates a `ld.global.cs` load instruction.

# **Parameters**

ptr

- memory location

# Returns

The value pointed by `ptr`

# \_\_device\_\_\_\_half \_\_ldcv (const \_\_half \*ptr) Generates a `ld.global.cv` load instruction. Parameters ptr - memory location Returns The value pointed by `ptr` \_\_device\_\_\_\_half2 \_\_ldcv (const \_\_half2 \*ptr) Generates a `ld.global.cv` load instruction. Parameters ptr - memory location Returns The value pointed by `ptr` \_\_device\_\_\_\_half \_\_ldg (const \_\_half \*ptr)

# Parameters

ptr

- memory location

# Returns

The value pointed by `ptr`

\_\_device\_\_ \_half2 \_\_ldg (const \_\_half2 \*ptr)

Generates a `ld.global.nc` load instruction.

Generates a `ld.global.nc` load instruction.

# **Parameters**

ptr

- memory location

# Returns

The value pointed by `ptr`

# Description

defined(\_\_CUDA\_ARCH\_\_) || (\_\_CUDA\_ARCH\_\_ >= 300)

\_\_device\_\_ \_half \_\_ldlu (const \_\_half \*ptr)

Generates a `ld.global.lu` load instruction.

# **Parameters**

# ptr

- memory location

# Returns

The value pointed by `ptr`

\_\_device\_\_ \_half2 \_\_ldlu (const \_\_half2 \*ptr)

Generates a `ld.global.lu` load instruction.

# **Parameters**

# ptr

- memory location

# Returns

The value pointed by `ptr`

\_\_device\_\_ \_half \_\_ll2half\_rd (const long long int i)

Convert a signed 64-bit integer to a half in round-down mode.

# **Parameters**

İ

- long long int. Is only being read.

# Returns

half

▶ į

converted to half.

# Description

Convert the signed 64-bit integer value  $\mathtt{i}$  to a half-precision floating-point value in round-down mode.

# \_\_host\_\_\_device\_ \_\_half \_\_ll2half\_rn (const long long int i)

Convert a signed 64-bit integer to a half in round-to-nearest-even mode.

# **Parameters**

i

- long long int. Is only being read.

# Returns

half

▶ i

converted to half.

# Description

Convert the signed 64-bit integer value i to a half-precision floating-point value in round-to-nearest-even mode.

# \_\_device\_\_ \_half \_\_ll2half\_ru (const long long int i)

Convert a signed 64-bit integer to a half in round-up mode.

# **Parameters**

i

- long long int. Is only being read.

# Returns

half

▶ i

converted to half.

# Description

Convert the signed 64-bit integer value i to a half-precision floating-point value in round-up mode.

# \_\_device\_\_ \_half \_\_ll2half\_rz (const long long int i)

Convert a signed 64-bit integer to a half in round-towards-zero mode.

# **Parameters**

i

- long long int. Is only being read.

# Returns

half

▶ į

converted to half.

# Description

Convert the signed 64-bit integer value i to a half-precision floating-point value in round-towards-zero mode.

# host device float low2float (const half2 a)

Converts low 16 bits of half2 to float and returns the result.

# **Parameters**

a

- half2. Is only being read.

## Returns

float

▶ The

low 16 bits of a converted to float.

# Description

Converts low 16 bits of half2 input a to 32-bit floating-point number and returns the result.

# \_device\_\_ \_half \_ low2half (const \_ half2 a)

Returns low 16 bits of half2 input.

# **Parameters**

а

- half2. Is only being read.

#### Returns

half

Returns

half which contains low 16 bits of the input a.

# Description

Returns low 16 bits of half2 input a.

\_\_device\_\_ \_half2 \_\_low2half2 (const \_\_half2 a)

Extracts low 16 bits from half2 input.

# **Parameters**

а

- half2. Is only being read.

# Returns

half2

► The

half2 with both halves equal to the low 16 bits of the input.

# Description

Extracts low 16 bits from half2 input a and returns a new half2 number which has both halves equal to the extracted bits.

\_\_device\_\_ \_half2 \_\_lowhigh2highlow (const \_\_half2 a)

Swaps both halves of the half2 input.

# **Parameters**

а

- half2. Is only being read.

# Returns

half2

a

with its halves being swapped.

# Description

Swaps both halves of the half2 input and returns a new half2 number with swapped halves.

# \_\_device\_\_ \_half2 \_\_lows2half2 (const \_\_half2 a, const \_\_half2 b)

Extracts low 16 bits from each of the two half2 inputs and combines into one half2 number.

# **Parameters**

а

- half2. Is only being read.

b

- half2. Is only being read.

#### Returns

half2

▶ The

low 16 bits of a and of b.

# Description

Extracts low 16 bits from each of the two half2 inputs and combines into one half2 number. Low 16 bits from input a is stored in low 16 bits of the return value, low 16 bits from input b is stored in high 16 bits of the return value.

# \_\_device\_\_ \_\_half \_\_shfl\_down\_sync (const unsigned mask, const \_\_half var, const unsigned int delta, const int width)

Exchange a variable between threads within a warp. Copy from a thread with higher ID relative to the caller.

# **Parameters**

#### mask

- unsigned int. Is only being read.

# var

- half. Is only being read.

#### delta

- int. Is only being read.

#### width

- int. Is only being read.

# Returns

Returns the 2-byte word referenced by var from the source thread ID as half. If the source thread ID is out of range or the source thread has exited, the calling thread's own var is returned.

# Description

Calculates a source thread ID by adding delta to the caller's thread ID. The value of var held by the resulting thread ID is returned: this has the effect of shifting var down the warp by delta threads. If width is less than warpSize then each subsection of the warp behaves as a separate entity with a starting logical thread ID of 0. As for <u>shfl up sync()</u>, the ID number of the source thread will not wrap around the value of width and so the upper delta threads will remain unchanged.

# \_\_device\_\_ \_\_half2 \_\_shfl\_down\_sync (const unsigned mask, const \_\_half2 var, const unsigned int delta, const int width)

Exchange a variable between threads within a warp. Copy from a thread with higher ID relative to the caller.

# **Parameters**

#### mask

- unsigned int. Is only being read.

#### var

- half2. Is only being read.

#### delta

- int. Is only being read.

#### width

- int. Is only being read.

#### Returns

Returns the 4-byte word referenced by var from the source thread ID as half2. If the source thread ID is out of range or the source thread has exited, the calling thread's own var is returned.

# Description

Calculates a source thread ID by adding delta to the caller's thread ID. The value of var held by the resulting thread ID is returned: this has the effect of shifting var down the warp by delta threads. If width is less than warpSize then each subsection of the warp behaves as a separate entity with a starting logical thread ID of 0. As for <u>shfl up sync()</u>, the ID number of

the source thread will not wrap around the value of width and so the upper delta threads will remain unchanged.

# \_\_device\_\_ \_half \_\_shfl\_sync (const unsigned mask, const half var, const int delta, const int width)

Exchange a variable between threads within a warp. Direct copy from indexed thread.

# **Parameters**

#### mask

- unsigned int. Is only being read.

#### var

- half. Is only being read.

#### delta

- int. Is only being read.

#### width

- int. Is only being read.

## Returns

Returns the 2-byte word referenced by var from the source thread ID as half. If the source thread ID is out of range or the source thread has exited, the calling thread's own var is returned.

# Description

Returns the value of var held by the thread whose ID is given by delta. If width is less than warpSize then each subsection of the warp behaves as a separate entity with a starting logical thread ID of 0. If delta is outside the range [0:width-1], the value returned corresponds to the value of var held by the delta modulo width (i.e. within the same subsection). width must have a value which is a power of 2; results are undefined if width is not a power of 2, or is a number greater than warpSize.

# \_\_device\_\_ \_half2 \_\_shfl\_sync (const unsigned mask, const \_half2 var, const int delta, const int width)

Exchange a variable between threads within a warp. Direct copy from indexed thread.

# **Parameters**

### mask

- unsigned int. Is only being read.

## var

- half2. Is only being read.

#### delta

- int. Is only being read.

# width

- int. Is only being read.

## Returns

Returns the 4-byte word referenced by var from the source thread ID as half2. If the source thread ID is out of range or the source thread has exited, the calling thread's own var is returned

# Description

Returns the value of var held by the thread whose ID is given by delta. If width is less than warpSize then each subsection of the warp behaves as a separate entity with a starting logical thread ID of 0. If delta is outside the range [0:width-1], the value returned corresponds to the value of var held by the delta modulo width (i.e. within the same subsection). width must have a value which is a power of 2; results are undefined if width is not a power of 2, or is a number greater than warpSize.

\_\_device\_\_ \_half \_\_shfl\_up\_sync (const unsigned mask, const \_\_half var, const unsigned int delta, const int width)

Exchange a variable between threads within a warp. Copy from a thread with lower ID relative to the caller.

# **Parameters**

#### mask

- unsigned int. Is only being read.

#### var

- half. Is only being read.

#### delta

- int. Is only being read.

#### width

- int. Is only being read.

#### Returns

Returns the 2-byte word referenced by var from the source thread ID as half. If the source thread ID is out of range or the source thread has exited, the calling thread's own var is returned.

# Description

Calculates a source thread ID by subtracting delta from the caller's lane ID. The value of var held by the resulting lane ID is returned: in effect, var is shifted up the warp by delta threads. If width is less than warpSize then each subsection of the warp behaves as a separate entity with a starting logical thread ID of 0. The source thread index will not wrap around the value of

width, so effectively the lower delta threads will be unchanged. width must have a value which is a power of 2; results are undefined if width is not a power of 2, or is a number greater than warpSize.

# \_\_device\_\_ \_half2 \_\_shfl\_up\_sync (const unsigned mask, const \_\_half2 var, const unsigned int delta, const int width)

Exchange a variable between threads within a warp. Copy from a thread with lower ID relative to the caller.

# **Parameters**

#### mask

- unsigned int. Is only being read.

#### var

- half2. Is only being read.

#### delta

- int. Is only being read.

#### width

- int. Is only being read.

#### Returns

Returns the 4-byte word referenced by var from the source thread ID as half2. If the source thread ID is out of range or the source thread has exited, the calling thread's own var is returned.

# Description

Calculates a source thread ID by subtracting delta from the caller's lane ID. The value of var held by the resulting lane ID is returned: in effect, var is shifted up the warp by delta threads. If width is less than warpSize then each subsection of the warp behaves as a separate entity with a starting logical thread ID of 0. The source thread index will not wrap around the value of width, so effectively the lower delta threads will be unchanged. width must have a value which is a power of 2; results are undefined if width is not a power of 2, or is a number greater than warpSize.

# \_\_device\_\_ \_\_half \_\_shfl\_xor\_sync (const unsigned mask, const \_\_half var, const int delta, const int width)

Exchange a variable between threads within a warp. Copy from a thread based on bitwise XOR of own thread ID.

# **Parameters**

## mask

- unsigned int. Is only being read.

#### var

- half. Is only being read.

#### delta

- int. Is only being read.

#### width

- int. Is only being read.

#### Returns

Returns the 2-byte word referenced by var from the source thread ID as half. If the source thread ID is out of range or the source thread has exited, the calling thread's own var is returned.

# Description

Calculates a source thread ID by performing a bitwise XOR of the caller's thread ID with mask: the value of var held by the resulting thread ID is returned. If width is less than warpSize then each group of width consecutive threads are able to access elements from earlier groups of threads, however if they attempt to access elements from later groups of threads their own value of var will be returned. This mode implements a butterfly addressing pattern such as is used in tree reduction and broadcast.

# \_\_device\_\_ \_\_half2 \_\_shfl\_xor\_sync (const unsigned mask, const half2 var, const int delta, const int width)

Exchange a variable between threads within a warp. Copy from a thread based on bitwise XOR of own thread ID.

## **Parameters**

#### mask

- unsigned int. Is only being read.

## var

- half2. Is only being read.

#### delta

- int. Is only being read.

# width

- int. Is only being read.

#### Returns

Returns the 4-byte word referenced by var from the source thread ID as half2. If the source thread ID is out of range or the source thread has exited, the calling thread's own var is returned.

# Description

Calculates a source thread ID by performing a bitwise XOR of the caller's thread ID with mask: the value of var held by the resulting thread ID is returned. If width is less than warpSize then each group of width consecutive threads are able to access elements from earlier groups of threads, however if they attempt to access elements from later groups of threads their own value of var will be returned. This mode implements a butterfly addressing pattern such as is used in tree reduction and broadcast.

# \_\_device\_\_ \_half \_\_short2half\_rd (const short int i)

Convert a signed short integer to a half in round-down mode.

# **Parameters**

i

- short int. Is only being read.

# Returns

half

**•** 

converted to half.

# Description

Convert the signed short integer value i to a half-precision floating-point value in round-down mode.

# \_\_host\_\_\_\_device\_\_ \_half \_\_short2half\_rn (const short int i)

Convert a signed short integer to a half in round-to-nearest-even mode.

# **Parameters**

i

- short int. Is only being read.

## Returns

half

i

converted to half.

# Description

Convert the signed short integer value i to a half-precision floating-point value in round-to-nearest-even mode.

# \_\_device\_\_ \_half \_\_short2half\_ru (const short int i)

Convert a signed short integer to a half in round-up mode.

# **Parameters**

i

- short int. Is only being read.

## Returns

half

converted to half.

# Description

Convert the signed short integer value i to a half-precision floating-point value in round-up mode.

# \_\_device\_\_ \_half \_\_short2half\_rz (const short int i)

Convert a signed short integer to a half in round-towards-zero mode.

# **Parameters**

i

- short int. Is only being read.

## Returns

half

converted to half.

# Description

Convert the signed short integer value i to a half-precision floating-point value in round-towards-zero mode.

# \_\_device\_\_ \_half \_\_short\_as\_half (const short int i)

Reinterprets bits in a signed short integer as a half.

# **Parameters**

i

- short int. Is only being read.

# Returns

half

The reinterpreted value.

# Description

Reinterprets the bits in the signed short integer i as a half-precision floating-point number.

# \_\_device\_\_ void \_\_stcg (const \_\_half \*ptr, const \_\_half value)

Generates a `st.global.cg` store instruction.

# **Parameters**

#### ptr

- memory location

#### value

- the value to be stored

# \_\_device\_\_ void \_\_stcg (const \_\_half2 \*ptr, const \_\_half2 value)

Generates a `st.global.cg` store instruction.

# **Parameters**

#### ptr

- memory location

# value

- the value to be stored

# \_\_device\_\_ void \_\_stcs (const \_\_half \*ptr, const \_\_half value)

Generates a `st.global.cs` store instruction.

# **Parameters**

# ptr

- memory location

#### value

- the value to be stored

# \_\_device\_\_ void \_\_stcs (const \_\_half2 \*ptr, const \_\_half2 value)

Generates a `st.global.cs` store instruction.

# **Parameters**

#### ptr

- memory location

#### value

- the value to be stored

# \_\_device\_\_ void \_\_stwb (const \_\_half \*ptr, const \_\_half value)

Generates a `st.global.wb` store instruction.

# **Parameters**

## ptr

- memory location

#### value

- the value to be stored

# \_\_device\_\_ void \_\_stwb (const \_\_half2 \*ptr, const \_\_half2 value)

Generates a `st.global.wb` store instruction.

# **Parameters**

#### ptr

- memory location

#### value

- the value to be stored

# \_\_device\_\_ void \_\_stwt (const \_\_half \*ptr, const \_\_half value)

Generates a `st.global.wt` store instruction.

# **Parameters**

# ptr

- memory location

#### value

- the value to be stored

# \_\_device\_\_ void \_\_stwt (const \_\_half2 \*ptr, const \_\_half2 value)

Generates a `st.global.wt` store instruction.

# **Parameters**

#### ptr

- memory location

#### value

- the value to be stored

\_\_device\_\_ \_half \_\_uint2half\_rd (const unsigned int i)

Convert an unsigned integer to a half in round-down mode.

# **Parameters**

i

- unsigned int. Is only being read.

# Returns

half

▶ i

converted to half.

# Description

Convert the unsigned integer value i to a half-precision floating-point value in round-down mode.

# \_\_host\_\_\_\_device\_\_ \_half \_\_uint2half\_rn (const unsigned int i)

Convert an unsigned integer to a half in round-to-nearest-even mode.

# **Parameters**

i

- unsigned int. Is only being read.

# Returns

half

▶ i

converted to half.

# Description

Convert the unsigned integer value i to a half-precision floating-point value in round-to-nearest-even mode.

# \_\_device\_\_ \_half \_\_uint2half\_ru (const unsigned int i)

Convert an unsigned integer to a half in round-up mode.

# **Parameters**

i

- unsigned int. Is only being read.

# Returns

half

▶ į

converted to half.

# Description

Convert the unsigned integer value  $\mathtt{i}$  to a half-precision floating-point value in round-up mode.

# \_\_device\_\_ \_half \_\_uint2half\_rz (const unsigned int i)

Convert an unsigned integer to a half in round-towards-zero mode.

# **Parameters**

i

- unsigned int. Is only being read.

# Returns

half

▶ j

converted to half.

# Description

Convert the unsigned integer value i to a half-precision floating-point value in round-towards-zero mode.

# \_\_device\_\_ \_half \_\_ull2half\_rd (const unsigned long long int i)

Convert an unsigned 64-bit integer to a half in round-down mode.

# **Parameters**

i

- unsigned long long int. Is only being read.

# Returns

half

converted to half.

# Description

Convert the unsigned 64-bit integer value  $\mathtt{i}$  to a half-precision floating-point value in round-down mode.

# \_\_host\_\_\_\_device\_\_ \_half \_\_ull2half\_rn (const unsigned long long int i)

Convert an unsigned 64-bit integer to a half in round-to-nearest-even mode.

# **Parameters**

i

- unsigned long long int. Is only being read.

# Returns

half

▶ i

converted to half.

# Description

Convert the unsigned 64-bit integer value  $\mathtt{i}$  to a half-precision floating-point value in round-to-nearest-even mode.

# \_\_device\_\_ \_half \_\_ull2half\_ru (const unsigned long long int i)

Convert an unsigned 64-bit integer to a half in round-up mode.

# **Parameters**

i

- unsigned long long int. Is only being read.

# Returns

half

converted to half.

# Description

Convert the unsigned 64-bit integer value i to a half-precision floating-point value in round-up mode.

# \_\_device\_\_ \_half \_\_ull2half\_rz (const unsigned long long int i)

Convert an unsigned 64-bit integer to a half in round-towards-zero mode.

# **Parameters**

i

- unsigned long long int. Is only being read.

# Returns

half

▶ i

converted to half.

# Description

Convert the unsigned 64-bit integer value  $\mathtt{i}$  to a half-precision floating-point value in round-towards-zero mode.

# \_\_device\_\_ \_half \_\_ushort2half\_rd (const unsigned short int i)

Convert an unsigned short integer to a half in round-down mode.

# **Parameters**

i

- unsigned short int. Is only being read.

# Returns

half

converted to half.

# Description

Convert the unsigned short integer value  $\mathtt{i}$  to a half-precision floating-point value in round-down mode.

# \_\_host\_\_\_device\_ \_ half \_\_ushort2half\_rn (const unsigned short int i)

Convert an unsigned short integer to a half in round-to-nearest-even mode.

# **Parameters**

i

- unsigned short int. Is only being read.

# Returns

half

▶ i

converted to half.

# Description

Convert the unsigned short integer value i to a half-precision floating-point value in round-to-nearest-even mode.

# \_\_device\_\_ \_half \_\_ushort2half\_ru (const unsigned short int i)

Convert an unsigned short integer to a half in round-up mode.

# **Parameters**

i

- unsigned short int. Is only being read.

# Returns

half

converted to half.

# Description

Convert the unsigned short integer value  $\mathtt{i}$  to a half-precision floating-point value in round-up mode.

# \_\_device\_\_ \_half \_\_ushort2half\_rz (const unsigned short int i)

Convert an unsigned short integer to a half in round-towards-zero mode.

# **Parameters**

i

- unsigned short int. Is only being read.

# Returns

half

▶ i

converted to half.

# Description

Convert the unsigned short integer value i to a half-precision floating-point value in round-towards-zero mode.

# \_\_device\_\_ \_half \_\_ushort\_as\_half (const unsigned short int i)

Reinterprets bits in an unsigned short integer as a half.

# **Parameters**

i

- unsigned short int. Is only being read.

# Returns

half

► The

reinterpreted value.

# Description

Reinterprets the bits in the unsigned short integer i as a half-precision floating-point number.

# 1.1.6. Half Math Functions

Half Precision Intrinsics

To use these functions, include the header file cuda\_fp16.h in your program.

# \_\_device\_\_ \_half hceil (const \_\_half h)

Calculate ceiling of the input argument.

# **Parameters**

h

- half. Is only being read.

# Returns

half

► The smallest integer value not less than h.

# Description

Compute the smallest integer value not less than h.

# \_\_device\_\_ \_half hcos (const \_\_half a)

Calculates half cosine in round-to-nearest-even mode.

# **Parameters**

а

- half. Is only being read.

# Returns

half

The cosine of a.

# Description

Calculates half cosine of input a in round-to-nearest-even mode.

# \_\_device\_\_ \_\_half hexp (const \_\_\_half a)

Calculates half natural exponential function in round-to-nearest mode.

# **Parameters**

a

- half. Is only being read.

# Returns

half

The natural exponential function on a.

# Description

Calculates half natural exponential function of input a in round-to-nearest-even mode.

# \_\_device\_\_ \_half hexp10 (const \_\_half a)

Calculates half decimal exponential function in round-to-nearest mode.

# **Parameters**

а

- half. Is only being read.

# Returns

half

► The decimal exponential function on a.

# Description

Calculates half decimal exponential function of input a in round-to-nearest-even mode.

# \_\_device\_\_ \_half hexp2 (const \_\_half a)

Calculates half binary exponential function in round-to-nearest mode.

# **Parameters**

a

- half. Is only being read.

#### Returns

half

▶ The

binary exponential function on a.

Calculates half binary exponential function of input a in round-to-nearest-even mode.

# \_\_device\_\_ \_\_half hfloor (const \_\_\_half h)

Calculate the largest integer less than or equal to h.

## **Parameters**

h

- half. Is only being read.

#### Returns

half

► The largest integer value which is less than or equal to h.

# Description

Calculate the largest integer value which is less than or equal to h.

# \_\_device\_\_ \_half hlog (const \_\_half a)

Calculates half natural logarithm in round-to-nearest-even mode.

## **Parameters**

а

- half. Is only being read.

## Returns

half

► The natural logarithm of a.

# Description

Calculates half natural logarithm of input a in round-to-nearest-even mode.

# \_\_device\_\_ \_half hlog10 (const \_\_half a)

Calculates half decimal logarithm in round-to-nearest-even mode.

## **Parameters**

a

- half. Is only being read.

#### Returns

half

The decimal logarithm of a.

# Description

Calculates half decimal logarithm of input a in round-to-nearest-even mode.

# 

Calculates half binary logarithm in round-to-nearest-even mode.

#### **Parameters**

а

- half. Is only being read.

#### Returns

half

The binary logarithm of a.

# Description

Calculates half binary logarithm of input a in round-to-nearest-even mode.

# \_\_device\_\_ \_\_half hrcp (const \_\_\_half a)

Calculates half reciprocal in round-to-nearest-even mode.

#### **Parameters**

a

- half. Is only being read.

#### Returns

half

The reciprocal of a.

# Description

Calculates half reciprocal of input a in round-to-nearest-even mode.

# \_\_device\_\_ \_half hrint (const \_\_half h)

Round input to nearest integer value in half-precision floating-point number.

## **Parameters**

h

- half. Is only being read.

## Returns

half

► The nearest integer to h.

# Description

Round h to the nearest integer value in half-precision floating-point format, with halfway cases rounded to the nearest even integer value.

# \_\_device\_\_ \_half hrsqrt (const \_\_half a)

Calculates half reciprocal square root in round-to-nearest-even mode.

## **Parameters**

а

- half. Is only being read.

## Returns

half

► The

reciprocal square root of a.

Calculates half reciprocal square root of input a in round-to-nearest mode.

# \_\_device\_\_ \_\_half hsin (const \_\_half a)

Calculates half sine in round-to-nearest-even mode.

## **Parameters**

а

- half. Is only being read.

#### Returns

half

The sine of a.

# Description

Calculates half sine of input a in round-to-nearest-even mode.

# \_\_device\_\_ \_half hsqrt (const \_\_half a)

Calculates half square root in round-to-nearest-even mode.

## **Parameters**

а

- half. Is only being read.

## Returns

half

► The square root of a.

# Description

Calculates half square root of input a in round-to-nearest-even mode.

# \_\_device\_\_ \_half htrunc (const \_\_half h)

Truncate input argument to the integral part.

## **Parameters**

h

- half. Is only being read.

#### Returns

half

The

truncated integer value.

# Description

Round h to the nearest integer value that does not exceed h in magnitude.

# 1.1.7. Half2 Math Functions

Half Precision Intrinsics

To use these functions, include the header file cuda fp16.h in your program.

# \_\_device\_\_ \_half2 h2ceil (const \_\_half2 h)

Calculate half2 vector ceiling of the input argument.

## **Parameters**

h

- half2. Is only being read.

#### Returns

half2

▶ The

vector of smallest integers not less than h.

# Description

For each component of vector h compute the smallest integer value not less than h.

# \_\_device\_\_ \_half2 h2cos (const \_\_half2 a)

Calculates half2 vector cosine in round-to-nearest-even mode.

## **Parameters**

a

- half2. Is only being read.

#### Returns

half2

The

elementwise cosine on vector a.

# Description

Calculates half2 cosine of input vector a in round-to-nearest-even mode.

# \_\_device\_\_ \_half2 h2exp (const \_\_half2 a)

Calculates half2 vector exponential function in round-to-nearest mode.

#### **Parameters**

а

- half2. Is only being read.

#### Returns

half2

► The

elementwise exponential function on vector a.

# Description

Calculates half2 exponential function of input vector a in round-to-nearest-even mode.

# \_device\_\_ \_half2 h2exp10 (const \_\_half2 a)

Calculates half2 vector decimal exponential function in round-to-nearest-even mode.

#### **Parameters**

а

- half2. Is only being read.

#### Returns

half2

The

elementwise decimal exponential function on vector a.

# Description

Calculates half2 decimal exponential function of input vector a in round-to-nearest-even mode.

# \_\_device\_\_ \_half2 h2exp2 (const \_\_half2 a)

Calculates half2 vector binary exponential function in round-to-nearest-even mode.

## **Parameters**

а

- half2. Is only being read.

#### Returns

half2

▶ The

elementwise binary exponential function on vector a.

# Description

Calculates half2 binary exponential function of input vector a in round-to-nearest-even mode.

# \_\_device\_\_ \_half2 h2floor (const \_\_half2 h)

Calculate the largest integer less than or equal to h.

### **Parameters**

h

- half2. Is only being read.

## Returns

half2

The

vector of largest integers which is less than or equal to h.

For each component of vector h calculate the largest integer value which is less than or equal to h.

# \_\_device\_\_ \_half2 h2log (const \_\_half2 a)

Calculates half2 vector natural logarithm in round-to-nearest-even mode.

#### **Parameters**

а

- half2. Is only being read.

#### Returns

half2

The elementwise natural logarithm on vector a.

# Description

Calculates half2 natural logarithm of input vector a in round-to-nearest-even mode.

# 

Calculates half2 vector decimal logarithm in round-to-nearest-even mode.

#### **Parameters**

а

- half2. Is only being read.

#### Returns

half2

► The elementwise decimal logarithm on vector a.

# Description

Calculates half2 decimal logarithm of input vector a in round-to-nearest-even mode.

# 

Calculates half2 vector binary logarithm in round-to-nearest-even mode.

## **Parameters**

a

- half2. Is only being read.

#### Returns

half2

The elementwise binary logarithm on vector a.

# Description

Calculates half2 binary logarithm of input vector a in round-to-nearest mode.

# 

Calculates half2 vector reciprocal in round-to-nearest-even mode.

#### **Parameters**

а

- half2. Is only being read.

#### Returns

half2

The elementwise reciprocal on vector a.

# Description

Calculates half2 reciprocal of input vector a in round-to-nearest-even mode.

# \_device\_\_ \_half2 h2rint (const \_\_half2 h)

Round input to nearest integer value in half-precision floating-point number.

## **Parameters**

h

- half2. Is only being read.

#### Returns

half2

► The vector of rounded integer values.

# Description

Round each component of half2 vector h to the nearest integer value in half-precision floating-point format, with halfway cases rounded to the nearest even integer value.

# \_\_device\_\_ \_half2 h2rsqrt (const \_\_half2 a)

Calculates half2 vector reciprocal square root in round-to-nearest mode.

#### **Parameters**

а

- half2. Is only being read.

#### Returns

half2

► The elementwise reciprocal square root on vector a.

# Description

Calculates half2 reciprocal square root of input vector a in round-to-nearest-even mode.

# \_\_device\_\_ \_half2 h2sin (const half2 a)

Calculates half2 vector sine in round-to-nearest-even mode.

#### **Parameters**

а

- half2. Is only being read.

#### Returns

half2

► The

elementwise sine on vector a.

Calculates half2 sine of input vector a in round-to-nearest-even mode.

# \_\_device\_\_ \_\_half2 h2sqrt (const \_\_\_half2 a)

Calculates half2 vector square root in round-to-nearest-even mode.

#### **Parameters**

а

- half2. Is only being read.

#### Returns

half2

► The elementwise square root on vector a.

# Description

Calculates half2 square root of input vector a in round-to-nearest mode.

# \_\_device\_\_ \_half2 h2trunc (const \_\_half2 h)

Truncate half2 vector input argument to the integral part.

## **Parameters**

h

- half2. Is only being read.

## Returns

half2

The

truncated h.

# Description

Round each component of vector h to the nearest integer value that does not exceed h in magnitude.

# 1.2. Bfloat16 Precision Intrinsics

This section describes nv\_bfloat16 precision intrinsic functions that are only supported in device code. To use these functions, include the header file cuda\_bf16.h in your program.

Bfloat16 Arithmetic Functions

Bfloat162 Arithmetic Functions

**Bfloat16 Comparison Functions** 

Bfloat162 Comparison Functions

Bfloat16 Precision Conversion and Data Movement

Bfloat16 Math Functions

Bfloat 162 Math Functions

# 1.2.1. Bfloat16 Arithmetic Functions

Bfloat16 Precision Intrinsics

To use these functions, include the header file cuda bf16.h in your program.

\_\_device\_\_ \_\_nv\_bfloat162 \_\_h2div (const \_\_nv\_bfloat162 a, const \_\_nv\_bfloat162 b)

Performs nv\_bfloat162 vector division in round-to-nearest-even mode.

## Description

Divides nv bfloat162 input vector a by input vector b in round-to-nearest mode.

\_\_device\_\_ \_nv\_bfloat16 \_\_habs (const \_\_nv\_bfloat16 a)

Calculates the absolute value of input  $nv\_bfloat16$  number and returns the result.

## **Parameters**

a

- nv\_bfloat16. Is only being read.

#### Returns

nv\_bfloat16

The absolute value of a.

# Description

Calculates the absolute value of input nv\_bfloat16 number and returns the result.

Performs nv\_bfloat16 addition in round-to-nearest-even mode.

# Description

Performs nv bfloat16 addition of inputs a and b, in round-to-nearest-even mode.

\_\_device\_\_ \_\_nv\_bfloat16 \_\_hadd\_sat (const \_\_nv\_bfloat16 a, const \_\_nv\_bfloat16 b)

Performs nv\_bfloat16 addition in round-to-nearest-even mode, with saturation to [0.0, 1.0].

## **Parameters**

а

- nv\_bfloat16. Is only being read.

b

- nv\_bfloat16. Is only being read.

#### Returns

nv bfloat16

The

sum of a and b, with respect to saturation.

Performs  $nv_bfloat16$  add of inputs a and b, in round-to-nearest-even mode, and clamps the result to range [0.0, 1.0]. NaN results are flushed to +0.0.

```
__device__ __nv_bfloat16 __hdiv (const __nv_bfloat16 a,
const __nv_bfloat16 b)
```

Performs nv bfloat16 division in round-to-nearest-even mode.

## Description

Divides nv bfloat16 input a by input b in round-to-nearest mode.

```
__device__ __nv_bfloat16 __hfma (const __nv_bfloat16 a, const __nv_bfloat16 b, const __nv_bfloat16 c)
```

Performs nv bfloat16 fused multiply-add in round-to-nearest-even mode.

# Description

Performs nv\_bfloat16 multiply on inputs a and b, then performs a nv\_bfloat16 add of the result with c, rounding the result once in round-to-nearest-even mode.

```
__device__ __nv_bfloat16 __hfma_relu (const __nv_bfloat16 a, const __nv_bfloat16 b, const __nv_bfloat16 c)
```

Performs  $nv\_bfloat16$  fused multiply-add in round-to-nearest-even mode with relusaturation.

### **Parameters**

а

- nv\_bfloat16. Is only being read.

b

- nv\_bfloat16. Is only being read.

C

- nv\_bfloat16. Is only being read.

#### Returns

nv bfloat16

► The

result of fused multiply-add operation on a, b, and c with relu saturation.

Performs nv\_bfloat16 multiply on inputs a and b, then performs a nv\_bfloat16 add of the result with c, rounding the result once in round-to-nearest-even mode. Then negative result is clamped to 0. NaN result is converted to canonical NaN.

\_\_device\_\_ \_\_nv\_bfloat16 \_\_hfma\_sat (const \_\_nv\_bfloat16 a, const \_\_nv\_bfloat16 b, const \_\_nv\_bfloat16 c)

Performs nv\_bfloat16 fused multiply-add in round-to-nearest-even mode, with saturation to [0.0, 1.0].

### **Parameters**

а

- nv\_bfloat16. Is only being read.

b

- nv\_bfloat16. Is only being read.

C

- nv\_bfloat16. Is only being read.

#### Returns

nv\_bfloat16

The

result of fused multiply-add operation on a, b, and c, with respect to saturation.

# Description

Performs nv\_bfloat16 multiply on inputs a and b, then performs a nv\_bfloat16 add of the result with c, rounding the result once in round-to-nearest-even mode, and clamps the result to range [0.0, 1.0]. NaN results are flushed to +0.0.

\_\_device\_\_ \_\_nv\_bfloat16 \_\_hmul (const \_\_nv\_bfloat16 a, const \_\_nv\_bfloat16 b)

Performs nv bfloat16 multiplication in round-to-nearest-even mode.

# Description

Performs nv bfloat16 multiplication of inputs a and b, in round-to-nearest mode.

\_\_device\_\_ \_\_nv\_bfloat16 \_\_hmul\_sat (const \_\_nv\_bfloat16 a, const \_\_nv\_bfloat16 b)

Performs  $nv_bfloat16$  multiplication in round-to-nearest-even mode, with saturation to [0.0, 1.0].

## **Parameters**

а

- nv\_bfloat16. Is only being read.

b

- nv\_bfloat16. Is only being read.

#### Returns

nv\_bfloat16

► The

result of multiplying a and b, with respect to saturation.

# Description

Performs  $nv\_bfloat16$  multiplication of inputs a and b, in round-to-nearest mode, and clamps the result to range [0.0, 1.0]. NaN results are flushed to +0.0.

\_\_device\_\_ \_nv\_bfloat16 \_\_hneg (const \_\_nv\_bfloat16 a)

Negates input  $nv\_bfloat16$  number and returns the result.

# Description

Negates input nv\_bfloat16 number and returns the result.

\_\_device\_\_ \_\_nv\_bfloat16 \_\_hsub (const \_\_nv\_bfloat16 a, const \_\_nv\_bfloat16 b)

Performs nv\_bfloat16 subtraction in round-to-nearest-even mode.

# Description

Subtracts nv bfloat16 input b from input a in round-to-nearest mode.

# \_\_device\_\_ \_\_nv\_bfloat16 \_\_hsub\_sat (const \_\_nv\_bfloat16 a, const \_\_nv\_bfloat16 b)

Performs nv\_bfloat16 subtraction in round-to-nearest-even mode, with saturation to [0.0, 1.0].

## **Parameters**

а

- nv\_bfloat16. Is only being read.

b

- nv\_bfloat16. Is only being read.

#### Returns

nv\_bfloat16

► The

result of subtraction of b from a, with respect to saturation.

# Description

Subtracts  $nv_bfloat16$  input b from input a in round-to-nearest mode, and clamps the result to range [0.0, 1.0]. NaN results are flushed to +0.0.

# 1.2.2. Bfloat 162 Arithmetic Functions

Bfloat16 Precision Intrinsics

To use these functions, include the header file cuda bf16.h in your program.

# \_\_device\_\_ \_nv\_bfloat162 \_\_habs2 (const \_\_nv\_bfloat162 a)

Calculates the absolute value of both halves of the input  $nv\_bfloat162$  number and returns the result.

#### **Parameters**

а

- nv\_bfloat162. Is only being read.

#### Returns

bfloat2

Returns

a with the absolute value of both halves.

Calculates the absolute value of both halves of the input  $nv\_bfloat162$  number and returns the result.

Performs nv bfloat162 vector addition in round-to-nearest-even mode.

# Description

Performs nv\_bfloat162 vector add of inputs a and b, in round-to-nearest mode.

```
__device__ __nv_bfloat162 __hadd2_sat (const __nv_bfloat162 a, const __nv_bfloat162 b)
```

Performs  $nv_bfloat162$  vector addition in round-to-nearest-even mode, with saturation to [0.0, 1.0].

#### **Parameters**

а

- nv\_bfloat162. Is only being read.

b

- nv bfloat162. Is only being read.

#### Returns

nv bfloat162

► The

sum of a and b, with respect to saturation.

# Description

Performs nv\_bfloat162 vector add of inputs a and b, in round-to-nearest mode, and clamps the results to range [0.0, 1.0]. NaN results are flushed to +0.0.

\_\_device\_\_ \_\_nv\_bfloat162 \_\_hcmadd (const \_\_nv\_bfloat162 a, const \_\_nv\_bfloat162 b, const \_\_nv\_bfloat162 c)

Performs fast complex multiply-accumulate.

#### **Parameters**

- a
- nv bfloat162. Is only being read.
- b
- nv\_bfloat162. Is only being read.

C

- nv\_bfloat162. Is only being read.

#### Returns

nv\_bfloat162

▶ The

result of complex multiply-accumulate operation on complex numbers a, b, and c

# Description

Interprets vector nv\_bfloat162 input pairs a, b, and c as complex numbers in nv\_bfloat16 precision and performs complex multiply-accumulate operation: a\*b + c

\_\_device\_\_ \_\_nv\_bfloat162 \_\_hfma2 (const \_\_nv\_bfloat162 a, const \_\_nv\_bfloat162 b, const \_\_nv\_bfloat162 c)

Performs nv bfloat162 vector fused multiply-add in round-to-nearest-even mode.

# Description

Performs nv\_bfloat162 vector multiply on inputs a and b, then performs a nv\_bfloat162 vector add of the result with c, rounding the result once in round-to-nearest-even mode.

```
__device__ __nv_bfloat162 __hfma2_relu (const
__nv_bfloat162 a, const __nv_bfloat162 b, const
__nv_bfloat162 c)
```

Performs  $nv\_bfloat162$  vector fused multiply-add in round-to-nearest-even mode with relusaturation.

## **Parameters**

а

- nv bfloat162. Is only being read.

b

- nv\_bfloat162. Is only being read.

c

- nv bfloat162. Is only being read.

#### Returns

nv\_bfloat162

► The

result of elementwise fused multiply-add operation on vectors a, b, and c with relu saturation.

# Description

Performs nv\_bfloat162 vector multiply on inputs a and b, then performs a nv\_bfloat162 vector add of the result with c, rounding the result once in round-to-nearest-even mode. Then negative result is clamped to 0. NaN result is converted to canonical NaN.

```
__device__ __nv_bfloat162 __hfma2_sat (const __nv_bfloat162 a, const __nv_bfloat162 b, const __nv_bfloat162 c)
```

Performs nv\_bfloat162 vector fused multiply-add in round-to-nearest-even mode, with saturation to [0.0, 1.0].

#### **Parameters**

а

- nv\_bfloat162. Is only being read.

b

- nv\_bfloat162. Is only being read.

C

- nv\_bfloat162. Is only being read.

#### Returns

nv\_bfloat162

► The

result of elementwise fused multiply-add operation on vectors a, b, and c, with respect to saturation.

Performs  $nv_bfloat162$  vector multiply on inputs a and b, then performs a  $nv_bfloat162$  vector add of the result with c, rounding the result once in round-to-nearest-even mode, and clamps the results to range [0.0, 1.0]. NaN results are flushed to +0.0.

Performs nv bfloat162 vector multiplication in round-to-nearest-even mode.

# Description

Performs nv\_bfloat162 vector multiplication of inputs a and b, in round-to-nearest-even mode

Performs nv\_bfloat162 vector multiplication in round-to-nearest-even mode, with saturation to [0.0, 1.0].

## **Parameters**

а

- nv bfloat162. Is only being read.

b

- nv\_bfloat162. Is only being read.

#### Returns

nv bfloat162

The

result of elementwise multiplication of vectors a and b, with respect to saturation.

# Description

Performs  $nv_bfloat162$  vector multiplication of inputs a and b, in round-to-nearest-even mode, and clamps the results to range [0.0, 1.0]. NaN results are flushed to +0.0.

Negates both halves of the input nv bfloat162 number and returns the result.

# Description

Negates both halves of the input nv\_bfloat162 number a and returns the result.

Performs nv\_bfloat162 vector subtraction in round-to-nearest-even mode.

# Description

Subtracts nv bfloat162 input vector b from input vector a in round-to-nearest-even mode.

Performs  $nv_bfloat162$  vector subtraction in round-to-nearest-even mode, with saturation to [0.0, 1.0].

#### **Parameters**

а

- nv\_bfloat162. Is only being read.

b

- nv\_bfloat162. Is only being read.

#### Returns

nv bfloat162

► The

subtraction of vector b from a, with respect to saturation.

## Description

Subtracts nv\_bfloat162 input vector b from input vector a in round-to-nearest-even mode, and clamps the results to range [0.0, 1.0]. NaN results are flushed to +0.0.

# 1.2.3. Bfloat16 Comparison Functions

Bfloat16 Precision Intrinsics

To use these functions, include the header file cuda bf16.h in your program.

Performs nv bfloat16 if-equal comparison.

#### **Parameters**

а

- nv\_bfloat16. Is only being read.

b

- nv\_bfloat16. Is only being read.

#### Returns

bool

The

boolean result of if-equal comparison of a and b.

# Description

Performs nv\_bfloat16 if-equal comparison of inputs a and b. NaN inputs generate false results.

# \_\_device\_\_ bool \_\_hequ (const \_\_nv\_bfloat16 a, const \_\_nv\_bfloat16 b)

Performs nv bfloat16 unordered if-equal comparison.

#### **Parameters**

а

- nv\_bfloat16. Is only being read.

b

- nv\_bfloat16. Is only being read.

#### Returns

bool

The

boolean result of unordered if-equal comparison of a and b.

# Description

Performs nv\_bfloat16 if-equal comparison of inputs a and b. NaN inputs generate true results.

Performs nv bfloat16 greater-equal comparison.

## **Parameters**

а

- nv\_bfloat16. Is only being read.

b

- nv\_bfloat16. Is only being read.

#### Returns

bool

The

boolean result of greater-equal comparison of a and b.

# Description

Performs nv\_bfloat16 greater-equal comparison of inputs a and b. NaN inputs generate false results.

# \_\_device\_\_ bool \_\_hgeu (const \_\_nv\_bfloat16 a, const \_\_nv\_bfloat16 b)

Performs nv bfloat16 unordered greater-equal comparison.

#### **Parameters**

а

- nv\_bfloat16. Is only being read.

b

- nv\_bfloat16. Is only being read.

#### Returns

bool

The

boolean result of unordered greater-equal comparison of a and b.

# Description

Performs nv\_bfloat16 greater-equal comparison of inputs a and b. NaN inputs generate true results.

Performs nv\_bfloat16 greater-than comparison.

#### **Parameters**

а

- nv\_bfloat16. Is only being read.

b

- nv\_bfloat16. Is only being read.

#### Returns

bool

The

boolean result of greater-than comparison of a and b.

# Description

Performs nv\_bfloat16 greater-than comparison of inputs a and b. NaN inputs generate false results.

# \_\_device\_\_ bool \_\_hgtu (const \_\_nv\_bfloat16 a, const \_\_nv\_bfloat16 b)

Performs nv bfloat16 unordered greater-than comparison.

#### **Parameters**

а

- nv\_bfloat16. Is only being read.

b

- nv\_bfloat16. Is only being read.

#### Returns

bool

▶ The

boolean result of unordered greater-than comparison of a and b.

# Description

Performs nv\_bfloat16 greater-than comparison of inputs a and b. NaN inputs generate true results.

# \_\_device\_\_ int \_\_hisinf (const \_\_nv\_bfloat16 a)

Checks if the input nv bfloat16 number is infinite.

## **Parameters**

a

- nv\_bfloat16. Is only being read.

## Returns

int

**▶** -1

iff a is equal to negative infinity,

iff a is equal to positive infinity,

► ∩

otherwise.

## Description

Checks if the input nv bfloat16 number a is infinite.

# \_\_device\_\_ bool \_\_hisnan (const \_\_nv\_bfloat16 a)

Determine whether nv\_bfloat16 argument is a NaN.

## **Parameters**

а

- nv\_bfloat16. Is only being read.

#### Returns

bool

true

iff argument is NaN.

# Description

Determine whether nv bfloat16 value a is a NaN.

Performs nv\_bfloat16 less-equal comparison.

## **Parameters**

а

- nv bfloat16. Is only being read.

b

- nv\_bfloat16. Is only being read.

#### Returns

bool

The

boolean result of less-equal comparison of a and b.

# Description

Performs nv\_bfloat16 less-equal comparison of inputs a and b. NaN inputs generate false results.

```
__device__ bool __hleu (const __nv_bfloat16 a, const
__nv_bfloat16 b)
```

Performs nv bfloat16 unordered less-equal comparison.

#### **Parameters**

а

- nv\_bfloat16. Is only being read.

b

- nv\_bfloat16. Is only being read.

#### Returns

bool

▶ The

boolean result of unordered less-equal comparison of a and b.

# Description

Performs nv\_bfloat16 less-equal comparison of inputs a and b. NaN inputs generate true results.

Performs nv\_bfloat16 less-than comparison.

## **Parameters**

а

- nv\_bfloat16. Is only being read.

b

- nv\_bfloat16. Is only being read.

#### Returns

bool

The

boolean result of less-than comparison of a and b.

# Description

Performs nv\_bfloat16 less-than comparison of inputs a and b. NaN inputs generate false results.

```
__device__ bool __hltu (const __nv_bfloat16 a, const __nv_bfloat16 b)
```

Performs nv bfloat16 unordered less-than comparison.

## **Parameters**

a

- nv\_bfloat16. Is only being read.

b

- nv\_bfloat16. Is only being read.

#### Returns

bool

The

boolean result of unordered less-than comparison of a and b.

# Description

Performs nv\_bfloat16 less-than comparison of inputs a and b. NaN inputs generate true results.

Calculates nv\_bfloat16 maximum of two input values.

# Description

Calculates nv bfloat16 max(a, b) defined as (a > b)? a : b.

- If either of inputs is NaN, the other input is returned.
- If both inputs are NaNs, then canonical NaN is returned.
- ▶ If values of both inputs are 0.0, then +0.0 > -0.0

Calculates nv\_bfloat16 maximum of two input values, NaNs pass through.

## Description

Calculates nv bfloat16 max(a, b) defined as (a > b)? a : b.

- If either of inputs is NaN, then canonical NaN is returned.
- ▶ If values of both inputs are 0.0, then +0.0 > -0.0

Calculates nv bfloat16 minimum of two input values.

# Description

Calculates nv\_bfloat16 min(a, b) defined as (a < b)? a : b.

- If either of inputs is NaN, the other input is returned.
- If both inputs are NaNs, then canonical NaN is returned.
- ▶ If values of both inputs are 0.0, then +0.0 > -0.0

Calculates nv bfloat16 minimum of two input values, NaNs pass through.

# Description

Calculates nv bfloat16 min(a, b) defined as (a < b)? a : b.

If either of inputs is NaN, then canonical NaN is returned.

▶ If values of both inputs are 0.0, then +0.0 > -0.0

# \_\_device\_\_ bool \_\_hne (const \_\_nv\_bfloat16 a, const nv bfloat16 b)

Performs nv bfloat16 not-equal comparison.

#### **Parameters**

а

- nv\_bfloat16. Is only being read.

b

- nv bfloat16. Is only being read.

## Returns

bool

▶ The

boolean result of not-equal comparison of a and b.

# Description

Performs nv\_bfloat16 not-equal comparison of inputs a and b. NaN inputs generate false results.

```
__device__ bool __hneu (const __nv_bfloat16 a, const
    nv bfloat16 b)
```

Performs nv bfloat16 unordered not-equal comparison.

## **Parameters**

а

- nv\_bfloat16. Is only being read.

b

- nv\_bfloat16. Is only being read.

#### Returns

bool

The

boolean result of unordered not-equal comparison of a and b.

# Description

Performs nv\_bfloat16 not-equal comparison of inputs a and b. NaN inputs generate true results.

# 1.2.4. Bfloat162 Comparison Functions

Bfloat16 Precision Intrinsics

To use these functions, include the header file cuda bf16.h in your program.

# \_\_device\_\_ bool \_\_hbeq2 (const \_\_nv\_bfloat162 a, const \_\_nv\_bfloat162 b)

Performs  $nv\_bfloat162$  vector if-equal comparison and returns boolean true iff both  $nv\_bfloat16$  results are true, boolean false otherwise.

## **Parameters**

a

- nv\_bfloat162. Is only being read.

b

- nv\_bfloat162. Is only being read.

#### Returns

bool

true

if both nv bfloat16 results of if-equal comparison of vectors a and b are true;

false

otherwise.

# Description

Performs nv\_bfloat162 vector if-equal comparison of inputs a and b. The bool result is set to true only if both nv\_bfloat16 if-equal comparisons evaluate to true, or false otherwise. NaN inputs generate false results.

```
__device__ bool __hbequ2 (const __nv_bfloat162 a, const __nv_bfloat162 b)
```

Performs nv\_bfloat162 vector unordered if-equal comparison and returns boolean true iff both nv bfloat16 results are true, boolean false otherwise.

#### **Parameters**

а

- nv\_bfloat162. Is only being read.

b

- nv\_bfloat162. Is only being read.

#### Returns

bool

true

if both  $nv_bfloat16$  results of unordered if-equal comparison of vectors a and b are true:

false

otherwise.

# Description

Performs nv\_bfloat162 vector if-equal comparison of inputs a and b. The bool result is set to true only if both nv\_bfloat16 if-equal comparisons evaluate to true, or false otherwise. NaN inputs generate true results.

```
__device__ bool __hbge2 (const __nv_bfloat162 a, const __nv_bfloat162 b)
```

Performs  $nv\_bfloat162$  vector greater-equal comparison and returns boolean true iff both  $nv\_bfloat16$  results are true, boolean false otherwise.

#### **Parameters**

а

- nv\_bfloat162. Is only being read.

b

- nv bfloat162. Is only being read.

#### Returns

bool

true

if both nv bfloat16 results of greater-equal comparison of vectors a and b are true;

false

otherwise.

# Description

Performs  $nv\_bfloat162$  vector greater-equal comparison of inputs a and b. The bool result is set to true only if both  $nv\_bfloat16$  greater-equal comparisons evaluate to true, or false otherwise. NaN inputs generate false results.

# \_\_device\_\_ bool \_\_hbgeu2 (const \_\_nv\_bfloat162 a, const nv bfloat162 b)

Performs nv\_bfloat162 vector unordered greater-equal comparison and returns boolean true iff both nv bfloat16 results are true, boolean false otherwise.

### **Parameters**

а

- nv\_bfloat162. Is only being read.

b

- nv\_bfloat162. Is only being read.

#### Returns

bool

true

if both  $nv_bfloat16$  results of unordered greater-equal comparison of vectors a and b are true:

false

otherwise.

# Description

Performs nv\_bfloat162 vector greater-equal comparison of inputs a and b. The bool result is set to true only if both nv\_bfloat16 greater-equal comparisons evaluate to true, or false otherwise. NaN inputs generate true results.

# \_\_device\_\_ bool \_\_hbgt2 (const \_\_nv\_bfloat162 a, const \_\_nv\_bfloat162 b)

Performs nv\_bfloat162 vector greater-than comparison and returns boolean true iff both nv bfloat16 results are true, boolean false otherwise.

#### **Parameters**

а

- nv\_bfloat162. Is only being read.

b

- nv\_bfloat162. Is only being read.

#### Returns

bool

true

if both nv bfloat16 results of greater-than comparison of vectors a and b are true;

false otherwise.

# Description

Performs  $nv\_bfloat162$  vector greater-than comparison of inputs a and b. The bool result is set to true only if both  $nv\_bfloat16$  greater-than comparisons evaluate to true, or false otherwise. NaN inputs generate false results.

# \_\_device\_\_ bool \_\_hbgtu2 (const \_\_nv\_bfloat162 a, const \_\_nv\_bfloat162 b)

Performs  $nv\_bfloat162$  vector unordered greater-than comparison and returns boolean true iff both  $nv\_bfloat16$  results are true, boolean false otherwise.

#### **Parameters**

а

- nv\_bfloat162. Is only being read.

b

- nv bfloat162. Is only being read.

#### Returns

bool

true

if both nv\_bfloat16 results of unordered greater-than comparison of vectors a and b are true:

false

otherwise.

# Description

Performs  $nv_bfloat162$  vector greater-than comparison of inputs a and b. The bool result is set to true only if both  $nv_bfloat16$  greater-than comparisons evaluate to true, or false otherwise. NaN inputs generate true results.

# \_\_device\_\_ bool \_\_hble2 (const \_\_nv\_bfloat162 a, const \_\_nv\_bfloat162 b)

Performs nv\_bfloat162 vector less-equal comparison and returns boolean true iff both nv bfloat16 results are true, boolean false otherwise.

### **Parameters**

а

- nv\_bfloat162. Is only being read.

b

- nv\_bfloat162. Is only being read.

#### Returns

bool

true

if both nv bfloat16 results of less-equal comparison of vectors a and b are true;

▶ false

otherwise.

# Description

Performs  $nv_bfloat162$  vector less-equal comparison of inputs a and b. The bool result is set to true only if both  $nv_bfloat16$  less-equal comparisons evaluate to true, or false otherwise. NaN inputs generate false results.

# \_\_device\_\_ bool \_\_hbleu2 (const \_\_nv\_bfloat162 a, const nv bfloat162 b)

Performs nv\_bfloat162 vector unordered less-equal comparison and returns boolean true iff both nv bfloat16 results are true, boolean false otherwise.

#### **Parameters**

а

- nv\_bfloat162. Is only being read.

b

- nv\_bfloat162. Is only being read.

### Returns

bool

▶ true

if both  $nv\_bfloat16$  results of unordered less-equal comparison of vectors a and b are true:

false

otherwise.

# Description

Performs  $nv_bfloat162$  vector less-equal comparison of inputs a and b. The bool result is set to true only if both  $nv_bfloat16$  less-equal comparisons evaluate to true, or false otherwise. NaN inputs generate true results.

# 

Performs nv\_bfloat162 vector less-than comparison and returns boolean true iff both nv bfloat16 results are true, boolean false otherwise.

## **Parameters**

а

- nv\_bfloat162. Is only being read.

b

- nv\_bfloat162. Is only being read.

#### Returns

bool

► true

if both nv bfloat16 results of less-than comparison of vectors a and b are true;

false

otherwise.

# Description

Performs  $nv\_bfloat162$  vector less-than comparison of inputs a and b. The bool result is set to true only if both  $nv\_bfloat16$  less-than comparisons evaluate to true, or false otherwise. NaN inputs generate false results.

# \_\_device\_\_ bool \_\_hbltu2 (const \_\_nv\_bfloat162 a, const \_\_nv\_bfloat162 b)

Performs nv\_bfloat162 vector unordered less-than comparison and returns boolean true iff both nv bfloat16 results are true, boolean false otherwise.

## **Parameters**

а

- nv\_bfloat162. Is only being read.

b

- nv\_bfloat162. Is only being read.

## Returns

bool

▶ true

if both  $nv\_bfloat16$  results of unordered less-than comparison of vectors a and b are true;

false

otherwise.

## Description

Performs  $nv\_bfloat162$  vector less-than comparison of inputs a and b. The bool result is set to true only if both  $nv\_bfloat16$  less-than comparisons evaluate to true, or false otherwise. NaN inputs generate true results.

# \_\_device\_\_ bool \_\_hbne2 (const \_\_nv\_bfloat162 a, const \_\_nv\_bfloat162 b)

Performs nv\_bfloat162 vector not-equal comparison and returns boolean true iff both nv bfloat16 results are true, boolean false otherwise.

#### **Parameters**

а

- nv\_bfloat162. Is only being read.

b

- nv\_bfloat162. Is only being read.

#### Returns

bool

true

if both nv bfloat16 results of not-equal comparison of vectors a and b are true,

false otherwise.

## Description

Performs  $nv_bfloat162$  vector not-equal comparison of inputs a and b. The bool result is set to true only if both  $nv_bfloat16$  not-equal comparisons evaluate to true, or false otherwise. NaN inputs generate false results.

## \_\_device\_\_ bool \_\_hbneu2 (const \_\_nv\_bfloat162 a, const \_\_nv\_bfloat162 b)

Performs nv\_bfloat162 vector unordered not-equal comparison and returns boolean true iff both nv bfloat16 results are true, boolean false otherwise.

## **Parameters**

а

- nv\_bfloat162. Is only being read.

b

- nv bfloat162. Is only being read.

#### Returns

bool

true

if both  $nv\_bfloat16$  results of unordered not-equal comparison of vectors a and b are true;

false

otherwise.

## Description

Performs  $nv\_bfloat162$  vector not-equal comparison of inputs a and b. The bool result is set to true only if both  $nv\_bfloat16$  not-equal comparisons evaluate to true, or false otherwise. NaN inputs generate true results.

# \_\_device\_\_ \_\_nv\_bfloat162 \_\_heq2 (const \_\_nv\_bfloat162 a, const \_\_nv\_bfloat162 b)

Performs nv\_bfloat162 vector if-equal comparison.

### **Parameters**

а

- nv\_bfloat162. Is only being read.

b

- nv\_bfloat162. Is only being read.

#### Returns

nv\_bfloat162

► The

vector result of if-equal comparison of vectors a and b.

## Description

Performs nv\_bfloat162 vector if-equal comparison of inputs a and b. The corresponding nv\_bfloat16 results are set to 1.0 for true, or 0.0 for false. NaN inputs generate false results.

# \_\_device\_\_ \_nv\_bfloat162 \_\_hequ2 (const \_\_nv\_bfloat162 a, const \_\_nv\_bfloat162 b)

Performs nv\_bfloat162 vector unordered if-equal comparison.

## **Parameters**

а

- nv\_bfloat162. Is only being read.

b

- nv\_bfloat162. Is only being read.

### Returns

nv\_bfloat162

The

vector result of unordered if-equal comparison of vectors a and b.

## Description

Performs nv\_bfloat162 vector if-equal comparison of inputs a and b. The corresponding nv\_bfloat16 results are set to 1.0 for true, or 0.0 for false. NaN inputs generate true results.

\_\_device\_\_ \_\_nv\_bfloat162 \_\_hge2 (const \_\_nv\_bfloat162 a, const \_\_nv\_bfloat162 b)

Performs nv bfloat162 vector greater-equal comparison.

#### **Parameters**

a

- nv\_bfloat162. Is only being read.

b

- nv\_bfloat162. Is only being read.

### Returns

nv\_bfloat162

The

vector result of greater-equal comparison of vectors a and b.

## Description

Performs  $nv_bfloat162$  vector greater-equal comparison of inputs a and b. The corresponding  $nv_bfloat16$  results are set to 1.0 for true, or 0.0 for false. NaN inputs generate false results.

\_\_device\_\_ \_\_nv\_bfloat162 \_\_hgeu2 (const \_\_nv\_bfloat162 a, const \_\_nv\_bfloat162 b)

Performs nv bfloat162 vector unordered greater-equal comparison.

### **Parameters**

a

- nv\_bfloat162. Is only being read.

b

- nv\_bfloat162. Is only being read.

#### Returns

nv\_bfloat162

► The

nv\_bfloat162 vector result of unordered greater-equal comparison of vectors a and b.

## Description

Performs  $nv_bfloat162$  vector greater-equal comparison of inputs a and b. The corresponding  $nv_bfloat16$  results are set to 1.0 for true, or 0.0 for false. NaN inputs generate true results.

# \_\_device\_\_ \_\_nv\_bfloat162 \_\_hgt2 (const \_\_nv\_bfloat162 a, const \_\_nv\_bfloat162 b)

Performs nv\_bfloat162 vector greater-than comparison.

## **Parameters**

а

- nv\_bfloat162. Is only being read.

b

- nv\_bfloat162. Is only being read.

### Returns

nv\_bfloat162

► The

vector result of greater-than comparison of vectors a and b.

## Description

Performs  $nv\_bfloat162$  vector greater-than comparison of inputs a and b. The corresponding  $nv\_bfloat16$  results are set to 1.0 for true, or 0.0 for false. NaN inputs generate false results.

## \_\_device\_\_ \_\_nv\_bfloat162 \_\_hgtu2 (const \_\_nv\_bfloat162 a, const \_\_nv\_bfloat162 b)

Performs nv bfloat162 vector unordered greater-than comparison.

## **Parameters**

a

- nv\_bfloat162. Is only being read.

b

- nv bfloat162. Is only being read.

#### Returns

nv\_bfloat162

▶ The

nv bfloat162 vector result of unordered greater-than comparison of vectors a and b.

## Description

Performs  $nv_bfloat162$  vector greater-than comparison of inputs a and b. The corresponding  $nv_bfloat16$  results are set to 1.0 for true, or 0.0 for false. NaN inputs generate true results.

# \_\_device\_\_ \_nv\_bfloat162 \_\_hisnan2 (const \_\_nv\_bfloat162 a)

Determine whether nv bfloat162 argument is a NaN.

#### **Parameters**

а

- nv\_bfloat162. Is only being read.

### Returns

nv\_bfloat162

The

 $nv_bfloat162$  with the corresponding  $nv_bfloat16$  results set to 1.0 for NaN, 0.0 otherwise.

## Description

Determine whether each nv\_bfloat16 of input nv\_bfloat162 number a is a NaN.

# \_\_device\_\_ \_\_nv\_bfloat162 \_\_hle2 (const \_\_nv\_bfloat162 a, const \_\_nv\_bfloat162 b)

Performs nv bfloat162 vector less-equal comparison.

#### **Parameters**

а

- nv\_bfloat162. Is only being read.

b

- nv\_bfloat162. Is only being read.

### Returns

nv bfloat162

▶ The

nv bfloat162 result of less-equal comparison of vectors a and b.

## Description

Performs  $nv_bfloat162$  vector less-equal comparison of inputs a and b. The corresponding  $nv_bfloat16$  results are set to 1.0 for true, or 0.0 for false. NaN inputs generate false results.

# \_\_device\_\_ \_\_nv\_bfloat162 \_\_hleu2 (const \_\_nv\_bfloat162 a, const \_\_nv\_bfloat162 b)

Performs nv bfloat162 vector unordered less-equal comparison.

#### **Parameters**

а

- nv\_bfloat162. Is only being read.

b

- nv\_bfloat162. Is only being read.

### Returns

nv\_bfloat162

► The

vector result of unordered less-equal comparison of vectors a and b.

## Description

Performs nv\_bfloat162 vector less-equal comparison of inputs a and b. The corresponding nv\_bfloat16 results are set to 1.0 for true, or 0.0 for false. NaN inputs generate true results.

## \_\_device\_\_ \_\_nv\_bfloat162 \_\_hlt2 (const \_\_nv\_bfloat162 a, const \_\_nv\_bfloat162 b)

Performs nv bfloat162 vector less-than comparison.

## **Parameters**

a

- nv\_bfloat162. Is only being read.

b

- nv bfloat162. Is only being read.

#### Returns

nv\_bfloat162

► The

nv bfloat162 vector result of less-than comparison of vectors a and b.

## Description

Performs nv\_bfloat162 vector less-than comparison of inputs a and b. The corresponding nv\_bfloat16 results are set to 1.0 for true, or 0.0 for false. NaN inputs generate false results.

# \_\_device\_\_ \_\_nv\_bfloat162 \_\_hltu2 (const \_\_nv\_bfloat162 a, const \_\_nv\_bfloat162 b)

Performs nv bfloat162 vector unordered less-than comparison.

#### **Parameters**

а

- nv\_bfloat162. Is only being read.

b

- nv\_bfloat162. Is only being read.

#### Returns

nv bfloat162

► The

vector result of unordered less-than comparison of vectors a and b.

## Description

Performs nv\_bfloat162 vector less-than comparison of inputs a and b. The corresponding nv\_bfloat16 results are set to 1.0 for true, or 0.0 for false. NaN inputs generate true results.

# \_\_device\_\_ \_nv\_bfloat162 \_\_hmax2 (const \_\_nv\_bfloat162 a, const \_\_nv\_bfloat162 b)

Calculates nv bfloat162 vector maximum of two inputs.

## Description

Calculates nv\_bfloat162 vector max(a, b). Elementwise nv\_bfloat16 operation is defined as (a > b)? a : b.

- If either of inputs is NaN, the other input is returned.
- If both inputs are NaNs, then canonical NaN is returned.
- ▶ If values of both inputs are 0.0, then +0.0 > -0.0

Calculates nv bfloat162 vector maximum of two inputs, NaNs pass through.

## Description

Calculates nv\_bfloat162 vector max(a, b). Elementwise nv\_bfloat16 operation is defined as (a > b)? a : b.

- If either of inputs is NaN, then canonical NaN is returned.
- ► If values of both inputs are 0.0, then +0.0 > -0.0

Calculates nv bfloat162 vector minimum of two inputs.

## Description

Calculates  $nv_bfloat162$  vector min(a, b). Elementwise  $nv_bfloat16$  operation is defined as (a < b)? a : b.

- If either of inputs is NaN, the other input is returned.
- If both inputs are NaNs, then canonical NaN is returned.
- ▶ If values of both inputs are 0.0, then +0.0 > -0.0

```
__device__ __nv_bfloat162 __hmin2_nan (const __nv_bfloat162 a, const __nv_bfloat162 b)
```

Calculates nv bfloat162 vector minimum of two inputs, NaNs pass through.

## Description

Calculates  $nv_bfloat162$  vector min(a, b). Elementwise  $nv_bfloat16$  operation is defined as (a < b)? a : b.

- If either of inputs is NaN, then canonical NaN is returned.
- ▶ If values of both inputs are 0.0, then +0.0 > -0.0

# \_\_device\_\_ \_\_nv\_bfloat162 \_\_hne2 (const \_\_nv\_bfloat162 a, const \_\_nv\_bfloat162 b)

Performs nv\_bfloat162 vector not-equal comparison.

### **Parameters**

а

- nv\_bfloat162. Is only being read.

b

- nv\_bfloat162. Is only being read.

#### Returns

nv\_bfloat162

► The

vector result of not-equal comparison of vectors a and b.

## Description

Performs nv\_bfloat162 vector not-equal comparison of inputs a and b. The corresponding nv\_bfloat16 results are set to 1.0 for true, or 0.0 for false. NaN inputs generate false results.

# \_\_device\_\_ \_nv\_bfloat162 \_\_hneu2 (const \_\_nv\_bfloat162 a, const \_\_nv\_bfloat162 b)

Performs nv\_bfloat162 vector unordered not-equal comparison.

## **Parameters**

а

- nv\_bfloat162. Is only being read.

b

- nv\_bfloat162. Is only being read.

### Returns

nv\_bfloat162

► The

vector result of unordered not-equal comparison of vectors a and b.

## Description

Performs  $nv\_bfloat162$  vector not-equal comparison of inputs a and b. The corresponding  $nv\_bfloat16$  results are set to 1.0 for true, or 0.0 for false. NaN inputs generate true results.

# 1.2.5. Bfloat16 Precision Conversion and Data Movement

Bfloat16 Precision Intrinsics

To use these functions, include the header file cuda bf16.h in your program.

Converts both halves of nv bfloat162 to float2 and returns the result.

#### **Parameters**

а

- nv\_bfloat162. Is only being read.

#### Returns

float2

▶ a

converted to float2.

## Description

Converts both halves of nv\_bfloat162 input a to float2 and returns the result.

```
__device__ __nv_bfloat162 __bfloat162bfloat162 (const __nv_bfloat16 a)
```

Returns nv bfloat162 with both halves equal to the input value.

#### **Parameters**

a

- nv\_bfloat16. Is only being read.

### Returns

nv bfloat162

▶ The

vector which has both its halves equal to the input a.

## Description

Returns nv\_bfloat162 number with both halves equal to the input a nv\_bfloat16 number.

# \_\_host\_\_\_\_device\_\_ float \_\_bfloat162float (const \_\_nv\_bfloat16 a)

Converts nv bfloat16 number to float.

## **Parameters**

а

- float. Is only being read.

### Returns

float

a

converted to float.

## Description

Converts nv\_bfloat16 number a to float.

## \_\_device\_\_ int \_\_bfloat162int\_rd (const \_\_nv\_bfloat16 h)

Convert a nv\_bfloat16 to a signed integer in round-down mode.

#### **Parameters**

h

- nv\_bfloat16. Is only being read.

#### Returns

int

▶ h

converted to a signed integer.

## Description

Convert the nv\_bfloat16 floating-point value h to a signed integer in round-down mode.

## \_\_device\_\_ int \_\_bfloat162int\_rn (const \_\_nv\_bfloat16 h)

Convert a nv\_bfloat16 to a signed integer in round-to-nearest-even mode.

## **Parameters**

h

- nv\_bfloat16. Is only being read.

## Returns

int

h converted to a signed integer.

## Description

Convert the nv\_bfloat16 floating-point value h to a signed integer in round-to-nearest-even mode.

## \_\_device\_\_ int \_\_bfloat162int\_ru (const \_\_nv\_bfloat16 h)

Convert a nv\_bfloat16 to a signed integer in round-up mode.

## **Parameters**

h

- nv\_bfloat16. Is only being read.

#### Returns

int

h converted to a signed integer.

## Description

Convert the nv\_bfloat16 floating-point value h to a signed integer in round-up mode.

Convert a nv\_bfloat16 to a signed integer in round-towards-zero mode.

## **Parameters**

h

- nv\_bfloat16. Is only being read.

## Returns

int

► h

converted to a signed integer.

## Description

Convert the  $nv_bfloat16$  floating-point value h to a signed integer in round-towards-zero mode.

## \_\_device\_\_ long long int \_\_bfloat162ll\_rd (const \_\_nv\_bfloat16 h)

Convert a nv\_bfloat16 to a signed 64-bit integer in round-down mode.

## **Parameters**

h

- nv\_bfloat16. Is only being read.

### Returns

long long int

► h

converted to a signed 64-bit integer.

## Description

Convert the nv\_bfloat16 floating-point value h to a signed 64-bit integer in round-down mode.

## \_\_device\_\_ long long int \_\_bfloat162ll\_rn (const \_\_nv\_bfloat16 h)

Convert a nv\_bfloat16 to a signed 64-bit integer in round-to-nearest-even mode.

## **Parameters**

h

- nv\_bfloat16. Is only being read.

## Returns

long long int

▶ h

converted to a signed 64-bit integer.

## Description

Convert the nv\_bfloat16 floating-point value h to a signed 64-bit integer in round-to-nearest-even mode.

## \_\_device\_\_ long long int \_\_bfloat162ll\_ru (const \_\_nv\_bfloat16 h)

Convert a nv\_bfloat16 to a signed 64-bit integer in round-up mode.

## **Parameters**

h

- nv\_bfloat16. Is only being read.

### Returns

long long int

► h

converted to a signed 64-bit integer.

## Description

Convert the nv\_bfloat16 floating-point value h to a signed 64-bit integer in round-up mode.

# \_\_host\_\_\_\_device\_\_ long long int \_\_bfloat162ll\_rz (const \_\_nv\_bfloat16 h)

Convert a nv\_bfloat16 to a signed 64-bit integer in round-towards-zero mode.

## **Parameters**

h

- nv\_bfloat16. Is only being read.

## Returns

long long int

► h

converted to a signed 64-bit integer.

## Description

Convert the nv\_bfloat16 floating-point value h to a signed 64-bit integer in round-towards-zero mode.

## \_\_device\_\_ short int \_\_bfloat162short\_rd (const \_\_nv\_bfloat16 h)

Convert a nv\_bfloat16 to a signed short integer in round-down mode.

## **Parameters**

h

- nv\_bfloat16. Is only being read.

### Returns

short int

► h

converted to a signed short integer.

## Description

Convert the nv\_bfloat16 floating-point value h to a signed short integer in round-down mode.

## \_\_device\_\_ short int \_\_bfloat162short\_rn (const \_\_nv\_bfloat16 h)

Convert a nv\_bfloat16 to a signed short integer in round-to-nearest-even mode.

## **Parameters**

h

- nv\_bfloat16. Is only being read.

## Returns

short int

► h

converted to a signed short integer.

## Description

Convert the nv\_bfloat16 floating-point value h to a signed short integer in round-to-nearest-even mode.

## \_\_device\_\_ short int \_\_bfloat162short\_ru (const \_\_nv\_bfloat16 h)

Convert a nv\_bfloat16 to a signed short integer in round-up mode.

## **Parameters**

h

- nv\_bfloat16. Is only being read.

### Returns

short int

► h

converted to a signed short integer.

## Description

Convert the nv\_bfloat16 floating-point value h to a signed short integer in round-up mode.

# \_\_host\_\_\_\_device\_\_ short int \_\_bfloat162short\_rz (const \_\_nv\_bfloat16 h)

Convert a nv\_bfloat16 to a signed short integer in round-towards-zero mode.

## **Parameters**

h

- nv\_bfloat16. Is only being read.

## Returns

short int

► h

converted to a signed short integer.

## Description

Convert the nv\_bfloat16 floating-point value h to a signed short integer in round-towards-zero mode.

## \_\_device\_\_ unsigned int \_\_bfloat162uint\_rd (const \_\_nv\_bfloat16 h)

Convert a nv\_bfloat16 to an unsigned integer in round-down mode.

## **Parameters**

h

- nv\_bfloat16. Is only being read.

### Returns

unsigned int

► h

converted to an unsigned integer.

## Description

Convert the nv\_bfloat16 floating-point value h to an unsigned integer in round-down mode.

## \_\_device\_\_ unsigned int \_\_bfloat162uint\_rn (const \_\_nv\_bfloat16 h)

Convert a nv\_bfloat16 to an unsigned integer in round-to-nearest-even mode.

## **Parameters**

h

- nv\_bfloat16. Is only being read.

## Returns

unsigned int

► h

converted to an unsigned integer.

## Description

Convert the nv\_bfloat16 floating-point value h to an unsigned integer in round-to-nearest-even mode.

## \_\_device\_\_ unsigned int \_\_bfloat162uint\_ru (const \_\_nv\_bfloat16 h)

Convert a nv\_bfloat16 to an unsigned integer in round-up mode.

## **Parameters**

h

- nv\_bfloat16. Is only being read.

### Returns

unsigned int

► h

converted to an unsigned integer.

## Description

Convert the nv\_bfloat16 floating-point value h to an unsigned integer in round-up mode.

# \_\_host\_\_\_\_device\_\_ unsigned int \_\_bfloat162uint\_rz (const \_\_nv\_bfloat16 h)

Convert a nv\_bfloat16 to an unsigned integer in round-towards-zero mode.

## **Parameters**

h

- nv\_bfloat16. Is only being read.

## Returns

unsigned int

▶ h

converted to an unsigned integer.

## Description

Convert the nv\_bfloat16 floating-point value h to an unsigned integer in round-towards-zero mode.

## \_\_device\_\_ unsigned long long int \_\_bfloat162ull\_rd (const \_\_nv\_bfloat16 h)

Convert a nv\_bfloat16 to an unsigned 64-bit integer in round-down mode.

## **Parameters**

h

- nv\_bfloat16. Is only being read.

### Returns

unsigned long long int

► h

converted to an unsigned 64-bit integer.

## Description

Convert the nv\_bfloat16 floating-point value h to an unsigned 64-bit integer in round-down mode.

## \_\_device\_\_ unsigned long long int \_\_bfloat162ull\_rn (const \_\_nv\_bfloat16 h)

Convert a nv\_bfloat16 to an unsigned 64-bit integer in round-to-nearest-even mode.

### **Parameters**

h

- nv\_bfloat16. Is only being read.

## Returns

unsigned long long int

h converted to an unsigned 64-bit integer.

## Description

Convert the nv\_bfloat16 floating-point value h to an unsigned 64-bit integer in round-to-nearest-even mode.

## \_\_device\_\_ unsigned long long int \_\_bfloat162ull\_ru (const \_\_nv\_bfloat16 h)

Convert a nv\_bfloat16 to an unsigned 64-bit integer in round-up mode.

## **Parameters**

h

- nv\_bfloat16. Is only being read.

### Returns

unsigned long long int

h converted to an unsigned 64-bit integer.

## Description

Convert the nv\_bfloat16 floating-point value h to an unsigned 64-bit integer in round-up mode.

# \_\_host\_\_\_\_device\_\_ unsigned long long int \_\_bfloat162ull\_rz (const \_\_nv\_bfloat16 h)

Convert a nv\_bfloat16 to an unsigned 64-bit integer in round-towards-zero mode.

## **Parameters**

h

- nv\_bfloat16. Is only being read.

## Returns

unsigned long long int

h

converted to an unsigned 64-bit integer.

## Description

Convert the nv\_bfloat16 floating-point value h to an unsigned 64-bit integer in round-towards-zero mode.

## \_\_device\_\_ unsigned short int \_\_bfloat162ushort\_rd (const \_\_nv\_bfloat16 h)

Convert a nv\_bfloat16 to an unsigned short integer in round-down mode.

## **Parameters**

h

- nv\_bfloat16. Is only being read.

### Returns

unsigned short int

► h

converted to an unsigned short integer.

## Description

Convert the nv\_bfloat16 floating-point value h to an unsigned short integer in round-down mode.

## \_\_device\_\_ unsigned short int \_\_bfloat162ushort\_rn (const \_\_nv\_bfloat16 h)

Convert a nv\_bfloat16 to an unsigned short integer in round-to-nearest-even mode.

### **Parameters**

h

- nv\_bfloat16. Is only being read.

## Returns

unsigned short int

► h

converted to an unsigned short integer.

## Description

Convert the nv\_bfloat16 floating-point value h to an unsigned short integer in round-to-nearest-even mode.

## \_\_device\_\_ unsigned short int \_\_bfloat162ushort\_ru (const \_\_nv\_bfloat16 h)

Convert a nv\_bfloat16 to an unsigned short integer in round-up mode.

## **Parameters**

h

- nv\_bfloat16. Is only being read.

### Returns

unsigned short int

► h

converted to an unsigned short integer.

## Description

Convert the nv\_bfloat16 floating-point value h to an unsigned short integer in round-up mode.

Convert a nv\_bfloat16 to an unsigned short integer in round-towards-zero mode.

## **Parameters**

h

- nv\_bfloat16. Is only being read.

## Returns

unsigned short int

► h

converted to an unsigned short integer.

## Description

Convert the nv\_bfloat16 floating-point value h to an unsigned short integer in round-towards-zero mode.

Reinterprets bits in a nv bfloat16 as a signed short integer.

## **Parameters**

h

- nv\_bfloat16. Is only being read.

### Returns

short int

The reinterpreted value.

## Description

Reinterprets the bits in the nv\_bfloat16 floating-point number h as a signed short integer.

## \_\_device\_\_ unsigned short int \_\_bfloat16\_as\_ushort (const \_\_nv\_bfloat16 h)

Reinterprets bits in a nv bfloat16 as an unsigned short integer.

## **Parameters**

h

- nv\_bfloat16. Is only being read.

## Returns

unsigned short int

The reinterpreted value.

## Description

Reinterprets the bits in the nv\_bfloat16 floating-point h as an unsigned short number.

## \_\_host\_\_\_\_device\_\_ \_nv\_bfloat16 \_\_double2bfloat16 (const double a)

Converts double number to  $nv_bfloat16$  precision in round-to-nearest-even mode and returns  $nv_bfloat16$  with converted value.

#### **Parameters**

а

- double. Is only being read.

#### Returns

nv\_bfloat16

a converted to nv bfloat16.

## Description

Converts double number a to nv\_bfloat16 precision in round-to-nearest-even mode.

## \_\_host\_\_\_\_device\_\_ \_nv\_bfloat162 \_\_float22bfloat162\_rn (const float2 a)

Converts both components of float2 number to nv\_bfloat16 precision in round-to-nearest-even mode and returns nv bfloat162 with converted values.

## **Parameters**

a

- float2. Is only being read.

#### Returns

nv bfloat162

► The

 $nv\_bfloat162$  which has corresponding halves equal to the converted float2 components.

## Description

Converts both components of float2 to  $nv_bfloat16$  precision in round-to-nearest mode and combines the results into one  $nv_bfloat162$  number. Low 16 bits of the return value correspond to a.x and high 16 bits of the return value correspond to a.y.

## \_\_host\_\_\_\_device\_\_ \_nv\_bfloat16 \_\_float2bfloat16 (const float a)

Converts float number to nv\_bfloat16 precision in round-to-nearest-even mode and returns nv bfloat16 with converted value.

### **Parameters**

a

- float. Is only being read.

## Returns

nv bfloat16

**▶** a

converted to nv\_bfloat16.

## Description

Converts float number a to nv\_bfloat16 precision in round-to-nearest-even mode.

# \_\_host\_\_\_\_device\_\_ \_nv\_bfloat162 \_\_float2bfloat162\_rn (const float a)

Converts input to nv\_bfloat16 precision in round-to-nearest-even mode and populates both halves of nv bfloat162 with converted value.

## **Parameters**

a

- float. Is only being read.

#### Returns

nv bfloat162

The

nv\_bfloat162 value with both halves equal to the converted nv\_bfloat16 precision number.

## Description

Converts input a to nv\_bfloat16 precision in round-to-nearest-even mode and populates both halves of nv\_bfloat162 with converted value.

# \_\_host\_\_\_\_device\_\_ \_\_nv\_bfloat16 \_\_float2bfloat16\_rd (const float a)

Converts float number to nv\_bfloat16 precision in round-down mode and returns nv bfloat16 with converted value.

## **Parameters**

а

- float. Is only being read.

#### Returns

nv\_bfloat16

a

converted to nv bfloat16.

## Description

Converts float number a to nv\_bfloat16 precision in round-down mode.

# \_\_host\_\_\_\_device\_\_ \_\_nv\_bfloat16 \_\_float2bfloat16\_rn (const float a)

Converts float number to nv\_bfloat16 precision in round-to-nearest-even mode and returns nv bfloat16 with converted value.

### **Parameters**

а

- float. Is only being read.

#### Returns

nv bfloat16

a

converted to nv\_bfloat16.

## Description

Converts float number a to nv\_bfloat16 precision in round-to-nearest-even mode.

## \_\_host\_\_\_\_device\_\_ \_\_nv\_bfloat16 \_\_float2bfloat16\_ru (const float a)

Converts float number to nv\_bfloat16 precision in round-up mode and returns nv\_bfloat16 with converted value.

## **Parameters**

а

- float. Is only being read.

## Returns

nv\_bfloat16

**a** 

converted to nv\_bfloat16.

## Description

Converts float number a to nv\_bfloat16 precision in round-up mode.

# \_\_host\_\_\_\_device\_\_ \_\_nv\_bfloat16 \_\_float2bfloat16\_rz (const float a)

Converts float number to nv\_bfloat16 precision in round-towards-zero mode and returns nv\_bfloat16 with converted value.

## **Parameters**

а

- float. Is only being read.

### Returns

nv bfloat16

a

converted to nv\_bfloat16.

## Description

Converts float number a to nv\_bfloat16 precision in round-towards-zero mode.

## \_\_host\_\_\_\_device\_\_ \_nv\_bfloat162 \_\_floats2bfloat162\_rn (const float a, const float b)

Converts both input floats to nv\_bfloat16 precision in round-to-nearest-even mode and returns nv bfloat162 with converted values.

### **Parameters**

а

- float. Is only being read.

b

- float. Is only being read.

#### Returns

nv\_bfloat162

► The

nv\_bfloat162 value with corresponding halves equal to the converted input floats.

## Description

Converts both input floats to nv\_bfloat16 precision in round-to-nearest-even mode and combines the results into one nv\_bfloat162 number. Low 16 bits of the return value correspond to the input a, high 16 bits correspond to the input b.

# \_\_device\_\_ \_\_nv\_bfloat162 \_\_halves2bfloat162 (const \_\_nv\_bfloat16 a, const \_\_nv\_bfloat16 b)

Combines two nv\_bfloat16 numbers into one nv\_bfloat162 number.

### **Parameters**

а

- nv\_bfloat16. Is only being read.

b

- nv\_bfloat16. Is only being read.

#### Returns

nv\_bfloat162

► The

nv\_bfloat162 with one nv\_bfloat16 equal to a and the other to b.

## Description

Combines two input nv\_bfloat16 number a and b into one nv\_bfloat162 number. Input a is stored in low 16 bits of the return value, input b is stored in high 16 bits of the return value.

# \_\_device\_\_ \_\_nv\_bfloat16 \_\_high2bfloat16 (const \_\_nv\_bfloat162 a)

Returns high 16 bits of nv bfloat162 input.

#### **Parameters**

a

- nv\_bfloat162. Is only being read.

## Returns

nv bfloat16

The

high 16 bits of the input.

## Description

Returns high 16 bits of nv\_bfloat162 input a.

Extracts high 16 bits from nv bfloat162 input.

## **Parameters**

a

- nv\_bfloat162. Is only being read.

## Returns

nv\_bfloat162

► The

nv bfloat162 with both halves equal to the high 16 bits of the input.

## Description

Extracts high 16 bits from nv\_bfloat162 input a and returns a new nv\_bfloat162 number which has both halves equal to the extracted bits.

Converts high 16 bits of nv bfloat162 to float and returns the result.

## **Parameters**

a

- nv\_bfloat162. Is only being read.

### Returns

float

► The

high 16 bits of a converted to float.

## Description

Converts high 16 bits of nv\_bfloat162 input a to 32-bit floating-point number and returns the result.

# \_\_device\_\_ \_\_nv\_bfloat162 \_\_highs2bfloat162 (const \_\_nv\_bfloat162 a, const \_\_nv\_bfloat162 b)

Extracts high 16 bits from each of the two  $nv\_bfloat162$  inputs and combines into one  $nv\_bfloat162$  number.

## **Parameters**

а

- nv\_bfloat162. Is only being read.

b

- nv\_bfloat162. Is only being read.

### Returns

nv\_bfloat162

► The

high 16 bits of a and of b.

## Description

Extracts high 16 bits from each of the two nv\_bfloat162 inputs and combines into one nv\_bfloat162 number. High 16 bits from input a is stored in low 16 bits of the return value, high 16 bits from input b is stored in high 16 bits of the return value.

## \_\_device\_\_ \_nv\_bfloat16 \_\_int2bfloat16\_rd (const int i)

Convert a signed integer to a nv bfloat16 in round-down mode.

#### **Parameters**

ı

- int. Is only being read.

## Returns

nv\_bfloat16

converted to nv bfloat16.

## Description

Convert the signed integer value i to a nv\_bfloat16 floating-point value in round-down mode.

## \_\_host\_\_\_\_device\_\_ \_nv\_bfloat16 \_\_int2bfloat16\_rn (const int i)

Convert a signed integer to a nv\_bfloat16 in round-to-nearest-even mode.

## **Parameters**

i

- int. Is only being read.

## Returns

nv\_bfloat16

converted to nv\_bfloat16.

## Description

Convert the signed integer value i to a nv\_bfloat16 floating-point value in round-to-nearest-even mode.

## \_\_device\_\_ \_\_nv\_bfloat16 \_\_int2bfloat16\_ru (const int i)

Convert a signed integer to a nv\_bfloat16 in round-up mode.

## **Parameters**

i

- int. Is only being read.

## Returns

nv\_bfloat16

converted to nv\_bfloat16.

## Description

Convert the signed integer value i to a nv\_bfloat16 floating-point value in round-up mode.

## \_\_device\_\_ \_nv\_bfloat16 \_\_int2bfloat16\_rz (const int i)

Convert a signed integer to a nv\_bfloat16 in round-towards-zero mode.

## **Parameters**

i

- int. Is only being read.

#### Returns

nv\_bfloat16

▶ į

converted to nv\_bfloat16.

## Description

Convert the signed integer value i to a nv\_bfloat16 floating-point value in round-towards-zero mode.

Generates a `ld.global.ca` load instruction.

### **Parameters**

#### ptr

- memory location

#### Returns

The value pointed by `ptr`

\_\_device\_\_ \_\_nv\_bfloat162 \_\_ldca (const \_\_nv\_bfloat162
\*ptr)

Generates a `ld.global.ca` load instruction.

### **Parameters**

#### ptr

- memory location

#### Returns

The value pointed by `ptr`

\_\_device\_\_ \_nv\_bfloat16 \_\_ldcg (const \_\_nv\_bfloat16 \*ptr)
Generates a `ld.global.cg` load instruction.

## **Parameters**

#### ptr

- memory location

#### Returns

The value pointed by `ptr`

\_\_device\_\_ \_nv\_bfloat162 \_\_ldcg (const \_\_nv\_bfloat162
\*ptr)

Generates a `ld.global.cg` load instruction.

### Parameters

#### ptr

- memory location

### Returns

The value pointed by `ptr`

\_\_device\_\_ \_nv\_bfloat16 \_\_ldcs (const \_\_nv\_bfloat16 \*ptr)
Generates a `ld.global.cs` load instruction.

## **Parameters**

#### ptr

- memory location

### Returns

The value pointed by `ptr`

\_\_device\_\_ \_nv\_bfloat162 \_\_ldcs (const \_\_nv\_bfloat162 \*ptr)

Generates a `ld.global.cs` load instruction.

## **Parameters**

#### ptr

- memory location

#### Returns

The value pointed by `ptr`

\_\_device\_\_ \_\_nv\_bfloat16 \_\_ldcv (const \_\_nv\_bfloat16 \*ptr)

Generates a `ld.global.cv` load instruction.

#### **Parameters**

#### ptr

- memory location

## Returns

The value pointed by `ptr`

\_\_device\_\_ \_nv\_bfloat162 \_\_ldcv (const \_\_nv\_bfloat162 \*ptr)

Generates a `ld.global.cv` load instruction.

#### **Parameters**

#### ptr

- memory location

## Returns

The value pointed by `ptr`

\_\_device\_\_ \_nv\_bfloat16 \_\_ldg (const \_\_nv\_bfloat16 \*ptr)

Generates a `ld.global.nc` load instruction.

#### **Parameters**

#### ptr

- memory location

### Returns

The value pointed by `ptr`

\_\_device\_\_ \_nv\_bfloat162 \_\_ldg (const \_\_nv\_bfloat162 \*ptr)

Generates a `ld.global.nc` load instruction.

#### **Parameters**

## ptr

- memory location

#### Returns

The value pointed by `ptr`

\_\_device\_\_ \_\_nv\_bfloat16 \_\_ldlu (const \_\_nv\_bfloat16 \*ptr)

Generates a `ld.global.lu` load instruction.

# **Parameters**

# ptr

- memory location

# Returns

The value pointed by `ptr`

\_\_device\_\_ \_\_nv\_bfloat162 \_\_ldlu (const \_\_nv\_bfloat162 \*ptr)

Generates a `ld.global.lu` load instruction.

#### **Parameters**

#### ptr

- memory location

# Returns

The value pointed by `ptr`

\_\_device\_\_ \_nv\_bfloat16 \_\_ll2bfloat16\_rd (const long long int i)

Convert a signed 64-bit integer to a nv\_bfloat16 in round-down mode.

# **Parameters**

i

- long long int. Is only being read.

# Returns

nv bfloat16

▶ i

converted to nv\_bfloat16.

# Description

Convert the signed 64-bit integer value i to a nv\_bfloat16 floating-point value in round-down mode.

# \_\_host\_\_\_\_device\_\_ \_nv\_bfloat16 \_\_ll2bfloat16\_rn (const long long int i)

Convert a signed 64-bit integer to a nv\_bfloat16 in round-to-nearest-even mode.

# **Parameters**

i

- long long int. Is only being read.

# Returns

nv bfloat16

**•** 

converted to nv bfloat16.

# Description

Convert the signed 64-bit integer value i to a nv\_bfloat16 floating-point value in round-to-nearest-even mode.

# \_\_device\_\_ \_\_nv\_bfloat16 \_\_ll2bfloat16\_ru (const long long int i)

Convert a signed 64-bit integer to a nv\_bfloat16 in round-up mode.

# **Parameters**

i

- long long int. Is only being read.

# Returns

nv bfloat16

▶ i

converted to nv\_bfloat16.

# Description

Convert the signed 64-bit integer value i to a nv\_bfloat16 floating-point value in round-up mode.

# \_\_device\_\_ \_nv\_bfloat16 \_\_ll2bfloat16\_rz (const long long int i)

Convert a signed 64-bit integer to a nv\_bfloat16 in round-towards-zero mode.

# **Parameters**

i

- long long int. Is only being read.

# Returns

nv\_bfloat16

**•** 

converted to nv\_bfloat16.

# Description

Convert the signed 64-bit integer value i to a nv\_bfloat16 floating-point value in round-towards-zero mode.

# \_\_device\_\_ \_\_nv\_bfloat16 \_\_low2bfloat16 (const \_\_nv\_bfloat162 a)

Returns low 16 bits of nv bfloat162 input.

# **Parameters**

a

- nv\_bfloat162. Is only being read.

# Returns

nv\_bfloat16

Returns

nv bfloat16 which contains low 16 bits of the input a.

# Description

Returns low 16 bits of nv bfloat162 input a.

# \_\_device\_\_ \_\_nv\_bfloat162 \_\_low2bfloat162 (const \_\_nv\_bfloat162 a)

Extracts low 16 bits from nv bfloat162 input.

# **Parameters**

а

- nv\_bfloat162. Is only being read.

# Returns

nv\_bfloat162

The

nv bfloat162 with both halves equal to the low 16 bits of the input.

# Description

Extracts low 16 bits from nv\_bfloat162 input a and returns a new nv\_bfloat162 number which has both halves equal to the extracted bits.

\_\_host\_\_\_device\_\_ float \_\_low2float (const \_\_nv\_bfloat162 a)

Converts low 16 bits of nv bfloat162 to float and returns the result.

# **Parameters**

a

- nv\_bfloat162. Is only being read.

# Returns

float

► The

low 16 bits of a converted to float.

# Description

Converts low 16 bits of nv\_bfloat162 input a to 32-bit floating-point number and returns the result.

# \_\_device\_\_ \_\_nv\_bfloat162 \_\_lowhigh2highlow (const nv bfloat162 a)

Swaps both halves of the nv\_bfloat162 input.

# **Parameters**

а

- nv bfloat162. Is only being read.

# Returns

nv\_bfloat162

**>** 2

with its halves being swapped.

# Description

Swaps both halves of the  $nv\_bfloat162$  input and returns a new  $nv\_bfloat162$  number with swapped halves.

Extracts low 16 bits from each of the two nv\_bfloat162 inputs and combines into one nv bfloat162 number.

# **Parameters**

а

- nv\_bfloat162. Is only being read.

b

- nv\_bfloat162. Is only being read.

#### Returns

nv\_bfloat162

▶ The

low 16 bits of a and of b.

# Description

Extracts low 16 bits from each of the two nv\_bfloat162 inputs and combines into one nv\_bfloat162 number. Low 16 bits from input a is stored in low 16 bits of the return value, low 16 bits from input b is stored in high 16 bits of the return value.

\_\_device\_\_ \_\_nv\_bfloat16 \_\_shfl\_down\_sync (const unsigned mask, const \_\_nv\_bfloat16 var, const unsigned int delta, const int width)

Exchange a variable between threads within a warp. Copy from a thread with higher ID relative to the caller.

# **Parameters**

#### mask

- unsigned int. Is only being read.

#### var

- nv\_bfloat16. Is only being read.

#### delta

- int. Is only being read.

#### width

- int. Is only being read.

#### Returns

Returns the 2-byte word referenced by var from the source thread ID as nv\_bfloat16. If the source thread ID is out of range or the source thread has exited, the calling thread's own var is returned.

# Description

Calculates a source thread ID by adding delta to the caller's thread ID. The value of var held by the resulting thread ID is returned: this has the effect of shifting var down the warp by delta threads. If width is less than warpSize then each subsection of the warp behaves as a separate entity with a starting logical thread ID of 0. As for <u>shfl up sync()</u>, the ID number of the source thread will not wrap around the value of width and so the upper delta threads will remain unchanged.

\_\_device\_\_ \_\_nv\_bfloat162 \_\_shfl\_down\_sync (const unsigned mask, const \_\_nv\_bfloat162 var, const unsigned int delta, const int width)

Exchange a variable between threads within a warp. Copy from a thread with higher ID relative to the caller.

# **Parameters**

#### mask

- unsigned int. Is only being read.

#### var

- nv\_bfloat162. Is only being read.

#### delta

- int. Is only being read.

#### width

- int. Is only being read.

#### Returns

Returns the 4-byte word referenced by var from the source thread ID as nv\_bfloat162. If the source thread ID is out of range or the source thread has exited, the calling thread's own var is returned.

# Description

Calculates a source thread ID by adding delta to the caller's thread ID. The value of var held by the resulting thread ID is returned: this has the effect of shifting var down the warp by delta threads. If width is less than warpSize then each subsection of the warp behaves as a separate entity with a starting logical thread ID of 0. As for <u>shfl up sync()</u>, the ID number of the source thread will not wrap around the value of width and so the upper delta threads will remain unchanged.

# \_\_device\_\_ \_\_nv\_bfloat16 \_\_shfl\_sync (const unsigned mask, const \_\_nv\_bfloat16 var, const int delta, const int width)

Exchange a variable between threads within a warp. Direct copy from indexed thread.

# **Parameters**

#### mask

- unsigned int. Is only being read.

#### var

- nv\_bfloat16. Is only being read.

#### delta

- int. Is only being read.

#### width

- int. Is only being read.

### Returns

Returns the 2-byte word referenced by var from the source thread ID as nv\_bfloat16. If the source thread ID is out of range or the source thread has exited, the calling thread's own var is returned.

# Description

Returns the value of var held by the thread whose ID is given by delta. If width is less than warpSize then each subsection of the warp behaves as a separate entity with a starting logical thread ID of 0. If delta is outside the range [0:width-1], the value returned corresponds to the value of var held by the delta modulo width (i.e. within the same subsection). width must have a value which is a power of 2; results are undefined if width is not a power of 2, or is a number greater than warpSize.

# \_\_device\_\_ \_\_nv\_bfloat162 \_\_shfl\_sync (const unsigned mask, const \_\_nv\_bfloat162 var, const int delta, const int width)

Exchange a variable between threads within a warp. Direct copy from indexed thread.

#### **Parameters**

#### mask

- unsigned int. Is only being read.

#### var

- nv\_bfloat162. Is only being read.

#### delta

- int. Is only being read.

#### width

- int. Is only being read.

#### Returns

Returns the 4-byte word referenced by var from the source thread ID as nv\_bfloat162. If the source thread ID is out of range or the source thread has exited, the calling thread's own var is returned.

# Description

Returns the value of var held by the thread whose ID is given by delta. If width is less than warpSize then each subsection of the warp behaves as a separate entity with a starting logical thread ID of 0. If delta is outside the range [0:width-1], the value returned corresponds to the value of var held by the delta modulo width (i.e. within the same subsection). width must have a value which is a power of 2; results are undefined if width is not a power of 2, or is a number greater than warpSize.

# \_\_device\_\_ \_\_nv\_bfloat16 \_\_shfl\_up\_sync (const unsigned mask, const \_\_nv\_bfloat16 var, const unsigned int delta, const int width)

Exchange a variable between threads within a warp. Copy from a thread with lower ID relative to the caller.

# **Parameters**

#### mask

- unsigned int. Is only being read.

#### var

- nv\_bfloat16. Is only being read.

#### delta

- int. Is only being read.

#### width

- int. Is only being read.

# Returns

Returns the 2-byte word referenced by var from the source thread ID as nv\_bfloat16. If the source thread ID is out of range or the source thread has exited, the calling thread's own var is returned.

# Description

Calculates a source thread ID by subtracting delta from the caller's lane ID. The value of var held by the resulting lane ID is returned: in effect, var is shifted up the warp by delta threads. If width is less than warpSize then each subsection of the warp behaves as a separate entity with a starting logical thread ID of 0. The source thread index will not wrap around the value of width, so effectively the lower delta threads will be unchanged. width must have a value which is a power of 2; results are undefined if width is not a power of 2, or is a number greater than warpSize.

# \_\_device\_\_ \_\_nv\_bfloat162 \_\_shfl\_up\_sync (const unsigned mask, const \_\_nv\_bfloat162 var, const unsigned int delta, const int width)

Exchange a variable between threads within a warp. Copy from a thread with lower ID relative to the caller

#### **Parameters**

#### mask

- unsigned int. Is only being read.

#### var

- nv bfloat162. Is only being read.

#### delta

- int. Is only being read.

#### width

- int. Is only being read.

#### Returns

Returns the 4-byte word referenced by var from the source thread ID as nv\_bfloat162. If the source thread ID is out of range or the source thread has exited, the calling thread's own var is returned.

# Description

Calculates a source thread ID by subtracting delta from the caller's lane ID. The value of var held by the resulting lane ID is returned: in effect, var is shifted up the warp by delta threads. If width is less than warpSize then each subsection of the warp behaves as a separate entity with a starting logical thread ID of 0. The source thread index will not wrap around the value of width, so effectively the lower delta threads will be unchanged. width must have a value which is a power of 2; results are undefined if width is not a power of 2, or is a number greater than warpSize.

# \_\_device\_\_ \_\_nv\_bfloat16 \_\_shfl\_xor\_sync (const unsigned mask, const \_\_nv\_bfloat16 var, const int delta, const int width)

Exchange a variable between threads within a warp. Copy from a thread based on bitwise XOR of own thread ID.

# **Parameters**

#### mask

- unsigned int. Is only being read.

#### var

- nv\_bfloat16. Is only being read.

#### delta

- int. Is only being read.

#### width

- int. Is only being read.

#### Returns

Returns the 2-byte word referenced by var from the source thread ID as nv\_bfloat16. If the source thread ID is out of range or the source thread has exited, the calling thread's own var is returned.

# Description

Calculates a source thread ID by performing a bitwise XOR of the caller's thread ID with mask: the value of var held by the resulting thread ID is returned. If width is less than warpSize then each group of width consecutive threads are able to access elements from earlier groups of threads, however if they attempt to access elements from later groups of threads their own value of var will be returned. This mode implements a butterfly addressing pattern such as is used in tree reduction and broadcast

# \_\_device\_\_ \_\_nv\_bfloat162 \_\_shfl\_xor\_sync (const unsigned mask, const \_\_nv\_bfloat162 var, const int delta, const int width)

Exchange a variable between threads within a warp. Copy from a thread based on bitwise XOR of own thread ID.

#### **Parameters**

#### mask

- unsigned int. Is only being read.

#### var

- nv bfloat162. Is only being read.

#### delta

- int. Is only being read.

#### width

- int. Is only being read.

# Returns

Returns the 4-byte word referenced by var from the source thread ID as nv\_bfloat162. If the source thread ID is out of range or the source thread has exited, the calling thread's own var is returned.

# Description

Calculates a source thread ID by performing a bitwise XOR of the caller's thread ID with mask: the value of var held by the resulting thread ID is returned. If width is less than warpSize then each group of width consecutive threads are able to access elements from earlier groups of threads, however if they attempt to access elements from later groups of threads their own value of var will be returned. This mode implements a butterfly addressing pattern such as is used in tree reduction and broadcast.

# \_\_device\_\_ \_nv\_bfloat16 \_\_short2bfloat16\_rd (const short int i)

Convert a signed short integer to a nv\_bfloat16 in round-down mode.

# **Parameters**

i

- short int. Is only being read.

# Returns

nv\_bfloat16

**•** 

converted to nv bfloat16.

# Description

Convert the signed short integer value i to a nv\_bfloat16 floating-point value in round-down mode.

# \_\_host\_\_\_device\_\_ \_nv\_bfloat16 \_\_short2bfloat16\_rn (const short int i)

Convert a signed short integer to a nv\_bfloat16 in round-to-nearest-even mode.

# **Parameters**

i

- short int. Is only being read.

# Returns

nv\_bfloat16

converted to nv\_bfloat16.

# Description

Convert the signed short integer value i to a nv\_bfloat16 floating-point value in round-to-nearest-even mode.

# \_\_device\_\_ \_nv\_bfloat16 \_\_short2bfloat16\_ru (const short int i)

Convert a signed short integer to a nv\_bfloat16 in round-up mode.

# **Parameters**

i

- short int. Is only being read.

# Returns

nv\_bfloat16

**•** 

converted to nv\_bfloat16.

# Description

Convert the signed short integer value i to a nv\_bfloat16 floating-point value in round-up mode.

# \_\_device\_\_ \_\_nv\_bfloat16 \_\_short2bfloat16\_rz (const short int i)

Convert a signed short integer to a nv\_bfloat16 in round-towards-zero mode.

# **Parameters**

i

- short int. Is only being read.

# Returns

nv\_bfloat16

converted to nv\_bfloat16.

# Description

Convert the signed short integer value i to a nv\_bfloat16 floating-point value in round-towards-zero mode.

# \_\_device\_\_ \_nv\_bfloat16 \_\_short\_as\_bfloat16 (const short int i)

Reinterprets bits in a signed short integer as a nv\_bfloat16.

# **Parameters**

i

- short int. Is only being read.

# Returns

nv\_bfloat16

The reinterpreted value.

# Description

Reinterprets the bits in the signed short integer i as a nv\_bfloat16 floating-point number.

Generates a `st.global.cg` store instruction.

# **Parameters**

#### ptr

- memory location

#### value

- the value to be stored

```
__device__ void __stcg (const __nv_bfloat162 *ptr, const
__nv_bfloat162 value)
```

Generates a `st.global.cg` store instruction.

# **Parameters**

#### ptr

- memory location

#### value

- the value to be stored

\_\_device\_\_ void \_\_stcs (const \_\_nv\_bfloat16 \*ptr, const \_\_nv\_bfloat16 value)

Generates a `st.global.cs` store instruction.

# **Parameters**

# ptr

- memory location

#### value

- the value to be stored

\_\_device\_\_ void \_\_stcs (const \_\_nv\_bfloat162 \*ptr, const \_\_nv\_bfloat162 value)

Generates a `st.global.cs` store instruction.

## **Parameters**

#### ptr

- memory location

#### value

- the value to be stored

\_\_device\_\_ void \_\_stwb (const \_\_nv\_bfloat16 \*ptr, const nv bfloat16 value)

Generates a `st.global.wb` store instruction.

# **Parameters**

#### ptr

- memory location

#### value

- the value to be stored

\_\_device\_\_ void \_\_stwb (const \_\_nv\_bfloat162 \*ptr, const \_\_nv\_bfloat162 value)

Generates a `st.global.wb` store instruction.

# **Parameters**

#### ptr

- memory location

#### value

- the value to be stored

```
__device__ void __stwt (const __nv_bfloat16 *ptr, const
__nv_bfloat16 value)
```

Generates a `st.global.wt` store instruction.

# **Parameters**

# ptr

- memory location

#### value

- the value to be stored

```
__device__ void __stwt (const __nv_bfloat162 *ptr, const __nv_bfloat162 value)
```

Generates a `st.global.wt` store instruction.

# **Parameters**

#### ptr

- memory location

#### value

- the value to be stored

# \_\_device\_\_ \_\_nv\_bfloat16 \_\_uint2bfloat16\_rd (const unsigned int i)

Convert an unsigned integer to a nv\_bfloat16 in round-down mode.

# **Parameters**

i

- unsigned int. Is only being read.

# Returns

nv\_bfloat16

converted to nv\_bfloat16.

# Description

Convert the unsigned integer value i to a nv\_bfloat16 floating-point value in round-down mode.

# \_\_host\_\_\_\_device\_\_ \_nv\_bfloat16 \_\_uint2bfloat16\_rn (const unsigned int i)

Convert an unsigned integer to a nv\_bfloat16 in round-to-nearest-even mode.

# **Parameters**

i

- unsigned int. Is only being read.

# Returns

nv\_bfloat16

▶ i

converted to nv\_bfloat16.

# Description

Convert the unsigned integer value i to a nv\_bfloat16 floating-point value in round-to-nearest-even mode.

# \_\_device\_\_ \_\_nv\_bfloat16 \_\_uint2bfloat16\_ru (const unsigned int i)

Convert an unsigned integer to a nv\_bfloat16 in round-up mode.

# **Parameters**

i

- unsigned int. Is only being read.

# Returns

nv\_bfloat16

converted to nv\_bfloat16.

# Description

Convert the unsigned integer value i to a nv\_bfloat16 floating-point value in round-up mode.

# \_\_device\_\_ \_\_nv\_bfloat16 \_\_uint2bfloat16\_rz (const unsigned int i)

Convert an unsigned integer to a nv\_bfloat16 in round-towards-zero mode.

# **Parameters**

i

- unsigned int. Is only being read.

# Returns

nv\_bfloat16

converted to nv\_bfloat16.

# Description

Convert the unsigned integer value i to a nv\_bfloat16 floating-point value in round-towards-zero mode.

# \_\_device\_\_ \_\_nv\_bfloat16 \_\_ull2bfloat16\_rd (const unsigned long long int i)

Convert an unsigned 64-bit integer to a nv\_bfloat16 in round-down mode.

# **Parameters**

i

- unsigned long long int. Is only being read.

# Returns

nv\_bfloat16

converted to nv\_bfloat16.

# Description

Convert the unsigned 64-bit integer value i to a nv\_bfloat16 floating-point value in round-down mode.

# \_\_host\_\_\_\_device\_\_ \_nv\_bfloat16 \_\_ull2bfloat16\_rn (const unsigned long long int i)

Convert an unsigned 64-bit integer to a nv\_bfloat16 in round-to-nearest-even mode.

# **Parameters**

i

- unsigned long long int. Is only being read.

# Returns

nv bfloat16

**•** 

converted to nv bfloat16.

# Description

Convert the unsigned 64-bit integer value i to a nv\_bfloat16 floating-point value in round-to-nearest-even mode.

# \_\_device\_\_ \_\_nv\_bfloat16 \_\_ull2bfloat16\_ru (const unsigned long long int i)

Convert an unsigned 64-bit integer to a nv\_bfloat16 in round-up mode.

# **Parameters**

i

- unsigned long long int. Is only being read.

# Returns

nv\_bfloat16

converted to nv\_bfloat16.

# Description

Convert the unsigned 64-bit integer value i to a nv\_bfloat16 floating-point value in round-up mode.

# \_\_device\_\_ \_\_nv\_bfloat16 \_\_ull2bfloat16\_rz (const unsigned long long int i)

Convert an unsigned 64-bit integer to a nv\_bfloat16 in round-towards-zero mode.

# **Parameters**

i

- unsigned long long int. Is only being read.

# Returns

nv\_bfloat16

▶ i

converted to nv\_bfloat16.

# Description

Convert the unsigned 64-bit integer value i to a nv\_bfloat16 floating-point value in round-towards-zero mode.

# \_\_device\_\_ \_\_nv\_bfloat16 \_\_ushort2bfloat16\_rd (const unsigned short int i)

Convert an unsigned short integer to a nv\_bfloat16 in round-down mode.

# **Parameters**

i

- unsigned short int. Is only being read.

# Returns

nv\_bfloat16

converted to nv\_bfloat16.

# Description

Convert the unsigned short integer value i to a nv\_bfloat16 floating-point value in round-down mode.

# \_\_host\_\_\_\_device\_\_ \_nv\_bfloat16 \_\_ushort2bfloat16\_rn (const unsigned short int i)

Convert an unsigned short integer to a nv\_bfloat16 in round-to-nearest-even mode.

# **Parameters**

i

- unsigned short int. Is only being read.

# Returns

nv bfloat16

converted to nv\_bfloat16.

# Description

Convert the unsigned short integer value i to a nv\_bfloat16 floating-point value in round-to-nearest-even mode.

# \_\_device\_\_ \_\_nv\_bfloat16 \_\_ushort2bfloat16\_ru (const unsigned short int i)

Convert an unsigned short integer to a nv\_bfloat16 in round-up mode.

# **Parameters**

i

- unsigned short int. Is only being read.

# Returns

nv\_bfloat16

converted to nv\_bfloat16.

# Description

Convert the unsigned short integer value i to a nv\_bfloat16 floating-point value in round-up mode.

# \_\_device\_\_ \_\_nv\_bfloat16 \_\_ushort2bfloat16\_rz (const unsigned short int i)

Convert an unsigned short integer to a nv\_bfloat16 in round-towards-zero mode.

# **Parameters**

i

- unsigned short int. Is only being read.

# Returns

nv\_bfloat16

▶ i

converted to nv\_bfloat16.

# Description

Convert the unsigned short integer value i to a nv\_bfloat16 floating-point value in round-towards-zero mode.

# \_\_device\_\_ \_\_nv\_bfloat16 \_\_ushort\_as\_bfloat16 (const unsigned short int i)

Reinterprets bits in an unsigned short integer as a nv bfloat16.

# **Parameters**

i

- unsigned short int. Is only being read.

# Returns

nv\_bfloat16

► The

reinterpreted value.

# Description

Reinterprets the bits in the unsigned short integer i as a nv\_bfloat16 floating-point number.

# 1.2.6. Bfloat16 Math Functions

Bfloat16 Precision Intrinsics

To use these functions, include the header file cuda\_bf16.h in your program.

# \_\_device\_\_ \_nv\_bfloat16 hceil (const \_\_nv\_bfloat16 h)

Calculate ceiling of the input argument.

# **Parameters**

h

- nv\_bfloat16. Is only being read.

# Returns

nv\_bfloat16

The smallest integer value not less than h.

# Description

Compute the smallest integer value not less than h.

# \_\_device\_\_ \_nv\_bfloat16 hcos (const \_\_nv\_bfloat16 a)

Calculates nv bfloat16 cosine in round-to-nearest-even mode.

#### **Parameters**

а

- nv\_bfloat16. Is only being read.

# Returns

nv bfloat16

► The cosine of a.

# Description

Calculates nv bfloat16 cosine of input a in round-to-nearest-even mode.

# \_\_device\_\_ \_\_nv\_bfloat16 hexp (const \_\_nv\_bfloat16 a)

Calculates nv bfloat16 natural exponential function in round-to-nearest mode.

# **Parameters**

а

- nv\_bfloat16. Is only being read.

# Returns

nv\_bfloat16

The natural exponential function on a.

# Description

Calculates nv\_bfloat16 natural exponential function of input a in round-to-nearest-even mode.

# 

 ${\tt Calculates\ nv\_bfloat16\ decimal\ exponential\ function\ in\ round-to-nearest\ mode}.$ 

# **Parameters**

а

- nv\_bfloat16. Is only being read.

#### Returns

nv bfloat16

The decimal exponential function on a.

# Description

Calculates nv\_bfloat16 decimal exponential function of input a in round-to-nearest-even mode.

# \_\_device\_\_ \_nv\_bfloat16 hexp2 (const \_\_nv\_bfloat16 a)

Calculates nv bfloat16 binary exponential function in round-to-nearest mode.

# **Parameters**

a

- nv\_bfloat16. Is only being read.

#### Returns

nv\_bfloat16

The

binary exponential function on a.

# Description

Calculates nv\_bfloat16 binary exponential function of input a in round-to-nearest-even mode.

# \_\_device\_\_ \_nv\_bfloat16 hfloor (const \_\_nv\_bfloat16 h)

Calculate the largest integer less than or equal to h.

# **Parameters**

h

- nv\_bfloat16. Is only being read.

# Returns

nv\_bfloat16

► The

largest integer value which is less than or equal to h.

# Description

Calculate the largest integer value which is less than or equal to h.

# \_\_device\_\_ \_nv\_bfloat16 hlog (const \_\_nv\_bfloat16 a)

Calculates nv bfloat16 natural logarithm in round-to-nearest-even mode.

# **Parameters**

а

- nv\_bfloat16. Is only being read.

#### Returns

nv\_bfloat16

The

natural logarithm of a.

# Description

 ${\tt Calculates\ nv\_bfloat16\ natural\ logarithm\ of\ input\ a\ in\ round-to-nearest-even\ mode}.$ 

# 

Calculates nv bfloat16 decimal logarithm in round-to-nearest-even mode.

# **Parameters**

a

- nv\_bfloat16. Is only being read.

# Returns

nv\_bfloat16

The decimal logarithm of a.

# Description

Calculates nv\_bfloat16 decimal logarithm of input a in round-to-nearest-even mode.

# 

Calculates nv bfloat16 binary logarithm in round-to-nearest-even mode.

#### **Parameters**

а

- nv\_bfloat16. Is only being read.

# Returns

nv bfloat16

The binary logarithm of a.

# Description

Calculates nv bfloat16 binary logarithm of input a in round-to-nearest-even mode.

# \_\_device\_\_ \_\_nv\_bfloat16 hrcp (const \_\_nv\_bfloat16 a)

Calculates nv bfloat16 reciprocal in round-to-nearest-even mode.

# **Parameters**

a

- nv\_bfloat16. Is only being read.

# Returns

nv bfloat16

The reciprocal of a.

# Description

Calculates nv bfloat16 reciprocal of input a in round-to-nearest-even mode.

# \_\_device\_\_ \_nv\_bfloat16 hrint (const \_\_nv\_bfloat16 h)

Round input to nearest integer value in nv\_bfloat16 floating-point number.

# **Parameters**

h

- nv\_bfloat16. Is only being read.

# Returns

nv\_bfloat16

► The nearest integer to h.

# Description

Round h to the nearest integer value in  $nv_bfloat16$  floating-point format, with bfloat16way cases rounded to the nearest even integer value.

# \_\_device\_\_ \_nv\_bfloat16 hrsqrt (const \_\_nv\_bfloat16 a)

Calculates nv bfloat16 reciprocal square root in round-to-nearest-even mode.

# **Parameters**

a

- nv\_bfloat16. Is only being read.

# Returns

nv\_bfloat16

The reciprocal square root of a.

# Description

Calculates nv\_bfloat16 reciprocal square root of input a in round-to-nearest mode.

# \_\_device\_\_ \_nv\_bfloat16 hsin (const \_\_nv\_bfloat16 a)

Calculates nv\_bfloat16 sine in round-to-nearest-even mode.

# **Parameters**

а

- nv\_bfloat16. Is only being read.

# Returns

nv\_bfloat16

► The

sine of a.

# Description

Calculates nv\_bfloat16 sine of input a in round-to-nearest-even mode.

# \_\_device\_\_ \_nv\_bfloat16 hsqrt (const \_\_nv\_bfloat16 a)

Calculates nv bfloat16 square root in round-to-nearest-even mode.

# **Parameters**

а

- nv\_bfloat16. Is only being read.

# Returns

nv\_bfloat16

The

square root of a.

# Description

Calculates nv bfloat16 square root of input a in round-to-nearest-even mode.

# \_\_device\_\_ \_\_nv\_bfloat16 htrunc (const \_\_nv\_bfloat16 h)

Truncate input argument to the integral part.

# **Parameters**

h

- nv\_bfloat16. Is only being read.

# Returns

nv\_bfloat16

The truncated integer value.

# Description

Round h to the nearest integer value that does not exceed h in magnitude.

# 1.2.7. Bfloat162 Math Functions

Bfloat16 Precision Intrinsics

To use these functions, include the header file cuda bf16.h in your program.

# \_\_device\_\_ \_nv\_bfloat162 h2ceil (const \_\_nv\_bfloat162 h)

Calculate nv bfloat162 vector ceiling of the input argument.

# **Parameters**

h

- nv\_bfloat162. Is only being read.

# Returns

nv\_bfloat162

► The

vector of smallest integers not less than h.

# Description

For each component of vector h compute the smallest integer value not less than h.

# \_\_device\_\_ \_ \_nv\_bfloat162 h2cos (const \_\_nv\_bfloat162 a)

Calculates nv\_bfloat162 vector cosine in round-to-nearest-even mode.

# **Parameters**

a

- nv\_bfloat162. Is only being read.

# Returns

nv\_bfloat162

▶ The

elementwise cosine on vector a.

# Description

Calculates nv\_bfloat162 cosine of input vector a in round-to-nearest-even mode.

# \_\_device\_\_ \_\_nv\_bfloat162 h2exp (const \_\_nv\_bfloat162 a)

Calculates nv bfloat162 vector exponential function in round-to-nearest mode.

#### **Parameters**

а

- nv bfloat162. Is only being read.

# Returns

nv bfloat162

► The

elementwise exponential function on vector a.

# Description

Calculates  $nv\_bfloat162$  exponential function of input vector a in round-to-nearest-even mode.

# \_\_device\_\_ \_nv\_bfloat162 h2exp10 (const \_\_nv\_bfloat162 a)

Calculates nv\_bfloat162 vector decimal exponential function in round-to-nearest-even mode.

# **Parameters**

a

- nv bfloat162. Is only being read.

# Returns

nv bfloat162

▶ The

elementwise decimal exponential function on vector a.

# Description

Calculates nv\_bfloat162 decimal exponential function of input vector a in round-to-nearest-even mode.

# 

Calculates nv\_bfloat162 vector binary exponential function in round-to-nearest-even mode.

# **Parameters**

a

- nv\_bfloat162. Is only being read.

# Returns

nv\_bfloat162

The

elementwise binary exponential function on vector a.

# Description

Calculates nv\_bfloat162 binary exponential function of input vector a in round-to-nearest-even mode.

# \_\_device\_\_ \_nv\_bfloat162 h2floor (const \_\_nv\_bfloat162 h)

Calculate the largest integer less than or equal to h.

# **Parameters**

h

- nv\_bfloat162. Is only being read.

# Returns

nv bfloat162

► The

vector of largest integers which is less than or equal to h.

# Description

For each component of vector h calculate the largest integer value which is less than or equal to h.

# \_\_device\_\_ \_nv\_bfloat162 h2log (const \_\_nv\_bfloat162 a)

Calculates nv bfloat162 vector natural logarithm in round-to-nearest-even mode.

# **Parameters**

a

- nv\_bfloat162. Is only being read.

#### Returns

nv\_bfloat162

The elementwise natural logarithm on vector a.

# Description

Calculates nv\_bfloat162 natural logarithm of input vector a in round-to-nearest-even mode.

# \_\_device\_\_ \_nv\_bfloat162 h2log10 (const \_\_nv\_bfloat162 a)

Calculates nv\_bfloat162 vector decimal logarithm in round-to-nearest-even mode.

# **Parameters**

a

- nv bfloat162. Is only being read.

# Returns

nv\_bfloat162

The

elementwise decimal logarithm on vector a.

# Description

Calculates nv\_bfloat162 decimal logarithm of input vector a in round-to-nearest-even mode.

# \_\_device\_\_ \_nv\_bfloat162 h2log2 (const \_\_nv\_bfloat162 a)

Calculates nv bfloat162 vector binary logarithm in round-to-nearest-even mode.

# **Parameters**

a

- nv\_bfloat162. Is only being read.

# Returns

nv\_bfloat162

 The elementwise binary logarithm on vector a.

# Description

Calculates nv\_bfloat162 binary logarithm of input vector a in round-to-nearest mode.

\_\_device\_\_ \_nv\_bfloat162 h2rcp (const \_\_nv\_bfloat162 a)

Calculates nv bfloat162 vector reciprocal in round-to-nearest-even mode.

#### **Parameters**

а

- nv\_bfloat162. Is only being read.

# Returns

nv bfloat162

► The elementwise reciprocal on vector a.

# Description

Calculates nv bfloat162 reciprocal of input vector a in round-to-nearest-even mode.

\_\_device\_\_ \_\_nv\_bfloat162 h2rint (const \_\_nv\_bfloat162 h)

Round input to nearest integer value in nv\_bfloat16 floating-point number.

# **Parameters**

h

- nv\_bfloat162. Is only being read.

# Returns

nv bfloat162

The vector of rounded integer values.

# Description

Round each component of nv\_bfloat162 vector h to the nearest integer value in nv\_bfloat16 floating-point format, with bfloat16way cases rounded to the nearest even integer value.

# \_\_device\_\_ \_nv\_bfloat162 h2rsqrt (const \_\_nv\_bfloat162 a)

Calculates nv bfloat162 vector reciprocal square root in round-to-nearest mode.

# **Parameters**

а

- nv\_bfloat162. Is only being read.

#### Returns

nv bfloat162

► The elementwise reciprocal square root on vector a.

# Description

Calculates nv\_bfloat162 reciprocal square root of input vector a in round-to-nearest-even mode.

# \_\_device\_\_ \_nv\_bfloat162 h2sin (const \_\_nv\_bfloat162 a)

Calculates nv bfloat162 vector sine in round-to-nearest-even mode.

# **Parameters**

a

- nv\_bfloat162. Is only being read.

#### Returns

nv\_bfloat162

The

elementwise sine on vector a.

# Description

Calculates nv\_bfloat162 sine of input vector a in round-to-nearest-even mode.

# \_\_device\_\_ \_nv\_bfloat162 h2sqrt (const \_\_nv\_bfloat162 a)

Calculates nv bfloat162 vector square root in round-to-nearest-even mode.

# **Parameters**

а

- nv\_bfloat162. Is only being read.

# Returns

nv\_bfloat162

► The elementwise square root on vector a.

# Description

Calculates nv bfloat162 square root of input vector a in round-to-nearest mode.

# \_\_device\_\_ \_nv\_bfloat162 h2trunc (const \_\_nv\_bfloat162 h)

Truncate nv bfloat162 vector input argument to the integral part.

# **Parameters**

h

- nv\_bfloat162. Is only being read.

# Returns

nv\_bfloat162

The

truncated h.

# Description

Round each component of vector h to the nearest integer value that does not exceed h in magnitude.

# 1.3. Mathematical Functions

CUDA mathematical functions are always available in device code.

Host implementations of the common mathematical functions are mapped in a platform-specific way to standard math library functions, provided by the host compiler and respective host libr where available. Some functions, not available with the host compilers, are implemented in crt/math\_functions.hpp header file. For example, see <a href="erfinv()">erfinv()</a>. Other, less common functions, like <a href="rhypot()">rhypot()</a>, <a href="cyclessel\_io()">cyl\_bessel\_io()</a> are only available in device code.

Note that many floating-point and integer functions names are overloaded for different argument types. For example, the <u>log()</u> function has the following prototypes:

```
f double log(double x);
    float log(float x);
    float logf(float x);
```

# 1.4. Single Precision Mathematical Functions

This section describes single precision mathematical functions. To use these functions you do not need to include any additional header files in your program.

# \_\_device\_\_ float acosf (float x)

Calculate the arc cosine of the input argument.

#### Returns

Result will be in radians, in the interval  $[0, \pi]$  for x inside [-1, +1].

- acosf(1) returns +0.
- acosf(x) returns NaN for x outside [-1, +1].

# Description

Calculate the principal value of the arc cosine of the input argument x.



# Note:

For accuracy information for this function see the CUDA C++ Programming Guide, Appendix E.1, Table 6.

# device float acoshf (float x)

Calculate the nonnegative arc hyperbolic cosine of the input argument.

## Returns

Result will be in the interval  $[0, +\infty]$ .

- acoshf(1) returns 0.
- ▶ acoshf(x) returns NaN for x in the interval  $[-\infty, 1]$ .

# Description

Calculate the nonnegative arc hyperbolic cosine of the input argument x.



#### Note:

For accuracy information for this function see the CUDA C++ Programming Guide, Appendix E.1, Table 6.

# \_device\_\_\_ float asinf (float x)

Calculate the arc sine of the input argument.

## Returns

Result will be in radians, in the interval  $[-\pi/2, +\pi/2]$  for x inside [-1, +1].

- asinf(0) returns +0.
- asinf(x) returns NaN for x outside [-1, +1].

# Description

Calculate the principal value of the arc sine of the input argument x.



#### Note:

For accuracy information for this function see the CUDA C++ Programming Guide, Appendix E.1, Table 6.

# device float asinhf (float x)

Calculate the arc hyperbolic sine of the input argument.

## Returns

asinhf(0) returns 1.

# Description

Calculate the arc hyperbolic sine of the input argument x.



#### Note:

For accuracy information for this function see the CUDA C++ Programming Guide, Appendix E.1, Table 6.

# \_\_device\_\_ float atan2f (float y, float x)

Calculate the arc tangent of the ratio of first and second input arguments.

## Returns

Result will be in radians, in the interval [-  $\pi$  , +  $\pi$  ].

 $\triangleright$  atan2f(0, 1) returns +0.

# Description

Calculate the principal value of the arc tangent of the ratio of first and second input arguments y / x. The quadrant of the result is determined by the signs of inputs y and x.



#### Note:

For accuracy information for this function see the CUDA C++ Programming Guide, Appendix E.1, Table 6.

# \_device\_\_ float atanf (float x)

Calculate the arc tangent of the input argument.

#### Returns

Result will be in radians, in the interval  $[-\pi/2, +\pi/2]$ .

ightharpoonup atanf(0) returns +0.

# Description

Calculate the principal value of the arc tangent of the input argument x.



#### Note:

For accuracy information for this function see the CUDA C++ Programming Guide, Appendix E.1, Table 6.

# \_\_device\_\_\_ float atanhf (float x)

Calculate the arc hyperbolic tangent of the input argument.

## Returns

- $\blacktriangleright$  atanhf(  $\pm 0$  ) returns  $\pm 0$ .
- ▶ atanhf(  $\pm 1$ ) returns  $\pm \infty$ .
- atanhf(x) returns NaN for x outside interval [-1, 1].

## Description

Calculate the arc hyperbolic tangent of the input argument x.



## Note:

For accuracy information for this function see the CUDA C++ Programming Guide, Appendix E.1, Table 6.

# device float cbrtf (float x)

Calculate the cube root of the input argument.

## Returns

Returns  $x^{1/3}$ .

- b cbrtf(  $\pm 0$ ) returns  $\pm 0$ .
- ▶ cbrtf(  $\pm \infty$ ) returns  $\pm \infty$ .

# Description

Calculate the cube root of x,  $x^{1/3}$ .

# •

## Note:

For accuracy information for this function see the CUDA C++ Programming Guide, Appendix E.1, Table 6.

# device float ceilf (float x)

Calculate ceiling of the input argument.

## Returns

Returns [x] expressed as a floating-point number.

- $\triangleright$  ceilf(  $\pm 0$ ) returns  $\pm 0$ .
- ▶ ceilf(  $\pm \infty$ ) returns  $\pm \infty$ .

# Description

Compute the smallest integer value not less than x.

# \_\_device\_\_ float copysignf (float x, float y)

Create value with given magnitude, copying sign of second value.

## Returns

Returns a value with the magnitude of x and the sign of y.

# Description

Create a floating-point value with the magnitude x and the sign of y.

# \_\_device\_\_ float cosf (float x)

Calculate the cosine of the input argument.

## Returns

- cosf(0) returns 1.
- $\triangleright$  cosf(  $\pm \infty$ ) returns NaN.

# Description

Calculate the cosine of the input argument x (measured in radians).



## Note:



- For accuracy information for this function see the CUDA C++ Programming Guide, Appendix E.1, Table 6.
- ► This function is affected by the --use\_fast\_math compiler flag. See the CUDA C++ Programming Guide, Appendix E.2, Table 8 for a complete list of functions affected.

# \_\_device\_\_\_ float coshf (float x)

Calculate the hyperbolic cosine of the input argument.

## Returns

- coshf(0) returns 1.
- ▶  $coshf(\pm \infty)$  returns  $+ \infty$ .

## Description

Calculate the hyperbolic cosine of the input argument x.



## Note:

For accuracy information for this function see the CUDA C++ Programming Guide, Appendix E.1, Table 6.

# \_device\_\_ float cospif (float x)

Calculate the cosine of the input argument  $imes \pi$ .

#### Returns

- $\triangleright$  cospif(  $\pm 0$  ) returns 1.
- ▶ cospif(  $\pm \infty$ ) returns NaN.

# Description

Calculate the cosine of  $x \times \pi$  (measured in radians), where x is the input argument.



## Note:

For accuracy information for this function see the CUDA C++ Programming Guide, Appendix E.1, Table 6.

# \_device\_\_\_ float cyl\_bessel\_i0f (float x)

Calculate the value of the regular modified cylindrical Bessel function of order 0 for the input argument.

#### Returns

Returns the value of the regular modified cylindrical Bessel function of order 0.

## Description

Calculate the value of the regular modified cylindrical Bessel function of order 0 for the input argument x,  $I_0(x)$ .



#### Note:

For accuracy information for this function see the CUDA C++ Programming Guide, Appendix E.1, Table 6.

# \_device\_\_ float cyl\_bessel\_i1f (float x)

Calculate the value of the regular modified cylindrical Bessel function of order 1 for the input argument.

#### Returns

Returns the value of the regular modified cylindrical Bessel function of order 1.

# Description

Calculate the value of the regular modified cylindrical Bessel function of order 1 for the input argument x,  $I_1(x)$ .



## Note:

For accuracy information for this function see the CUDA C++ Programming Guide, Appendix E.1, Table 6.

# \_device\_\_ float erfcf (float x)

Calculate the complementary error function of the input argument.

#### Returns

- ▶ erfcf( $-\infty$ ) returns 2.
- ▶  $\operatorname{erfcf}(+\infty)\operatorname{returns} +0$ .

# Description

Calculate the complementary error function of the input argument x, 1 - erf(x).



#### Note:

For accuracy information for this function see the CUDA C++ Programming Guide, Appendix E.1, Table 6.

# \_\_device\_\_\_ float erfcinvf (float y)

Calculate the inverse complementary error function of the input argument.

## Returns

- ▶ erfcinvf(0) returns  $+\infty$ .
- erfcinvf(2) returns -∞.

## Description

Calculate the inverse complementary error function of the input argument y, for y in the interval [0, 2]. The inverse complementary error function find the value x that satisfies the equation  $y = \operatorname{erfc}(x)$ , for  $0 \le y \le 2$ , and  $-\infty \le x \le \infty$ .



#### Note:

For accuracy information for this function see the CUDA C++ Programming Guide, Appendix E.1, Table 6.

# \_device\_\_ float erfcxf (float x)

Calculate the scaled complementary error function of the input argument.

#### Returns

- ▶  $\operatorname{erfcxf}(-\infty)\operatorname{returns} + \infty$
- erfcxf(  $+\infty$ ) returns +0
- $\operatorname{erfcxf}(x)$  returns  $+\infty$  if the correctly calculated value is outside the single floating-point range.

# Description

Calculate the scaled complementary error function of the input argument x,  $e^{x^2} \cdot \operatorname{erfc}(x)$ .

# ,

## Note:

For accuracy information for this function see the CUDA C++ Programming Guide, Appendix E.1, Table 6.

# device float erff (float x)

Calculate the error function of the input argument.

## Returns

- erff(  $\pm 0$ ) returns  $\pm 0$ .
- erff(  $\pm \infty$ ) returns  $\pm 1$ .

# Description

Calculate the value of the error function for the input argument x,  $\frac{2}{\sqrt{\pi}} \int_{0}^{x} e^{-t^2} dt$ .



#### Note:

For accuracy information for this function see the CUDA C++ Programming Guide, Appendix E.1, Table 6.

# \_device\_\_ float erfinvf (float y)

Calculate the inverse error function of the input argument.

#### Returns

- ▶ erfinvf(1) returns  $+\infty$ .
- ▶ erfinvf(-1) returns  $-\infty$ .

# Description

Calculate the inverse error function of the input argument y, for y in the interval [-1, 1]. The inverse error function finds the value x that satisfies the equation y = erf(x), for  $-1 \le y \le 1$ , and  $-\infty \le x \le \infty$ .

# **,**

#### Note:

For accuracy information for this function see the CUDA C++ Programming Guide, Appendix E.1, Table 6.

# \_\_\_device\_\_\_ float exp10f (float x)

Calculate the base 10 exponential of the input argument.

## Returns

Returns  $10^{x}$ .

# Description

Calculate the base 10 exponential of the input argument x.



#### Note:

- For accuracy information for this function see the CUDA C++ Programming Guide, Appendix E.1, Table 6.
- ► This function is affected by the --use\_fast\_math compiler flag. See the CUDA C++ Programming Guide, Appendix E.2, Table 8 for a complete list of functions affected.

# \_device\_\_\_ float exp2f (float x)

Calculate the base 2 exponential of the input argument.

## Returns

Returns  $2^x$ .

# Description

Calculate the base 2 exponential of the input argument x.



## Note:

For accuracy information for this function see the CUDA C++ Programming Guide, Appendix E.1, Table 6.

# \_device\_\_ float expf (float x)

Calculate the base e exponential of the input argument.

#### Returns

Returns  $e^{x}$ .

# Description

Calculate the base e exponential of the input argument x,  $e^x$ .



#### Note:

- For accuracy information for this function see the CUDA C++ Programming Guide, Appendix E.1, Table 6.
- This function is affected by the --use\_fast\_math compiler flag. See the CUDA C++ Programming Guide, Appendix E.2, Table 8 for a complete list of functions affected.

# \_\_device\_\_ float expm1f (float x)

Calculate the base e exponential of the input argument, minus 1.

## Returns

Returns  $e^{x} - 1$ .

# Description

Calculate the base e exponential of the input argument x, minus 1.



## Note:

For accuracy information for this function see the CUDA C++ Programming Guide, Appendix E.1, Table 6.

# \_device\_\_ float fabsf (float x)

Calculate the absolute value of its argument.

#### Returns

Returns the absolute value of its argument.

- ▶ fabs(  $\pm \infty$ ) returns  $+ \infty$ .
- fabs(  $\pm 0$ ) returns 0.

## Description

Calculate the absolute value of the input argument x.



## Note:



For accuracy information for this function see the CUDA C++ Programming Guide, Appendix E.1, Table 6.

# \_device\_\_ float fdimf (float x, float y)

Compute the positive difference between x and y.

## Returns

Returns the positive difference between x and y.

- fdimf(x, y) returns x y if x > y.
- fdimf(x, y) returns +0 if  $x \le y$ .

## Description

Compute the positive difference between x and y. The positive difference is x - y when x > y and +0 otherwise.



#### Note:

For accuracy information for this function see the CUDA C++ Programming Guide, Appendix E.1, Table 6.

# \_\_device\_\_\_ float fdividef (float x, float y)

Divide two floating-point values.

## Returns

Returns x / y.

# Description

Compute x divided by y. If  $-use\_fast\_math$  is specified, use  $\underline{fdividef()}$  for higher performance, otherwise use normal division.



#### Note:

- For accuracy information for this function see the CUDA C++ Programming Guide, Appendix E.1, Table 6.
- ► This function is affected by the --use\_fast\_math compiler flag. See the CUDA C++ Programming Guide, Appendix E.2, Table 8 for a complete list of functions affected.

# \_device\_\_ float floorf (float x)

Calculate the largest integer less than or equal to x.

## Returns

Returns [x] expressed as a floating-point number.

- ▶ floorf(  $\pm \infty$ ) returns  $\pm \infty$ .
- floorf(  $\pm 0$  ) returns  $\pm 0$ .

# Description

Calculate the largest integer value which is less than or equal to x.



#### Note:

For accuracy information for this function see the CUDA C++ Programming Guide, Appendix E.1, Table 6.

# device float fmaf (float x, float y, float z)

Compute  $x \times y + z$  as a single operation.

## Returns

Returns the rounded value of  $x \times y + z$  as a single operation.

- fmaf(  $\pm \infty$ ,  $\pm 0$ , z) returns NaN.
- ► fmaf(  $\pm 0$ ,  $\pm \infty$ , z) returns NaN.
- ▶ fmaf(x, y,  $-\infty$ ) returns NaN if  $x \times y$  is an exact  $+\infty$ .
- ▶ fmaf(x, y, +∞) returns NaN if  $x \times y$  is an exact -∞.

# Description

Compute the value of  $x \times y + z$  as a single ternary operation. After computing the value to infinite precision, the value is rounded once.



#### Note:

For accuracy information for this function see the CUDA C++ Programming Guide, Appendix E.1, Table 6.

# \_device\_\_\_ float fmaxf (float x, float y)

Determine the maximum numeric value of the arguments.

## Returns

Returns the maximum numeric values of the arguments x and y.

- If both arguments are NaN, returns NaN.
- If one argument is NaN, returns the numeric argument.

# Description

Determines the maximum numeric value of the arguments x and y. Treats NaN arguments as missing data. If one argument is a NaN and the other is legitimate numeric value, the numeric value is chosen.



#### Note:

For accuracy information for this function see the CUDA C++ Programming Guide, Appendix E.1, Table 6.

# \_\_device\_\_ float fminf (float x, float y)

Determine the minimum numeric value of the arguments.

## Returns

Returns the minimum numeric value of the arguments x and y.

- If both arguments are NaN, returns NaN.
- If one argument is NaN, returns the numeric argument.

# Description

Determines the minimum numeric value of the arguments x and y. Treats NaN arguments as missing data. If one argument is a NaN and the other is legitimate numeric value, the numeric value is chosen.



## Note:

For accuracy information for this function see the CUDA C++ Programming Guide, Appendix E.1, Table 6.

# \_device\_\_ float fmodf (float x, float y)

Calculate the floating-point remainder of x / y.

## Returns

- ► Returns the floating-point remainder of x / y.
- fmodf(  $\pm 0$ , y) returns  $\pm 0$  if y is not zero.
- fmodf(x,  $\pm \infty$ ) returns x if x is finite.
- fmodf(x, y) returns NaN if x is  $\pm \infty$  or y is zero.
- If either argument is NaN, NaN is returned.

## Description

Calculate the floating-point remainder of x / y. The floating-point remainder of the division operation x / y calculated by this function is exactly the value x - n\*y, where n is x / y with its fractional part truncated. The computed value will have the same sign as x, and its magnitude will be less than the magnitude of y.



#### Note:

For accuracy information for this function see the CUDA C++ Programming Guide, Appendix E.1, Table 6.

# \_\_device\_\_\_ float frexpf (float x, int \*nptr)

Extract mantissa and exponent of a floating-point value.

## Returns

Returns the fractional component m.

- frexp(0, nptr) returns 0 for the fractional component and zero for the integer component.
- frexp( $\pm 0$ , nptr) returns  $\pm 0$  and stores zero in the location pointed to by nptr.
- ▶ frexp(  $\pm \infty$ , nptr) returns  $\pm \infty$  and stores an unspecified value in the location to which nptr points.
- frexp(NaN, y) returns a NaN and stores an unspecified value in the location to which nptr points.

# Description

Decomposes the floating-point value x into a component m for the normalized fraction element and another term n for the exponent. The absolute value of m will be greater than or equal to

0.5 and less than 1.0 or it will be equal to 0;  $x = m \cdot 2^n$ . The integer exponent n will be stored in the location to which nptr points.



#### Note:

For accuracy information for this function see the CUDA C++ Programming Guide, Appendix E.1, Table 6.

# \_device\_\_ float hypotf (float x, float y)

Calculate the square root of the sum of squares of two arguments.

#### Returns

Returns the length of the hypotenuse  $\sqrt{x^2 + y^2}$ . If the correct value would overflow, returns  $+ \infty$ . If the correct value would underflow, returns 0.

## Description

Calculates the length of the hypotenuse of a right triangle whose two sides have lengths  $\mathbf{x}$  and  $\mathbf{y}$  without undue overflow or underflow.



#### Note:

For accuracy information for this function see the CUDA C++ Programming Guide, Appendix E.1. Table 6.

# \_\_device\_\_ int ilogbf (float x)

Compute the unbiased integer exponent of the argument.

#### Returns

- If successful, returns the unbiased exponent of the argument.
- ▶ ilogbf(0) returns INT MIN.
- ▶ ilogbf(NaN) returns INT MIN.
- ▶ ilogbf(x) returns INT MAX if x is  $\infty$  or the correct value is greater than INT MAX.
- ightharpoonup ilogbf(x) returns INT MIN if the correct value is less than INT MIN.
- ▶ Note: above behavior does not take into account FP ILOGBO nor FP ILOGBNAN.

# Description

Calculates the unbiased integer exponent of the input argument x.

# •

#### Note:

For accuracy information for this function see the CUDA C++ Programming Guide, Appendix E.1, Table 6.

# \_\_device\_\_ \_\_RETURN\_TYPE isfinite (float a)

Determine whether argument is finite.

## Returns

- ▶ With Visual Studio 2013 host compiler: \_\_RETURN\_TYPE is 'bool'. Returns true if and only if a is a finite value.
- With other host compilers: \_\_RETURN\_TYPE is 'int'. Returns a nonzero value if and only if a is a finite value.

# Description

Determine whether the floating-point value a is a finite value (zero, subnormal, or normal and not infinity or NaN).

# \_\_device\_\_ \_RETURN\_TYPE isinf (float a)

Determine whether argument is infinite.

#### Returns

- ▶ With Visual Studio 2013 host compiler: \_\_RETURN\_TYPE is 'bool'. Returns true if and only if a is a infinite value.
- With other host compilers: \_\_RETURN\_TYPE is 'int'. Returns a nonzero value if and only if a is a infinite value.

# Description

Determine whether the floating-point value a is an infinite value (positive or negative).

# 

Determine whether argument is a NaN.

## Returns

▶ With Visual Studio 2013 host compiler: \_\_RETURN\_TYPE is 'bool'. Returns true if and only if a is a NaN value.

▶ With other host compilers: \_\_RETURN\_TYPE is 'int'. Returns a nonzero value if and only if a is a NaN value.

# Description

Determine whether the floating-point value a is a NaN.

# \_\_device\_\_ float j0f (float x)

Calculate the value of the Bessel function of the first kind of order 0 for the input argument.

## Returns

Returns the value of the Bessel function of the first kind of order 0.

- ▶  $j0f(\pm \infty)$  returns +0.
- ▶ j0f(NaN) returns NaN.

# Description

Calculate the value of the Bessel function of the first kind of order 0 for the input argument x,  $J_0(x)$ .



#### Note:

For accuracy information for this function see the CUDA C++ Programming Guide, Appendix E.1, Table 6.

# \_\_device\_\_ float j1f (float x)

Calculate the value of the Bessel function of the first kind of order 1 for the input argument.

## Returns

Returns the value of the Bessel function of the first kind of order 1.

- ightharpoonup j1f(  $\pm 0$  ) returns  $\pm 0$ .
- ▶  $j1f(\pm \infty)$  returns  $\pm 0$ .
- ▶ j1f(NaN) returns NaN.

# Description

Calculate the value of the Bessel function of the first kind of order 1 for the input argument x,  $J_1(x)$ .

# •

## Note:

For accuracy information for this function see the CUDA C++ Programming Guide, Appendix E.1, Table 6.

# \_device\_\_ float inf (int n, float x)

Calculate the value of the Bessel function of the first kind of order n for the input argument.

## Returns

Returns the value of the Bessel function of the first kind of order n.

- jnf(n, NaN) returns NaN.
- inf(n, x) returns NaN for n < 0.
- ▶  $\inf(n, +\infty)$  returns +0.

## Description

Calculate the value of the Bessel function of the first kind of order n for the input argument x,  $J_n(x)$ .



#### Note:

For accuracy information for this function see the CUDA C++ Programming Guide, Appendix E.1, Table 6.

# \_\_device\_\_ float ldexpf (float x, int exp)

Calculate the value of  $x \cdot 2^{exp}$ .

## Returns

▶ ldexpf(x) returns  $\pm \infty$  if the correctly calculated value is outside the single floating-point range.

# Description

Calculate the value of  $x \cdot 2^{exp}$  of the input arguments x and exp.



## Note:

For accuracy information for this function see the CUDA C++ Programming Guide, Appendix E.1, Table 6.

# \_device\_\_\_ float lgammaf (float x)

Calculate the natural logarithm of the absolute value of the gamma function of the input argument.

#### Returns

- ▶ lgammaf(1) returns +0.
- ▶ lgammaf(2) returns +0.
- ▶ lgammaf(x) returns  $\pm \infty$  if the correctly calculated value is outside the single floating-point range.
- ▶ lgammaf(x) returns  $+\infty$  if  $x \le 0$  and x is an integer.
- lgammaf( $-\infty$ ) returns  $-\infty$ .
- lgammaf(  $+ \infty$ ) returns  $+ \infty$ .

## Description

Calculate the natural logarithm of the absolute value of the gamma function of the input argument x, namely the value of  $\log_e \int_0^\infty e^{-t} t^{x-1} dt$ .



#### Note:

For accuracy information for this function see the CUDA C++ Programming Guide, Appendix E.1, Table 6.

# \_\_device\_\_ long long int llrintf (float x)

Round input to nearest integer value.

#### Returns

Returns rounded integer value.

# Description

Round x to the nearest integer value, with halfway cases rounded to the nearest even integer value. If the result is outside the range of the return type, the result is undefined.

# \_device\_\_ long long int llroundf (float x)

Round to nearest integer value.

#### Returns

Returns rounded integer value.

# Description

Round x to the nearest integer value, with halfway cases rounded away from zero. If the result is outside the range of the return type, the result is undefined.



## Note:

This function may be slower than alternate rounding methods. See <u>llrintf()</u>.

# \_\_device\_\_ float log10f (float x)

Calculate the base 10 logarithm of the input argument.

## Returns

- log10f(  $\pm 0$ ) returns  $-\infty$ .
- $\blacktriangleright$  log10f(1) returns +0.
- log10f(x) returns NaN for x < 0.
- ▶  $log10f(+\infty)$  returns  $+\infty$ .

# Description

Calculate the base 10 logarithm of the input argument x.



#### Note:

For accuracy information for this function see the CUDA C++ Programming Guide, Appendix E.1, Table 6.

# \_device\_\_ float log1pf (float x)

Calculate the value of  $log_{\rho}(1+x)$ .

## Returns

- log1pf(  $\pm 0$  ) returns  $\pm 0$ .
- log1pf(-1) returns  $-\infty$ .
- ▶ log1pf(x) returns NaN for x < -1.
- ▶  $log1pf(+\infty)$  returns  $+\infty$ .

# Description

Calculate the value of  $log_o(1+x)$  of the input argument x.

# ,

## Note:

For accuracy information for this function see the CUDA C++ Programming Guide, Appendix E.1, Table 6.

# \_device\_\_ float log2f (float x)

Calculate the base 2 logarithm of the input argument.

## Returns

- log2f(  $\pm 0$ ) returns  $-\infty$ .
- $\triangleright$  log2f(1) returns +0.
- log2f(x) returns NaN for x < 0.
- ▶  $\log 2f(+\infty)$  returns  $+\infty$ .

## Description

Calculate the base 2 logarithm of the input argument x.



#### Note:

For accuracy information for this function see the CUDA C++ Programming Guide, Appendix E.1, Table 6.

# \_\_device\_\_\_ float logbf (float x)

Calculate the floating-point representation of the exponent of the input argument.

#### Returns

- ▶ logbf  $\pm 0$  returns  $-\infty$
- ▶  $logbf + \infty returns + \infty$

# Description

Calculate the floating-point representation of the exponent of the input argument x.



#### Note:

For accuracy information for this function see the CUDA C++ Programming Guide, Appendix E.1, Table 6.

# \_device\_\_ float logf (float x)

Calculate the natural logarithm of the input argument.

#### Returns

- ▶  $logf(\pm 0)$  returns  $-\infty$ .
- $\triangleright$  logf(1) returns +0.
- logf(x) returns NaN for x < 0.
- ▶  $logf(+\infty)$  returns  $+\infty$ .

## Description

Calculate the natural logarithm of the input argument x.



## Note:

For accuracy information for this function see the CUDA C++ Programming Guide, Appendix E.1, Table 6.

# \_\_device\_\_ long int lrintf (float x)

Round input to nearest integer value.

## Returns

Returns rounded integer value.

# Description

Round x to the nearest integer value, with halfway cases rounded to the nearest even integer value. If the result is outside the range of the return type, the result is undefined.

# \_\_device\_\_ long int lroundf (float x)

Round to nearest integer value.

#### Returns

Returns rounded integer value.

## Description

Round x to the nearest integer value, with halfway cases rounded away from zero. If the result is outside the range of the return type, the result is undefined.



#### Note:

This function may be slower than alternate rounding methods. See <a href="mailto:lrintfl">lrintfl</a>.

# device float max (const float a, const float b)

Calculate the maximum value of the input float arguments.

# Description

Calculate the maximum value of the arguments a and b. Behavior is equivalent to <a href="maxfl">fmaxfl</a> function.

Note, this is different from std:: specification

# \_\_device\_\_ float min (const float a, const float b)

Calculate the minimum value of the input float arguments.

# Description

Calculate the minimum value of the arguments a and b. Behavior is equivalent to <a href="minimum">fminf()</a> function.

Note, this is different from std:: specification

# \_\_device\_\_ float modff (float x, float \*iptr)

Break down the input argument into fractional and integral parts.

#### Returns

- $\blacktriangleright$  modff(  $\pm x$ , iptr) returns a result with the same sign as x.
- ▶ modff(  $\pm \infty$ , iptr) returns  $\pm 0$  and stores  $\pm \infty$  in the object pointed to by iptr.
- modff(NaN, iptr) stores a NaN in the object pointed to by iptr and returns a NaN.

# Description

Break down the argument x into fractional and integral parts. The integral part is stored in the argument iptr. Fractional and integral parts are given the same sign as the argument x.



## Note:

For accuracy information for this function see the CUDA C++ Programming Guide, Appendix E.1, Table 6.

# \_\_device\_\_\_ float nanf (const char \*tagp)

Returns "Not a Number" value.

## Returns

nanf(tagp) returns NaN.

## Description

Return a representation of a quiet NaN. Argument tagp selects one of the possible representations.



#### Note:

For accuracy information for this function see the CUDA C++ Programming Guide, Appendix E.1, Table 6.

# \_\_device\_\_ float nearbyintf (float x)

Round the input argument to the nearest integer.

## Returns

- nearbyintf(  $\pm 0$ ) returns  $\pm 0$ .
- ▶ nearbyintf(  $\pm \infty$ ) returns  $\pm \infty$ .

# Description

Round argument x to an integer value in single precision floating-point format.



#### Note:

For accuracy information for this function see the CUDA C++ Programming Guide, Appendix E.1, Table 6.

# device float nextafterf (float x, float y)

Return next representable single-precision floating-point value after argument  $\mathbf{x}$  in the direction of  $\mathbf{y}$ .

## Returns

- nextafterf(x, y) = y if x equals y
- $\blacktriangleright$  nextafterf(x, y) = NaN if either x or y are NaN

# Description

Calculate the next representable single-precision floating-point value following  $\mathbf{x}$  in the direction of  $\mathbf{y}$ . For example, if  $\mathbf{y}$  is greater than  $\mathbf{x}$ ,  $\underline{\mathsf{nextafterf()}}$  returns the smallest representable number greater than  $\mathbf{x}$ 



#### Note:

For accuracy information for this function see the CUDA C++ Programming Guide, Appendix E.1. Table 6.

# device float norm3df (float a, float b, float c)

Calculate the square root of the sum of squares of three coordinates of the argument.

## Returns

Returns the length of the 3D  $\sqrt{p.x^2+p.y^2+p.z^2}$ . If the correct value would overflow, returns  $+\infty$ . If the correct value would underflow, returns 0.

## Description

Calculates the length of three dimensional vector  $\mathbf{p}$  in Euclidean space without undue overflow or underflow.



## Note:

For accuracy information for this function see the CUDA C++ Programming Guide, Appendix E.1, Table 6.

# \_\_device\_\_ float norm4df (float a, float b, float c, float d)

Calculate the square root of the sum of squares of four coordinates of the argument.

## Returns

Returns the length of the 4D vector  $\sqrt{\mathbf{p}.\mathbf{x}^2 + \mathbf{p}.\mathbf{y}^2 + \mathbf{p}.\mathbf{z}^2 + \mathbf{p}.\mathbf{t}^2}$ . If the correct value would overflow, returns  $+\infty$ . If the correct value would underflow, returns 0.

# Description

Calculates the length of four dimensional vector p in Euclidean space without undue overflow or underflow.

# •

## Note:

For accuracy information for this function see the CUDA C++ Programming Guide, Appendix E.1, Table 6.

# \_device\_\_ float normcdff (float y)

Calculate the standard normal cumulative distribution function.

#### Returns

- normcdff( + ∞) returns 1
- ▶ normcdff( $-\infty$ ) returns +0

# Description

Calculate the cumulative distribution function of the standard normal distribution for input argument y,  $\Phi(y)$ .



#### Note:

For accuracy information for this function see the CUDA C++ Programming Guide, Appendix E.1, Table 6.

# \_\_device\_\_\_ float normcdfinvf (float y)

Calculate the inverse of the standard normal cumulative distribution function.

## Returns

- ▶ normcdfinvf(0) returns  $-\infty$ .
- ▶ normcdfinvf(1) returns  $+\infty$ .
- normcdfinvf(x) returns NaN if x is not in the interval [0,1].

# Description

Calculate the inverse of the standard normal cumulative distribution function for input argument y,  $\Phi^{-1}(y)$ . The function is defined for input values in the interval (0, 1).



## Note:

For accuracy information for this function see the CUDA C++ Programming Guide, Appendix E.1, Table 6.

# \_device\_\_ float normf (int dim, const float \*a)

Calculate the square root of the sum of squares of any number of coordinates.

## Returns

Returns the length of the vector  $\sqrt{\mathbf{p}.1^2 + \mathbf{p}.2^2 + ... + \mathbf{p}.\dim^2}$ . If the correct value would overflow, returns  $+ \infty$ . If the correct value would underflow, returns 0.

# Description

Calculates the length of a vector p, dimension of which is passed as an argument without undue overflow or underflow.



#### Note:

For accuracy information for this function see the CUDA C++ Programming Guide, Appendix E.1, Table 6.

# \_device\_\_ float powf (float x, float y)

Calculate the value of first argument to the power of second argument.

#### Returns

- ▶ powf(  $\pm 0$ , y) returns  $\pm \infty$  for y an integer less than 0.
- **powf**( $\pm 0$ , y) returns  $\pm 0$  for y an odd integer greater than 0.
- **powf(**  $\pm 0$ , y) returns +0 for y > 0 and not and odd integer.
- ▶ powf(-1,  $\pm \infty$ ) returns 1.
- ightharpoonup powf(+1, y) returns 1 for any y, even a NaN.
- $\triangleright$  powf(x,  $\pm 0$ ) returns 1 for any x, even a NaN.
- ightharpoonup powf(x, y) returns a NaN for finite x < 0 and finite non-integer y.
- ▶ powf(x,  $-\infty$ ) returns  $+\infty$  for |x| < 1.
- ▶ powf(x,  $-\infty$ ) returns +0 for |x| > 1.
- ▶ powf(x,  $+\infty$ ) returns +0 for |x| < 1.
- ▶ powf(x, +∞) returns +∞ for |x| > 1.
- ▶ powf( $-\infty$ , y) returns -0 for y an odd integer less than 0.
- ▶ powf( $-\infty$ , y) returns +0 for y < 0 and not an odd integer.
- ▶ powf( $-\infty$ , y) returns  $-\infty$  for y an odd integer greater than 0.
- ▶ powf( $-\infty$ , y) returns  $+\infty$  for y > 0 and not an odd integer.
- ▶ powf(  $+\infty$ , y) returns +0 for y < 0.

▶ powf(  $+\infty$ , y) returns  $+\infty$  for y > 0.

# Description

Calculate the value of x to the power of y.



#### Note:

For accuracy information for this function see the CUDA C++ Programming Guide, Appendix E.1, Table 6.

# \_\_device\_\_ float rcbrtf (float x)

Calculate reciprocal cube root function.

## Returns

- rcbrt( $\pm 0$ ) returns  $\pm \infty$ .
- rcbrt( $\pm \infty$ ) returns  $\pm 0$ .

# Description

Calculate reciprocal cube root function of x



#### Note:

For accuracy information for this function see the CUDA C++ Programming Guide, Appendix E.1, Table 6.

# \_\_device\_\_\_ float remainderf (float x, float y)

Compute single-precision floating-point remainder.

## Returns

- remainderf(x, 0) returns NaN.
- ► remainderf(  $\pm \infty$ , y) returns NaN.
- remainderf(x,  $\pm \infty$ ) returns x for finite x.

# Description

Compute single-precision floating-point remainder  $\mathbf{r}$  of dividing  $\mathbf{x}$  by  $\mathbf{y}$  for nonzero  $\mathbf{y}$ . Thus r = x - ny. The value  $\mathbf{n}$  is the integer value nearest  $\frac{\mathbf{X}}{\mathbf{y}}$ . In the case when  $|n - \frac{\mathbf{X}}{\mathbf{y}}| = \frac{1}{2}$ , the even  $\mathbf{n}$  value is chosen.

# ,

#### Note:

For accuracy information for this function see the CUDA C++ Programming Guide, Appendix E.1, Table 6.

# \_\_device\_\_\_ float remquof (float x, float y, int \*quo)

Compute single-precision floating-point remainder and part of quotient.

## Returns

Returns the remainder.

- remquof(x, 0, quo) returns NaN.
- remquof(  $\pm \infty$ , y, quo) returns NaN.
- remquof(x,  $\pm \infty$ , quo) returns x.

# Description

Compute a double-precision floating-point remainder in the same way as the <u>remainderf()</u> function. Argument quo returns part of quotient upon division of x by y. Value quo has the same sign as  $\frac{X}{y}$  and may not be the exact quotient but agrees with the exact quotient in the low order 3 bits.



#### Note:

For accuracy information for this function see the CUDA C++ Programming Guide, Appendix E.1, Table 6.

# device float rhypotf (float x, float y)

Calculate one over the square root of the sum of squares of two arguments.

## Returns

Returns one over the length of the hypotenuse  $\frac{1}{\sqrt{x^2+y^2}}$ . If the square root would overflow,

returns 0. If the square root would underflow, returns  $+\infty$ .

# Description

Calculates one over the length of the hypotenuse of a right triangle whose two sides have lengths x and y without undue overflow or underflow.



## Note:

For accuracy information for this function see the CUDA C++ Programming Guide, Appendix E.1, Table 6.

# \_\_device\_\_\_ float rintf (float x)

Round input to nearest integer value in floating-point.

## Returns

Returns rounded integer value.

## Description

Round  $\mathbf{x}$  to the nearest integer value in floating-point format, with halfway cases rounded to the nearest even integer value.

# device float rnorm3df (float a, float b, float c)

Calculate one over the square root of the sum of squares of three coordinates of the argument.

#### Returns

Returns one over the length of the 3D vector  $\frac{1}{\sqrt{p.x^2+p.y^2+p.z^2}}$ . If the square root would overflow, returns 0. If the square root would underflow, returns  $+\infty$ .

# Description

Calculates one over the length of three dimension vector p in Euclidean space without undue overflow or underflow



#### Note:

For accuracy information for this function see the CUDA C++ Programming Guide, Appendix E.1, Table 6.

# \_\_device\_\_ float rnorm4df (float a, float b, float c, float d)

Calculate one over the square root of the sum of squares of four coordinates of the argument.

## Returns

Returns one over the length of the 3D vector  $\frac{1}{\sqrt{p.x^2+p.y^2+p.z^2+p.z^2}}$ . If the square root would overflow, returns 0. If the square root would underflow, returns  $+\infty$ .

## Description

Calculates one over the length of four dimension vector p in Euclidean space without undue overflow or underflow.



#### Note:

For accuracy information for this function see the CUDA C++ Programming Guide, Appendix E.1, Table 6.

# device float rnormf (int dim, const float \*a)

Calculate the reciprocal of square root of the sum of squares of any number of coordinates.

## Returns

Returns one over the length of the vector  $\frac{1}{\sqrt{p.1^2+p.2^2+...+p.dim^2}}$ . If the square root would overflow, returns 0. If the square root would underflow, returns  $+\infty$ .

# Description

Calculates one over the length of vector p, dimension of which is passed as an argument, in Euclidean space without undue overflow or underflow.



## Note:

For accuracy information for this function see the CUDA C++ Programming Guide, Appendix E.1, Table 6.

# device float roundf (float x)

Round to nearest integer value in floating-point.

## Returns

Returns rounded integer value.

## Description

Round  $\mathbf{x}$  to the nearest integer value in floating-point format, with halfway cases rounded away from zero.



#### Note:

This function may be slower than alternate rounding methods. See <u>rintf()</u>.

# \_device\_\_ float rsqrtf (float x)

Calculate the reciprocal of the square root of the input argument.

#### Returns

Returns  $1/\sqrt{x}$ .

- ► rsqrtf(  $+\infty$ ) returns +0.
- rsqrtf(  $\pm 0$ ) returns  $\pm \infty$ .
- rsqrtf(x) returns NaN if x is less than 0.

# Description

Calculate the reciprocal of the nonnegative square root of x,  $1/\sqrt{x}$ .



#### Note:

For accuracy information for this function see the CUDA C++ Programming Guide, Appendix E.1, Table 6.

# \_device\_\_\_ float scalblnf (float x, long int n)

Scale floating-point input by integer power of two.

## Returns

Returns  $x * 2^n$ .

- $\triangleright$  scalblnf(  $\pm 0$ , n) returns  $\pm 0$ .
- $\triangleright$  scalblnf(x, 0) returns x.
- ▶ scalblnf( $\pm \infty$ , n) returns  $\pm \infty$ .

## Description

Scale x by  $2^n$  by efficient manipulation of the floating-point exponent.

# \_\_device\_\_\_ float scalbnf (float x, int n)

Scale floating-point input by integer power of two.

#### Returns

Returns  $x * 2^n$ .

- $\triangleright$  scalbnf(  $\pm 0$ , n) returns  $\pm 0$ .
- $\triangleright$  scalbnf(x, 0) returns x.
- ▶ scalbnf(  $\pm \infty$ , n) returns  $\pm \infty$ .

# Description

Scale x by  $2^n$  by efficient manipulation of the floating-point exponent.

# 

Return the sign bit of the input.

## Returns

Reports the sign bit of all values including infinities, zeros, and NaNs.

- ▶ With Visual Studio 2013 host compiler: \_\_RETURN\_TYPE is 'bool'. Returns true if and only if a is negative.
- ▶ With other host compilers: \_\_RETURN\_TYPE is 'int'. Returns a nonzero value if and only if a is negative.

# Description

Determine whether the floating-point value a is negative.

# \_device\_\_ void sincosf (float x, float \*sptr, float \*cptr)

Calculate the sine and cosine of the first input argument.

## Returns

none

## Description

Calculate the sine and cosine of the first input argument x (measured in radians). The results for sine and cosine are written into the second argument, sptr, and, respectively, third argument, cptr.

#### See also:

sinf() and cosf().



## Note:

- For accuracy information for this function see the CUDA C++ Programming Guide, Appendix E.1, Table 6.
- This function is affected by the --use\_fast\_math compiler flag. See the CUDA C++ Programming Guide, Appendix E.2, Table 8 for a complete list of functions affected.

# \_\_device\_\_ void sincospif (float x, float \*sptr, float \*cptr)

Calculate the sine and cosine of the first input argument  $\times \pi$ .

## Returns

none

# Description

Calculate the sine and cosine of the first input argument, x (measured in radians), x  $\pi$ . The results for sine and cosine are written into the second argument, x and, respectively, third argument, x cptr.

#### See also:

sinpif() and cospif().

# **,**

## Note:

For accuracy information for this function see the CUDA C++ Programming Guide, Appendix E.1, Table 6.

# device float sinf (float x)

Calculate the sine of the input argument.

#### Returns

- $\triangleright$  sinf(  $\pm 0$  ) returns  $\pm 0$ .
- ▶  $sinf(\pm \infty)$  returns NaN.

# Description

Calculate the sine of the input argument x (measured in radians).



## Note:

- For accuracy information for this function see the CUDA C++ Programming Guide, Appendix E.1, Table 6.
- This function is affected by the --use\_fast\_math compiler flag. See the CUDA C++ Programming Guide, Appendix E.2, Table 8 for a complete list of functions affected.

# \_\_device\_\_ float sinhf (float x)

Calculate the hyperbolic sine of the input argument.

## Returns

- $\triangleright$  sinhf(  $\pm 0$ ) returns  $\pm 0$ .
- ▶  $\sinh(\pm \infty)$  returns  $\pm \infty$ .

# Description

Calculate the hyperbolic sine of the input argument x.



#### Note:

For accuracy information for this function see the CUDA C++ Programming Guide, Appendix E.1, Table 6.

# \_device\_\_ float sinpif (float x)

Calculate the sine of the input argument  $imes \pi$ .

## Returns

- ▶ sinpif(  $\pm 0$ ) returns  $\pm 0$ .
- ▶  $sinpif(\pm \infty)$  returns NaN.

# Description

Calculate the sine of  $x \times \pi$  (measured in radians), where x is the input argument.



#### Note:

For accuracy information for this function see the CUDA C++ Programming Guide, Appendix E.1, Table 6.

# \_\_device\_\_ float sqrtf (float x)

Calculate the square root of the input argument.

## Returns

Returns  $\sqrt{x}$ .

- sqrtf( $\pm 0$ ) returns  $\pm 0$ .
- ▶  $sqrtf(+\infty)$  returns  $+\infty$ .
- $\triangleright$  sqrtf(x) returns NaN if x is less than 0.

# Description

Calculate the nonnegative square root of x,  $\sqrt{x}$ .



## Note:

For accuracy information for this function see the CUDA C++ Programming Guide, Appendix E.1, Table 6.

# \_device\_\_ float tanf (float x)

Calculate the tangent of the input argument.

## Returns

- $\blacktriangleright$  tanf(  $\pm 0$ ) returns  $\pm 0$ .
- ▶  $tanf(\pm \infty)$  returns NaN.

# Description

Calculate the tangent of the input argument x (measured in radians).



#### Note:

- For accuracy information for this function see the CUDA C++ Programming Guide, Appendix E.1, Table 6.
- ► This function is affected by the --use\_fast\_math compiler flag. See the CUDA C++ Programming Guide, Appendix E.2, Table 8 for a complete list of functions affected.

# \_\_device\_\_ float tanhf (float x)

Calculate the hyperbolic tangent of the input argument.

#### Returns

 $\blacktriangleright$  tanhf(  $\pm 0$ ) returns  $\pm 0$ .

# Description

Calculate the hyperbolic tangent of the input argument x.



#### Note:

For accuracy information for this function see the CUDA C++ Programming Guide, Appendix E.1, Table 6.

# \_device\_\_ float tgammaf (float x)

Calculate the gamma function of the input argument.

## Returns

▶ tgammaf(  $\pm 0$ ) returns  $\pm \infty$ .

- ▶ tgammaf(2) returns +1.
- tgammaf(x) returns  $\pm \infty$  if the correctly calculated value is outside the single floating-point range.
- tgammaf(x) returns NaN if x < 0 and x is an integer.
- ▶ tgammaf( $-\infty$ ) returns NaN.
- ▶ tgammaf(  $+ \infty$ ) returns  $+ \infty$ .

Calculate the gamma function of the input argument x, namely the value of  $\int_0^\infty e^{-t}t^{x-1}dt$ .



## Note:

For accuracy information for this function see the CUDA C++ Programming Guide, Appendix E.1, Table 6.

# \_\_device\_\_ float truncf (float x)

Truncate input argument to the integral part.

## Returns

Returns truncated integer value.

# Description

Round x to the nearest integer value that does not exceed x in magnitude.

# \_\_device\_\_ float y0f (float x)

Calculate the value of the Bessel function of the second kind of order 0 for the input argument.

## Returns

Returns the value of the Bessel function of the second kind of order 0.

- ▶ y0f(0) returns  $-\infty$ .
- $\triangleright$  y0f(x) returns NaN for x < 0.
- ▶  $y0f(+\infty)$  returns +0.
- ▶ y0f(NaN) returns NaN.

## Description

Calculate the value of the Bessel function of the second kind of order 0 for the input argument x,  $Y_0(x)$ .

# ,

## Note:

For accuracy information for this function see the CUDA C++ Programming Guide, Appendix E.1, Table 6.

# \_device\_\_ float y1f (float x)

Calculate the value of the Bessel function of the second kind of order 1 for the input argument.

## Returns

Returns the value of the Bessel function of the second kind of order 1.

- ▶ y1f(0) returns  $-\infty$ .
- ightharpoonup y1f(x) returns NaN for x < 0.
- ▶  $y1f(+\infty)$  returns +0.
- ▶ y1f(NaN) returns NaN.

# Description

Calculate the value of the Bessel function of the second kind of order 1 for the input argument x,  $Y_1(x)$ .



## Note:

For accuracy information for this function see the CUDA C++ Programming Guide, Appendix E.1, Table 6.

# \_device\_\_ float ynf (int n, float x)

Calculate the value of the Bessel function of the second kind of order n for the input argument.

## Returns

Returns the value of the Bessel function of the second kind of order n.

- $\triangleright$  ynf(n, x) returns NaN for n < 0.
- ▶ ynf(n, 0) returns  $-\infty$ .
- $\blacktriangleright$  ynf(n, x) returns NaN for x < 0.
- ▶  $ynf(n, +\infty)$  returns +0.
- ynf(n, NaN) returns NaN.

Calculate the value of the Bessel function of the second kind of order n for the input argument x,  $Y_n(x)$ .



#### Note:

For accuracy information for this function see the CUDA C++ Programming Guide, Appendix E.1, Table 6.

# 1.5. Double Precision Mathematical Functions

This section describes double precision mathematical functions. To use these functions you do not need to include any additional header files in your program.

# device double acos (double x)

Calculate the arc cosine of the input argument.

#### Returns

Result will be in radians, in the interval [0,  $\pi$ ] for x inside [-1, +1].

- ightharpoonup acos(1) returns +0.
- acos(x) returns NaN for x outside [-1, +1].

# Description

Calculate the principal value of the arc cosine of the input argument x.



## Note:

For accuracy information for this function see the CUDA C++ Programming Guide, Appendix E.1, Table 7.

# \_device\_\_ double acosh (double x)

Calculate the nonnegative arc hyperbolic cosine of the input argument.

## Returns

Result will be in the interval  $[0, +\infty]$ .

- acosh(1) returns 0.
- ▶  $a\cosh(x)$  returns NaN for x in the interval  $[-\infty, 1]$ .

Calculate the nonnegative arc hyperbolic cosine of the input argument x.



#### Note:

For accuracy information for this function see the CUDA C++ Programming Guide, Appendix E.1, Table 7.

# device double asin (double x)

Calculate the arc sine of the input argument.

#### Returns

Result will be in radians, in the interval  $[-\pi/2, +\pi/2]$  for x inside [-1, +1].

- ightharpoonup asin(0) returns +0.
- asin(x) returns NaN for x outside [-1, +1].

# Description

Calculate the principal value of the arc sine of the input argument x.



#### Note:

For accuracy information for this function see the CUDA C++ Programming Guide, Appendix E.1, Table 7.

# \_\_device\_\_\_ double asinh (double x)

Calculate the arc hyperbolic sine of the input argument.

## Returns

asinh(0) returns 1.

# Description

Calculate the arc hyperbolic sine of the input argument x.



#### Note:



For accuracy information for this function see the CUDA C++ Programming Guide, Appendix E.1, Table 7.

# device double atan (double x)

Calculate the arc tangent of the input argument.

#### Returns

Result will be in radians, in the interval [-  $\pi$  /2, +  $\pi$  /2].

 $\triangleright$  atan(0) returns +0.

## Description

Calculate the principal value of the arc tangent of the input argument x.



#### Note:

For accuracy information for this function see the CUDA C++ Programming Guide, Appendix E.1, Table 7.

# device double atan2 (double y, double x)

Calculate the arc tangent of the ratio of first and second input arguments.

## Returns

Result will be in radians, in the interval  $[-\pi/+\pi]$ .

atan2(0, 1) returns +0.

# Description

Calculate the principal value of the arc tangent of the ratio of first and second input arguments y / x. The quadrant of the result is determined by the signs of inputs y and x.



#### Note:

For accuracy information for this function see the CUDA C++ Programming Guide, Appendix E.1, Table 7.

# device double atanh (double x)

Calculate the arc hyperbolic tangent of the input argument.

## Returns

- ightharpoonup atanh(  $\pm 0$ ) returns  $\pm 0$ .
- ▶ atanh( $\pm 1$ ) returns  $\pm \infty$ .
- atanh(x) returns NaN for x outside interval [-1, 1].

# Description

Calculate the arc hyperbolic tangent of the input argument x.



#### Note:

For accuracy information for this function see the CUDA C++ Programming Guide, Appendix E.1, Table 7.

# \_device\_\_ double cbrt (double x)

Calculate the cube root of the input argument.

#### Returns

Returns  $x^{1/3}$ .

- $\triangleright$  cbrt(  $\pm 0$ ) returns  $\pm 0$ .
- ▶ cbrt( $\pm \infty$ ) returns  $\pm \infty$ .

## Description

Calculate the cube root of x,  $x^{1/3}$ .



## Note:

For accuracy information for this function see the CUDA C++ Programming Guide, Appendix E.1, Table 7.

# \_\_\_device\_\_\_ double ceil (double x)

Calculate ceiling of the input argument.

## Returns

Returns [x] expressed as a floating-point number.

- $\triangleright$  ceil(  $\pm 0$ ) returns  $\pm 0$ .
- ▶ ceil(  $\pm \infty$ ) returns  $\pm \infty$ .

Compute the smallest integer value not less than x.

# \_\_device\_\_ double copysign (double x, double y)

Create value with given magnitude, copying sign of second value.

## Returns

Returns a value with the magnitude of x and the sign of y.

## Description

Create a floating-point value with the magnitude  ${\bf x}$  and the sign of  ${\bf y}$ .

# \_\_\_device\_\_\_ double cos (double x)

Calculate the cosine of the input argument.

## Returns

- $\triangleright$  cos(  $\pm 0$ ) returns 1.
- ▶  $\cos(\pm \infty)$  returns NaN.

# Description

Calculate the cosine of the input argument x (measured in radians).



## Note:

For accuracy information for this function see the CUDA C++ Programming Guide, Appendix E.1, Table 7.

# \_device\_\_ double cosh (double x)

Calculate the hyperbolic cosine of the input argument.

#### Returns

- cosh(0) returns 1.
- ▶  $\cosh(\pm \infty)$  returns  $+ \infty$ .

Calculate the hyperbolic cosine of the input argument x.



#### Note:

For accuracy information for this function see the CUDA C++ Programming Guide, Appendix E.1, Table 7.

# \_\_device\_\_\_ double cospi (double x)

Calculate the cosine of the input argument  $imes \pi$ .

## Returns

- $\triangleright$  cospi(  $\pm 0$  ) returns 1.
- ▶ cospi(  $\pm \infty$ ) returns NaN.

## Description

Calculate the cosine of  $x \times \pi$  (measured in radians), where x is the input argument.



#### Note:

For accuracy information for this function see the CUDA C++ Programming Guide, Appendix E.1, Table 7.

# \_device\_\_ double cyl\_bessel\_i0 (double x)

Calculate the value of the regular modified cylindrical Bessel function of order 0 for the input argument.

#### Returns

Returns the value of the regular modified cylindrical Bessel function of order 0.

# Description

Calculate the value of the regular modified cylindrical Bessel function of order 0 for the input argument x,  $I_0(x)$ .



#### Note:



For accuracy information for this function see the CUDA C++ Programming Guide, Appendix E.1, Table 7.

# \_device\_\_ double cyl\_bessel\_i1 (double x)

Calculate the value of the regular modified cylindrical Bessel function of order 1 for the input argument.

## Returns

Returns the value of the regular modified cylindrical Bessel function of order 1.

## Description

Calculate the value of the regular modified cylindrical Bessel function of order 1 for the input argument x,  $I_1(x)$ .



#### Note:

For accuracy information for this function see the CUDA C++ Programming Guide, Appendix E.1, Table 7.

# device double erf (double x)

Calculate the error function of the input argument.

## Returns

- erf(  $\pm 0$ ) returns  $\pm 0$ .
- $\triangleright$  erf(  $\pm \infty$ ) returns  $\pm 1$ .

# Description

Calculate the value of the error function for the input argument x,  $\frac{2}{\sqrt{\pi}} \int_{0}^{\infty} e^{-t^2} dt$ .



## Note:

For accuracy information for this function see the CUDA C++ Programming Guide, Appendix E.1, Table 7.

# \_device\_\_ double erfc (double x)

Calculate the complementary error function of the input argument.

## Returns

- ▶ erfc( $-\infty$ ) returns 2.
- ▶ erfc(  $+ \infty$ ) returns +0.

# Description

Calculate the complementary error function of the input argument x, 1 - erf(x).



#### Note:

For accuracy information for this function see the CUDA C++ Programming Guide, Appendix E.1, Table 7.

# \_\_device\_\_ double erfcinv (double y)

Calculate the inverse complementary error function of the input argument.

## Returns

- ▶ erfcinv(0) returns  $+\infty$ .
- ▶ erfcinv(2) returns  $-\infty$ .

# Description

Calculate the inverse complementary error function of the input argument y, for y in the interval [0, 2]. The inverse complementary error function find the value x that satisfies the equation  $y = \operatorname{erfc}(x)$ , for  $0 \le y \le 2$ , and  $-\infty \le x \le \infty$ .



#### Note:

For accuracy information for this function see the CUDA C++ Programming Guide, Appendix E.1, Table 7.

# \_device\_\_\_ double erfcx (double x)

Calculate the scaled complementary error function of the input argument.

#### Returns

▶  $\operatorname{erfcx}(-\infty)\operatorname{returns} + \infty$ 

- ▶  $\operatorname{erfcx}(+\infty)\operatorname{returns}+0$
- $\operatorname{erfcx}(x)$  returns  $+\infty$  if the correctly calculated value is outside the double floating-point range.

Calculate the scaled complementary error function of the input argument x,  $e^{x^2} \cdot \operatorname{erfc}(x)$ .



#### Note:

For accuracy information for this function see the CUDA C++ Programming Guide, Appendix E.1, Table 7.

# \_device\_\_ double erfinv (double y)

Calculate the inverse error function of the input argument.

## Returns

- ▶ erfinv(1) returns  $+\infty$ .
- ▶ erfinv(-1) returns  $-\infty$ .

# Description

Calculate the inverse error function of the input argument y, for y in the interval [-1, 1]. The inverse error function finds the value x that satisfies the equation y = erf(x), for  $-1 \le y \le 1$ , and  $-\infty \le x \le \infty$ .



#### Note:

For accuracy information for this function see the CUDA C++ Programming Guide, Appendix E.1, Table 7.

# \_device\_\_ double exp (double x)

Calculate the base e exponential of the input argument.

## Returns

Returns  $e^{x}$ .

# Description

Calculate the base e exponential of the input argument x.

# •

## Note:

For accuracy information for this function see the CUDA C++ Programming Guide, Appendix E.1, Table 7.

# \_\_device\_\_ double exp10 (double x)

Calculate the base 10 exponential of the input argument.

## Returns

Returns  $10^{x}$ .

## Description

Calculate the base 10 exponential of the input argument x.



#### Note:

For accuracy information for this function see the CUDA C++ Programming Guide, Appendix E.1, Table 7.

# \_\_device\_\_ double exp2 (double x)

Calculate the base 2 exponential of the input argument.

## Returns

Returns  $2^x$ .

# Description

Calculate the base 2 exponential of the input argument x.



## Note:

For accuracy information for this function see the CUDA C++ Programming Guide, Appendix E.1, Table 7.

# \_\_device\_\_ double expm1 (double x)

Calculate the base e exponential of the input argument, minus 1.

## Returns

Returns  $e^{x} - 1$ .

Calculate the base e exponential of the input argument x, minus 1.



#### Note:

For accuracy information for this function see the CUDA C++ Programming Guide, Appendix E.1, Table 7.

# device double fabs (double x)

Calculate the absolute value of the input argument.

## Returns

Returns the absolute value of the input argument.

- ▶ fabs(  $\pm \infty$ ) returns  $+ \infty$ .
- fabs(  $\pm 0$ ) returns 0.

## Description

Calculate the absolute value of the input argument x.



## Note:

For accuracy information for this function see the CUDA C++ Programming Guide, Appendix E.1, Table 7.

# \_\_device\_\_\_ double fdim (double x, double y)

Compute the positive difference between x and y.

#### Returns

Returns the positive difference between x and y.

- fdim(x, y) returns x y if x > y.
- fdim(x, y) returns +0 if  $x \le y$ .

## Description

Compute the positive difference between x and y. The positive difference is x - y when x > y and +0 otherwise.

# **,**

## Note:

For accuracy information for this function see the CUDA C++ Programming Guide, Appendix E.1, Table 6.

# device double floor (double x)

Calculate the largest integer less than or equal to x.

## Returns

Returns [x] expressed as a floating-point number.

- ▶ floor( $\pm \infty$ ) returns  $\pm \infty$ .
- floor(  $\pm 0$ ) returns  $\pm 0$ .

# Description

Calculates the largest integer value which is less than or equal to x.



## Note:

For accuracy information for this function see the CUDA C++ Programming Guide, Appendix E.1, Table 7.

# \_device\_\_ double fma (double x, double y, double z)

Compute  $x \times y + z$  as a single operation.

## Returns

Returns the rounded value of  $x \times y + z$  as a single operation.

- ▶ fma(  $\pm \infty$  .  $\pm 0$  . z) returns NaN.
- fma(  $\pm 0$ ,  $\pm \infty$ , z) returns NaN.
- ▶ fma(x, y,  $-\infty$ ) returns NaN if  $x \times y$  is an exact  $+\infty$ .
- ▶ fma(x, y, +∞) returns NaN if  $x \times y$  is an exact -∞.

## Description

Compute the value of  $x \times y + z$  as a single ternary operation. After computing the value to infinite precision, the value is rounded once.



## Note:



For accuracy information for this function see the CUDA C++ Programming Guide, Appendix E.1, Table 7.

# \_\_device\_\_\_ double fmax (double, double)

Determine the maximum numeric value of the arguments.

#### Returns

Returns the maximum numeric values of the arguments x and y.

- If both arguments are NaN, returns NaN.
- If one argument is NaN, returns the numeric argument.

## Description

Determines the maximum numeric value of the arguments x and y. Treats NaN arguments as missing data. If one argument is a NaN and the other is legitimate numeric value, the numeric value is chosen.



#### Note:

For accuracy information for this function see the CUDA C++ Programming Guide, Appendix E.1, Table 7.

# \_\_device\_\_ double fmin (double x, double y)

Determine the minimum numeric value of the arguments.

#### Returns

Returns the minimum numeric value of the arguments x and y.

- If both arguments are NaN, returns NaN.
- If one argument is NaN, returns the numeric argument.

# Description

Determines the minimum numeric value of the arguments x and y. Treats NaN arguments as missing data. If one argument is a NaN and the other is legitimate numeric value, the numeric value is chosen.



#### Note:

For accuracy information for this function see the CUDA C++ Programming Guide, Appendix E.1, Table 7.

# \_device\_\_ double fmod (double x, double y)

Calculate the double-precision floating-point remainder of x / y.

## Returns

- ► Returns the floating-point remainder of x / y.
- fmod(  $\pm 0$ , y) returns  $\pm 0$  if y is not zero.
- ▶ fmod(x,  $\pm \infty$ ) returns x if x is finite.
- fmod(x, y) returns NaN if x is  $\pm \infty$  or y is zero.
- If either argument is NaN, NaN is returned.

## Description

Calculate the double-precision floating-point remainder of x / y. The floating-point remainder of the division operation x / y calculated by this function is exactly the value x - n\*y, where n is x / y with its fractional part truncated. The computed value will have the same sign as x, and its magnitude will be less than the magnitude of y.



#### Note:

For accuracy information for this function see the CUDA C++ Programming Guide, Appendix E.1, Table 7.

# \_device\_\_ double frexp (double x, int \*nptr)

Extract mantissa and exponent of a floating-point value.

## Returns

Returns the fractional component m.

- frexp(0, nptr) returns 0 for the fractional component and zero for the integer component.
- frexp( $\pm 0$ , nptr) returns  $\pm 0$  and stores zero in the location pointed to by nptr.
- frexp( $\pm \infty$ , nptr) returns  $\pm \infty$  and stores an unspecified value in the location to which nptr points.
- frexp(NaN, y) returns a NaN and stores an unspecified value in the location to which nptr points.

# Description

Decompose the floating-point value x into a component m for the normalized fraction element and another term n for the exponent. The absolute value of m will be greater than or equal to

0.5 and less than 1.0 or it will be equal to 0;  $x = m \cdot 2^n$ . The integer exponent n will be stored in the location to which nptr points.



## Note:

For accuracy information for this function see the CUDA C++ Programming Guide, Appendix E.1, Table 7.

# \_\_device\_\_\_ double hypot (double x, double y)

Calculate the square root of the sum of squares of two arguments.

#### Returns

Returns the length of the hypotenuse  $\sqrt{x^2+y^2}$ . If the correct value would overflow, returns  $+\infty$ . If the correct value would underflow, returns 0.

## Description

Calculate the length of the hypotenuse of a right triangle whose two sides have lengths  $\mathbf{x}$  and  $\mathbf{y}$  without undue overflow or underflow.



#### Note:

For accuracy information for this function see the CUDA C++ Programming Guide, Appendix E.1. Table 7.

# \_\_device\_\_\_ int ilogb (double x)

Compute the unbiased integer exponent of the argument.

#### Returns

- If successful, returns the unbiased exponent of the argument.
- ▶ ilogb(0) returns INT MIN.
- ▶ ilogb(NaN) returns INT MIN.
- ▶ ilogb(x) returns INT\_MAX if x is  $\infty$  or the correct value is greater than INT\_MAX.
- ▶ ilogb(x) returns INT MIN if the correct value is less than INT MIN.
- ▶ Note: above behavior does not take into account FP ILOGBO nor FP ILOGBNAN.

# Description

Calculates the unbiased integer exponent of the input argument x.

# •

#### Note:

For accuracy information for this function see the CUDA C++ Programming Guide, Appendix E.1, Table 7.

# \_\_device\_\_ \_\_RETURN\_TYPE isfinite (double a)

Determine whether argument is finite.

## Returns

- ▶ With Visual Studio 2013 host compiler: \_\_RETURN\_TYPE is 'bool'. Returns true if and only if a is a finite value.
- With other host compilers: \_\_RETURN\_TYPE is 'int'. Returns a nonzero value if and only if a is a finite value.

# Description

Determine whether the floating-point value a is a finite value (zero, subnormal, or normal and not infinity or NaN).

# \_\_device\_\_ \_RETURN\_TYPE isinf (double a)

Determine whether argument is infinite.

#### Returns

- ▶ With Visual Studio 2013 host compiler: Returns true if and only if a is a infinite value.
- With other host compilers: Returns a nonzero value if and only if a is a infinite value.

# Description

Determine whether the floating-point value a is an infinite value (positive or negative).

# \_\_device\_\_ \_RETURN\_TYPE isnan (double a)

Determine whether argument is a NaN.

## Returns

- ▶ With Visual Studio 2013 host compiler: \_\_RETURN\_TYPE is 'bool'. Returns true if and only if a is a NaN value.
- With other host compilers: \_\_RETURN\_TYPE is 'int'. Returns a nonzero value if and only if a is a NaN value.

Determine whether the floating-point value a is a NaN.

# \_device\_\_ double j0 (double x)

Calculate the value of the Bessel function of the first kind of order 0 for the input argument.

## Returns

Returns the value of the Bessel function of the first kind of order 0.

- ▶  $j0(\pm \infty)$  returns +0.
- ▶ j0(NaN) returns NaN.

## Description

Calculate the value of the Bessel function of the first kind of order 0 for the input argument x,  $J_0(x)$ .



## Note:

For accuracy information for this function see the CUDA C++ Programming Guide, Appendix E.1, Table 7.

# \_device\_\_ double j1 (double x)

Calculate the value of the Bessel function of the first kind of order 1 for the input argument.

#### Returns

Returns the value of the Bessel function of the first kind of order 1.

- ightharpoonup j1(  $\pm 0$ ) returns  $\pm 0$ .
- ▶  $j1(\pm \infty)$  returns  $\pm 0$ .
- ▶ j1(NaN) returns NaN.

# Description

Calculate the value of the Bessel function of the first kind of order 1 for the input argument x,  $J_1(x)$ .



#### Note

For accuracy information for this function see the CUDA C++ Programming Guide, Appendix E.1, Table 7.

# \_device\_\_\_ double jn (int n, double x)

Calculate the value of the Bessel function of the first kind of order n for the input argument.

## Returns

Returns the value of the Bessel function of the first kind of order n.

- ▶ jn(n, NaN) returns NaN.
- in(n, x) returns NaN for n < 0.
- ▶  $jn(n, +\infty)$  returns +0.

## Description

Calculate the value of the Bessel function of the first kind of order n for the input argument x,  $J_n(x)$ .



#### Note:

For accuracy information for this function see the CUDA C++ Programming Guide, Appendix E.1, Table 7.

# \_\_\_device\_\_\_ double ldexp (double x, int exp)

Calculate the value of  $x \cdot 2^{exp}$ .

## Returns

▶ ldexp(x) returns  $\pm \infty$  if the correctly calculated value is outside the double floating-point range.

# Description

Calculate the value of  $x \cdot 2^{exp}$  of the input arguments x and exp.



## Note:

For accuracy information for this function see the CUDA C++ Programming Guide, Appendix E.1, Table 7.

# \_device\_\_\_ double lgamma (double x)

Calculate the natural logarithm of the absolute value of the gamma function of the input argument.

#### Returns

- ▶ lgamma(1) returns +0.
- ▶ lgamma(2) returns +0.
- ▶ lgamma(x) returns  $\pm \infty$  if the correctly calculated value is outside the double floating-point range.
- ▶ lgamma(x) returns  $+\infty$  if  $x \le 0$  and x is an integer.
- lgamma( $-\infty$ ) returns  $-\infty$ .
- ▶ lgamma(  $+ \infty$ ) returns  $+ \infty$ .

## Description

Calculate the natural logarithm of the absolute value of the gamma function of the input argument x, namely the value of  $\log_e \left| \int_0^\infty e^{-t} t^{x-1} dt \right|$ 



#### Note:

For accuracy information for this function see the CUDA C++ Programming Guide, Appendix E.1, Table 7.

# \_\_device\_\_ long long int llrint (double x)

Round input to nearest integer value.

#### Returns

Returns rounded integer value.

# Description

Round x to the nearest integer value, with halfway cases rounded to the nearest even integer value. If the result is outside the range of the return type, the result is undefined.

# \_device\_\_ long long int llround (double x)

Round to nearest integer value.

#### Returns

Returns rounded integer value.

Round x to the nearest integer value, with halfway cases rounded away from zero. If the result is outside the range of the return type, the result is undefined.



#### Note:

This function may be slower than alternate rounding methods. See <a href="llrint()"><u>llrint()</u></a>.

# \_\_device\_\_\_ double log (double x)

Calculate the base e logarithm of the input argument.

## Returns

- ▶  $\log(\pm 0)$  returns  $-\infty$ .
- $\triangleright$  log(1) returns +0.
- $\triangleright$  log(x) returns NaN for x < 0.
- ▶  $log(+\infty)$  returns  $+\infty$

## Description

Calculate the base e logarithm of the input argument  ${\tt x}$ .



## Note:

For accuracy information for this function see the CUDA C++ Programming Guide, Appendix E.1, Table 7.

# \_\_device\_\_ double log10 (double x)

Calculate the base 10 logarithm of the input argument.

#### Returns

- log10(  $\pm 0$ ) returns  $-\infty$ .
- ▶ log10(1) returns +0.
- log10(x) returns NaN for x < 0.
- ▶  $log10(+\infty)$  returns  $+\infty$ .

# Description

Calculate the base 10 logarithm of the input argument x.

# ,

## Note:

For accuracy information for this function see the CUDA C++ Programming Guide, Appendix E.1, Table 7.

# \_device\_\_ double log1p (double x)

Calculate the value of  $log_o(1+x)$ .

#### Returns

- log1p( $\pm 0$ ) returns  $\pm 0$ .
- log1p(-1) returns  $-\infty$ .
- log1p(x) returns NaN for x < -1.
- log1p( + ∞) returns + ∞.

# Description

Calculate the value of  $log_o(1+x)$  of the input argument x.



## Note:

For accuracy information for this function see the CUDA C++ Programming Guide, Appendix E.1, Table 7.

# \_device\_\_ double log2 (double x)

Calculate the base 2 logarithm of the input argument.

## Returns

- log2(  $\pm 0$ ) returns  $-\infty$ .
- $\triangleright$  log2(1) returns +0.
- log2(x) returns NaN for x < 0.
- ▶  $\log 2(+\infty)$  returns  $+\infty$ .

## Description

Calculate the base 2 logarithm of the input argument x.



#### Note:



For accuracy information for this function see the CUDA C++ Programming Guide, Appendix E.1, Table 7.

# \_\_device\_\_\_ double logb (double x)

Calculate the floating-point representation of the exponent of the input argument.

#### Returns

- ▶ logb  $\pm 0$  returns  $-\infty$
- logb ± ∞ returns + ∞

## Description

Calculate the floating-point representation of the exponent of the input argument x.



#### Note:

For accuracy information for this function see the CUDA C++ Programming Guide, Appendix E.1, Table 7.

# \_\_device\_\_ long int lrint (double x)

Round input to nearest integer value.

## Returns

Returns rounded integer value.

# Description

Round x to the nearest integer value, with halfway cases rounded to the nearest even integer value. If the result is outside the range of the return type, the result is undefined.

# \_\_device\_\_ long int lround (double x)

Round to nearest integer value.

## Returns

Returns rounded integer value.

# Description

Round x to the nearest integer value, with halfway cases rounded away from zero. If the result is outside the range of the return type, the result is undefined.



#### Note:

This function may be slower than alternate rounding methods. See <a href="lint()">lrint()</a>.

# \_\_device\_\_ double max (const double a, const float b)

Calculate the maximum value of the input double and float arguments.

# Description

Convert float argument b to double, followed by fmax().

Note, this is different from std:: specification

# \_\_device\_\_\_ double max (const float a, const double b)

Calculate the maximum value of the input float and double arguments.

# Description

Convert float argument a to double, followed by fmax().

Note, this is different from std:: specification

# \_\_device\_\_ double max (const double a, const double b)

Calculate the maximum value of the input float arguments.

# Description

Calculate the maximum value of the arguments a and b. Behavior is equivalent to <u>fmax()</u> function.

Note, this is different from std:: specification

# \_\_device\_\_ double min (const double a, const float b)

Calculate the minimum value of the input double and float arguments.

# Description

Convert float argument b to double, followed by fmin().

Note, this is different from std:: specification

# \_device\_\_ double min (const float a, const double b)

Calculate the minimum value of the input float and double arguments.

# Description

Convert float argument a to double, followed by fmin().

Note, this is different from std:: specification

# \_\_device\_\_ double min (const double a, const double b)

Calculate the minimum value of the input float arguments.

# Description

Calculate the minimum value of the arguments a and b. Behavior is equivalent to <u>fmin()</u> function.

Note, this is different from std:: specification

# \_\_device\_\_ double modf (double x, double \*iptr)

Break down the input argument into fractional and integral parts.

## Returns

- $\blacktriangleright$  modf( $\pm x$ , iptr) returns a result with the same sign as x.
- ▶ modf( $\pm \infty$ , iptr) returns  $\pm 0$  and stores  $\pm \infty$  in the object pointed to by iptr.
- modf(NaN, iptr) stores a NaN in the object pointed to by iptr and returns a NaN.

# Description

Break down the argument x into fractional and integral parts. The integral part is stored in the argument iptr. Fractional and integral parts are given the same sign as the argument x.



#### Note

For accuracy information for this function see the CUDA C++ Programming Guide, Appendix E.1, Table 7.

# \_device\_\_\_ double nan (const char \*tagp)

Returns "Not a Number" value.

## Returns

nan(tagp) returns NaN.

## Description

Return a representation of a quiet NaN. Argument tagp selects one of the possible representations.



#### Note:

For accuracy information for this function see the CUDA C++ Programming Guide, Appendix E.1, Table 7.

# \_\_device\_\_ double nearbyint (double x)

Round the input argument to the nearest integer.

## Returns

- nearbyint(  $\pm 0$ ) returns  $\pm 0$ .
- ▶ nearbyint(  $\pm \infty$ ) returns  $\pm \infty$ .

# Description

Round argument x to an integer value in double precision floating-point format.



#### Note:

For accuracy information for this function see the CUDA C++ Programming Guide, Appendix E.1, Table 7.

# \_device\_\_ double nextafter (double x, double y)

Return next representable double-precision floating-point value after argument  $\mathbf{x}$  in the direction of  $\mathbf{y}$ .

## Returns

- nextafter(x, y) = y if x equals y
- $\blacktriangleright$  nextafter(x, y) = NaN if either x or y are NaN

Calculate the next representable double-precision floating-point value following  $\mathbf{x}$  in the direction of  $\mathbf{y}$ . For example, if  $\mathbf{y}$  is greater than  $\mathbf{x}$ ,  $\underline{\text{nextafter()}}$  returns the smallest representable number greater than  $\mathbf{x}$ 



#### Note:

For accuracy information for this function see the CUDA C++ Programming Guide, Appendix E.1, Table 7.

# \_device\_\_ double norm (int dim, const double \*t)

Calculate the square root of the sum of squares of any number of coordinates.

## Returns

Returns the length of the dim-D vector  $\sqrt{\mathbf{p}.1^2 + \mathbf{p}.2^2 + ... + \mathbf{p}.\mathbf{dim}^2}$ . If the correct value would overflow, returns  $+\infty$ . If the correct value would underflow, returns 0. If two of the input arguments is 0, returns remaining argument

## Description

Calculate the length of a vector p, dimension of which is passed as an argument without undue overflow or underflow.



## Note:

For accuracy information for this function see the CUDA C++ Programming Guide, Appendix E.1, Table 7.

# \_\_device\_\_ double norm3d (double a, double b, double c)

Calculate the square root of the sum of squares of three coordinates of the argument.

## Returns

Returns the length of 3D vector  $\sqrt{\mathbf{p}.\mathbf{x}^2 + \mathbf{p}.\mathbf{y}^2 + \mathbf{p}.\mathbf{z}^2}$ . If the correct value would overflow, returns  $+\infty$ . If the correct value would underflow, returns 0.

Calculate the length of three dimensional vector p in Euclidean space without undue overflow or underflow.



#### Note:

For accuracy information for this function see the CUDA C++ Programming Guide, Appendix E.1, Table 7.

# \_\_device\_\_ double norm4d (double a, double b, double c, double d)

Calculate the square root of the sum of squares of four coordinates of the argument.

#### Returns

Returns the length of 4D vector  $\sqrt{\mathbf{p}.\mathbf{x}^2 + \mathbf{p}.\mathbf{y}^2 + \mathbf{p}.\mathbf{z}^2 + \mathbf{p}.\mathbf{t}^2}$ . If the correct value would overflow, returns  $+\infty$ . If the correct value would underflow, returns 0.

## Description

Calculate the length of four dimensional vector p in Euclidean space without undue overflow or underflow.



#### Note:

For accuracy information for this function see the CUDA C++ Programming Guide, Appendix E.1, Table 7.

# \_\_device\_\_\_ double normcdf (double y)

Calculate the standard normal cumulative distribution function.

#### Returns

- normcdf( + ∞) returns 1
- ▶ normcdf( $-\infty$ ) returns +0

# Description

Calculate the cumulative distribution function of the standard normal distribution for input argument y,  $\Phi(y)$ .

# •

## Note:

For accuracy information for this function see the CUDA C++ Programming Guide, Appendix E.1, Table 7.

# \_device\_\_ double normcdfinv (double y)

Calculate the inverse of the standard normal cumulative distribution function.

#### Returns

- normcdfinv(0) returns -∞.
- ▶ normcdfinv(1) returns  $+\infty$ .
- normcdfinv(x) returns NaN if x is not in the interval [0,1].

# Description

Calculate the inverse of the standard normal cumulative distribution function for input argument y,  $\Phi^{-1}(y)$ . The function is defined for input values in the interval (0, 1).



#### Note:

For accuracy information for this function see the CUDA C++ Programming Guide, Appendix E.1, Table 7.

# \_\_device\_\_ double pow (double x, double y)

Calculate the value of first argument to the power of second argument.

## Returns

- ▶ pow(  $\pm 0$ , y) returns  $\pm \infty$  for y an integer less than 0.
- pow(  $\pm 0$ , y) returns  $\pm 0$  for y an odd integer greater than 0.
- $\triangleright$  pow(  $\pm 0$ , y) returns +0 for y > 0 and not and odd integer.
- ▶ pow(-1,  $\pm \infty$ ) returns 1.
- pow(+1, y) returns 1 for any y, even a NaN.
- ightharpoonup pow(x,  $\pm 0$ ) returns 1 for any x, even a NaN.
- ightharpoonup pow(x, y) returns a NaN for finite x < 0 and finite non-integer y.
- ▶ pow(x,  $-\infty$ ) returns  $+\infty$  for |x| < 1.
- ▶ pow(x,  $-\infty$ ) returns +0 for |x| > 1.
- ▶ pow(x,  $+\infty$ ) returns +0 for |x| < 1.

- ▶ pow(x, +∞) returns +∞ for |x| > 1.
- ▶ pow( $-\infty$ , y) returns -0 for y an odd integer less than 0.
- ▶ pow( $-\infty$ , y) returns +0 for y < 0 and not an odd integer.
- ▶ pow( $-\infty$ , y) returns  $-\infty$  for y an odd integer greater than 0.
- ▶ pow( $-\infty$ , y) returns  $+\infty$  for y > 0 and not an odd integer.
- ▶ pow(  $+\infty$ , y) returns +0 for y < 0.
- ▶ pow(  $+\infty$ , y) returns  $+\infty$  for y > 0.

Calculate the value of x to the power of y



#### Note:

For accuracy information for this function see the CUDA C++ Programming Guide, Appendix E.1. Table 7.

# \_\_device\_\_ double rcbrt (double x)

Calculate reciprocal cube root function.

#### Returns

- rcbrt( $\pm 0$ ) returns  $\pm \infty$ .
- rcbrt( $\pm \infty$ ) returns  $\pm 0$ .

# Description

Calculate reciprocal cube root function of x



#### Note:

For accuracy information for this function see the CUDA C++ Programming Guide, Appendix E.1, Table 7.

# \_device\_\_ double remainder (double x, double y)

Compute double-precision floating-point remainder.

#### Returns

- remainder(x, 0) returns NaN.
- remainder( $\pm \infty$ , y) returns NaN.

remainder(x,  $\pm \infty$ ) returns x for finite x.

# Description

Compute double-precision floating-point remainder  $\mathbf{r}$  of dividing  $\mathbf{x}$  by  $\mathbf{y}$  for nonzero  $\mathbf{y}$ . Thus  $r = \mathbf{x} - n\mathbf{y}$ . The value  $\mathbf{n}$  is the integer value nearest  $\frac{\mathbf{X}}{\mathbf{Y}}$ . In the case when  $|\mathbf{n} - \frac{\mathbf{X}}{\mathbf{Y}}| = \frac{1}{2}$ , the even  $\mathbf{n}$  value is chosen.



#### Note:

For accuracy information for this function see the CUDA C++ Programming Guide, Appendix E.1, Table 7.

# \_\_device\_\_ double remquo (double x, double y, int \*quo)

Compute double-precision floating-point remainder and part of quotient.

#### Returns

Returns the remainder.

- remquo(x, 0, quo) returns NaN.
- remquo(  $\pm \infty$ , y, quo) returns NaN.
- remquo(x,  $\pm \infty$ , quo) returns x.

# Description

Compute a double-precision floating-point remainder in the same way as the <u>remainder()</u> function. Argument quo returns part of quotient upon division of x by y. Value quo has the same sign as  $\frac{X}{y}$  and may not be the exact quotient but agrees with the exact quotient in the low order 3 bits.



## Note:

For accuracy information for this function see the CUDA C++ Programming Guide, Appendix E.1, Table 7.

# \_device\_\_ double rhypot (double x, double y)

Calculate one over the square root of the sum of squares of two arguments.

## Returns

Returns one over the length of the hypotenuse  $\frac{1}{\sqrt{x^2+y^2}}$ . If the square root would overflow, returns 0. If the square root would underflow, returns  $+\infty$ .

## Description

Calculate one over the length of the hypotenuse of a right triangle whose two sides have lengths x and y without undue overflow or underflow.



#### Note:

For accuracy information for this function see the CUDA C++ Programming Guide, Appendix E.1, Table 7.

# \_\_device\_\_ double rint (double x)

Round to nearest integer value in floating-point.

#### Returns

Returns rounded integer value.

# Description

Round  $\mathbf{x}$  to the nearest integer value in floating-point format, with halfway cases rounded to the nearest even integer value.

# device double rnorm (int dim, const double \*t)

Calculate the reciprocal of square root of the sum of squares of any number of coordinates.

## Returns

Returns one over the length of the vector  $\frac{1}{\sqrt{p.1^2+p.2^2+...+p.dim^2}}$ . If the square root would overflow, returns 0. If the square root would underflow, returns  $+\infty$ .

# Description

Calculates one over the length of vector p, dimension of which is passed as an argument, in Euclidean space without undue overflow or underflow.

# **,**

#### Note:

For accuracy information for this function see the CUDA C++ Programming Guide, Appendix E.1, Table 7.

# \_\_device\_\_ double rnorm3d (double a, double b, double c)

Calculate one over the square root of the sum of squares of three coordinates of the argument.

#### Returns

Returns one over the length of the 3D vector  $\frac{1}{\sqrt{p.x^2+p.y^2+p.z^2}}$ . If the square root would overflow, returns 0. If the square root would underflow, returns  $+\infty$ .

## Description

Calculate one over the length of three dimensional vector p in Euclidean space undue overflow or underflow.



#### Note:

For accuracy information for this function see the CUDA C++ Programming Guide, Appendix E.1, Table 7.

# \_\_device\_\_ double rnorm4d (double a, double b, double c, double d)

Calculate one over the square root of the sum of squares of four coordinates of the argument.

## Returns

Returns one over the length of the 3D vector  $\frac{1}{\sqrt{p.x^2+p.y^2+p.z^2+p.t^2}}$ . If the square root would overflow, returns 0. If the square root would underflow, returns  $+\infty$ .

# Description

Calculate one over the length of four dimensional vector p in Euclidean space undue overflow or underflow.



## Note:

For accuracy information for this function see the CUDA C++ Programming Guide, Appendix E.1, Table 7.

# \_device\_\_ double round (double x)

Round to nearest integer value in floating-point.

## Returns

Returns rounded integer value.

## Description

Round x to the nearest integer value in floating-point format, with halfway cases rounded away from zero.



#### Note:

This function may be slower than alternate rounding methods. See <u>rint()</u>.

# \_device\_\_ double rsqrt (double x)

Calculate the reciprocal of the square root of the input argument.

## Returns

Returns  $1/\sqrt{x}$ .

- rsgrt( + ∞) returns +0.
- rsqrt(  $\pm 0$ ) returns  $\pm \infty$ .
- rsqrt(x) returns NaN if x is less than 0.

# Description

Calculate the reciprocal of the nonnegative square root of x,  $1/\sqrt{x}$ .



#### Note:

For accuracy information for this function see the CUDA C++ Programming Guide, Appendix E.1, Table 7.

# \_device\_\_ double scalbln (double x, long int n)

Scale floating-point input by integer power of two.

### Returns

Returns  $x * 2^n$ .

- $\triangleright$  scalbln(  $\pm 0$ , n) returns  $\pm 0$ .
- ightharpoonup scalbln(x, 0) returns x.
- ▶ scalbln( $\pm \infty$ , n) returns  $\pm \infty$ .

### Description

Scale x by  $2^n$  by efficient manipulation of the floating-point exponent.

# \_\_device\_\_ double scalbn (double x, int n)

Scale floating-point input by integer power of two.

#### Returns

Returns  $x * 2^n$ .

- $\triangleright$  scalbn(  $\pm 0$ , n) returns  $\pm 0$ .
- $\triangleright$  scalbn(x, 0) returns x.
- ▶ scalbn(  $\pm \infty$ , n) returns  $\pm \infty$ .

### Description

Scale x by  $2^n$  by efficient manipulation of the floating-point exponent.

# \_device\_\_ \_\_RETURN\_TYPE signbit (double a)

Return the sign bit of the input.

### Returns

Reports the sign bit of all values including infinities, zeros, and NaNs.

- With Visual Studio 2013 host compiler: \_\_RETURN\_TYPE is 'bool'. Returns true if and only if a is negative.
- With other host compilers: \_\_RETURN\_TYPE is 'int'. Returns a nonzero value if and only if a is negative.

# Description

Determine whether the floating-point value a is negative.

# \_device\_\_ double sin (double x)

Calculate the sine of the input argument.

### Returns

- $\triangleright$  sin(  $\pm 0$ ) returns  $\pm 0$ .
- ▶  $sin(\pm \infty)$  returns NaN.

### Description

Calculate the sine of the input argument x (measured in radians).



#### Note:

For accuracy information for this function see the CUDA C++ Programming Guide, Appendix E.1, Table 7.

# \_\_device\_\_ void sincos (double x, double \*sptr, double \*cptr)

Calculate the sine and cosine of the first input argument.

#### Returns

none

### Description

Calculate the sine and cosine of the first input argument x (measured in radians). The results for sine and cosine are written into the second argument, sptr, and, respectively, third argument, cptr.

#### See also:

sin() and cos().



### Note:

For accuracy information for this function see the CUDA C++ Programming Guide, Appendix E.1, Table 7.

# \_\_device\_\_ void sincospi (double x, double \*sptr, double \*cptr)

Calculate the sine and cosine of the first input argument  $\times \pi$ .

### Returns

none

### Description

Calculate the sine and cosine of the first input argument, x (measured in radians), x  $\pi$ . The results for sine and cosine are written into the second argument, x and, respectively, third argument, x cptr.

#### See also:

sinpi() and cospi().



#### Note:

For accuracy information for this function see the CUDA C++ Programming Guide, Appendix E.1, Table 7.

# \_device\_\_ double sinh (double x)

Calculate the hyperbolic sine of the input argument.

#### Returns

- $\triangleright$  sinh(  $\pm 0$ ) returns  $\pm 0$ .
- ▶  $\sinh(\pm \infty)$  returns  $\pm \infty$ .

### Description

Calculate the hyperbolic sine of the input argument x.



### Note:

For accuracy information for this function see the CUDA C++ Programming Guide, Appendix E.1, Table 7.

# \_device\_\_ double sinpi (double x)

Calculate the sine of the input argument  $imes \pi$ .

### Returns

- $\triangleright$  sinpi(  $\pm 0$  ) returns  $\pm 0$ .
- ▶  $sinpi(\pm \infty)$  returns NaN.

### Description

Calculate the sine of  $x \times \pi$  (measured in radians), where x is the input argument.



#### Note:

For accuracy information for this function see the CUDA C++ Programming Guide, Appendix E.1, Table 7.

# \_\_device\_\_ double sqrt (double x)

Calculate the square root of the input argument.

### Returns

Returns  $\sqrt{x}$ .

- $\triangleright$  sqrt(  $\pm 0$  ) returns  $\pm 0$ .
- ▶  $sqrt(+\infty)$  returns  $+\infty$ .
- $\triangleright$  sqrt(x) returns NaN if x is less than 0.

# Description

Calculate the nonnegative square root of x,  $\sqrt{x}$ .



### Note:

For accuracy information for this function see the CUDA C++ Programming Guide, Appendix E.1, Table 7.

# \_device\_\_ double tan (double x)

Calculate the tangent of the input argument.

### Returns

- $\blacktriangleright$  tan(  $\pm 0$ ) returns  $\pm 0$ .
- ▶  $tan(\pm \infty)$  returns NaN.

### Description

Calculate the tangent of the input argument x (measured in radians).



#### Note:

For accuracy information for this function see the CUDA C++ Programming Guide, Appendix E.1, Table 7.

# \_\_device\_\_ double tanh (double x)

Calculate the hyperbolic tangent of the input argument.

### Returns

 $\blacktriangleright$  tanh(  $\pm 0$ ) returns  $\pm 0$ .

### Description

Calculate the hyperbolic tangent of the input argument x.



### Note:

For accuracy information for this function see the CUDA C++ Programming Guide, Appendix E.1. Table 7.

# \_device\_\_ double tgamma (double x)

Calculate the gamma function of the input argument.

#### Returns

- ▶ tgamma(  $\pm 0$ ) returns  $\pm \infty$ .
- tgamma(2) returns +1.
- ▶ tgamma(x) returns  $\pm \infty$  if the correctly calculated value is outside the double floating-point range.

- tgamma(x) returns NaN if x < 0 and x is an integer.
- ▶ tgamma( $-\infty$ ) returns NaN.
- ▶ tgamma(  $+ \infty$ ) returns  $+ \infty$ .

Calculate the gamma function of the input argument x, namely the value of  $\int_{0}^{\infty} e^{-t}t^{x-1}dt$ .



### Note:

For accuracy information for this function see the CUDA C++ Programming Guide, Appendix E.1, Table 7.

# \_\_device\_\_ double trunc (double x)

Truncate input argument to the integral part.

### Returns

Returns truncated integer value.

### Description

Round x to the nearest integer value that does not exceed x in magnitude.

# \_\_device\_\_ double y0 (double x)

Calculate the value of the Bessel function of the second kind of order 0 for the input argument.

### Returns

Returns the value of the Bessel function of the second kind of order 0.

- ▶ y0(0) returns  $-\infty$ .
- $\triangleright$  y0(x) returns NaN for x < 0.
- ▶  $y0(+\infty)$  returns +0.
- ▶ y0(NaN) returns NaN.

## Description

Calculate the value of the Bessel function of the second kind of order 0 for the input argument x,  $Y_0(x)$ .



#### Note:



For accuracy information for this function see the CUDA C++ Programming Guide, Appendix E.1, Table 7.

# \_device\_\_ double y1 (double x)

Calculate the value of the Bessel function of the second kind of order 1 for the input argument.

### Returns

Returns the value of the Bessel function of the second kind of order 1.

- ▶ y1(0) returns  $-\infty$ .
- $\triangleright$  y1(x) returns NaN for x < 0.
- ▶  $y1(+\infty)$  returns +0.
- ▶ y1(NaN) returns NaN.

### Description

Calculate the value of the Bessel function of the second kind of order 1 for the input argument x,  $Y_1(x)$ .



#### Note:

For accuracy information for this function see the CUDA C++ Programming Guide, Appendix E.1, Table 7.

# \_\_device\_\_\_ double yn (int n, double x)

Calculate the value of the Bessel function of the second kind of order n for the input argument.

### Returns

Returns the value of the Bessel function of the second kind of order n.

- $\triangleright$  yn(n, x) returns NaN for n < 0.
- $\triangleright$  yn(n, 0) returns  $-\infty$ .
- $\triangleright$  yn(n, x) returns NaN for x < 0.
- ▶  $yn(n, +\infty)$  returns +0.
- yn(n, NaN) returns NaN.

### Description

Calculate the value of the Bessel function of the second kind of order n for the input argument x,  $Y_n(x)$ .



#### Note:

For accuracy information for this function see the CUDA C++ Programming Guide, Appendix E.1, Table 7.

# 1.6. Integer Mathematical Functions

This section describes integer mathematical functions. To use these functions you do not need to include any additional header files in your program.

# \_\_device\_\_ int abs (int a)

Calculate the absolute value of the input int argument.

### Description

Calculate the absolute value of the input argument a.

# \_\_device\_\_ long int labs (long int a)

Calculate the absolute value of the input long int argument.

### Description

Calculate the absolute value of the input argument a.

# \_\_device\_\_ long long int llabs (long long int a)

Calculate the absolute value of the input long long int argument.

# Description

Calculate the absolute value of the input argument a.

# \_\_device\_\_ long long int llmax (const long long int a, const long long int b)

Calculate the maximum value of the input long long int arguments.

# Description

Calculate the maximum value of the arguments a and b.

# \_\_device\_\_ long long int llmin (const long long int a, const long long int b)

Calculate the minimum value of the input long long int arguments.

### Description

Calculate the minimum value of the arguments a and b.

# \_\_device\_\_ unsigned long long int max (const unsigned long long int a, const long long int b)

Calculate the maximum value of the input unsigned long long int and long long int arguments.

### Description

Calculate the maximum value of the arguments a and b, perform integer promotion first.

# \_\_device\_\_ unsigned long long int max (const long long int a, const unsigned long long int b)

Calculate the maximum value of the input long long int and unsigned long long int arguments.

# Description

Calculate the maximum value of the arguments a and b, perform integer promotion first.

# \_\_device\_\_ unsigned long long int max (const unsigned long long int a, const unsigned long long int b)

Calculate the maximum value of the input unsigned long long int arguments.

## Description

Calculate the maximum value of the arguments a and b.

# \_\_device\_\_ long long int max (const long long int a, const long long int b)

Calculate the maximum value of the input long long int arguments.

### Description

Calculate the maximum value of the arguments a and b.

# \_\_device\_\_ unsigned long int max (const unsigned long int a, const long int b)

Calculate the maximum value of the input unsigned long int and long int arguments.

### Description

Calculate the maximum value of the arguments a and b, perform integer promotion first.

# \_\_device\_\_ unsigned long int max (const long int a, const unsigned long int b)

Calculate the maximum value of the input long int and unsigned long int arguments.

# Description

Calculate the maximum value of the arguments a and b, perform integer promotion first.

# \_\_device\_\_ unsigned long int max (const unsigned long int a, const unsigned long int b)

Calculate the maximum value of the input unsigned long int arguments.

# Description

Calculate the maximum value of the arguments a and b.

# \_\_device\_\_ long int max (const long int a, const long int b)

Calculate the maximum value of the input long int arguments.

# Description

Calculate the maximum value of the arguments a and b.

# \_\_device\_\_ unsigned int max (const unsigned int a, const int b)

Calculate the maximum value of the input unsigned int and int arguments.

### Description

Calculate the maximum value of the arguments a and b, perform integer promotion first.

# \_\_device\_\_ unsigned int max (const int a, const unsigned int b)

Calculate the maximum value of the input int and unsigned int arguments.

### Description

Calculate the maximum value of the arguments a and b, perform integer promotion first.

# \_\_device\_\_ unsigned int max (const unsigned int a, const unsigned int b)

Calculate the maximum value of the input unsigned int arguments.

# Description

Calculate the maximum value of the arguments a and b.

# \_\_device\_\_ int max (const int a, const int b)

Calculate the maximum value of the input int arguments.

# Description

Calculate the maximum value of the arguments a and b.

# \_\_device\_\_ unsigned long long int min (const unsigned long long int a, const long long int b)

Calculate the minimum value of the input unsigned long long int and long long int arguments.

### Description

Calculate the minimum value of the arguments a and b, perform integer promotion first.

# \_\_device\_\_ unsigned long long int min (const long long int a, const unsigned long long int b)

Calculate the minimum value of the input long long int and unsigned long long int arguments.

### Description

Calculate the minimum value of the arguments a and b, perform integer promotion first.

\_\_device\_\_ unsigned long long int min (const unsigned long long int a, const unsigned long long int b)

Calculate the minimum value of the input unsigned long long int arguments.

### Description

Calculate the minimum value of the arguments a and b.

\_\_device\_\_ long long int min (const long long int a, const long long int b)

Calculate the minimum value of the input long long int arguments.

# Description

Calculate the minimum value of the arguments a and b.

\_\_device\_\_ unsigned long int min (const unsigned long int a, const long int b)

Calculate the minimum value of the input unsigned long int and long int arguments.

# Description

Calculate the minimum value of the arguments a and b, perform integer promotion first.

# \_\_device\_\_ unsigned long int min (const long int a, const unsigned long int b)

Calculate the minimum value of the input long int and unsigned long int arguments.

### Description

Calculate the minimum value of the arguments a and b, perform integer promotion first.

# \_\_device\_\_ unsigned long int min (const unsigned long int a, const unsigned long int b)

Calculate the minimum value of the input unsigned long int arguments.

### Description

Calculate the minimum value of the arguments a and b.

# \_\_device\_\_ long int min (const long int a, const long int b)

Calculate the minimum value of the input long int arguments.

# Description

Calculate the minimum value of the arguments a and b.

# \_\_device\_\_ unsigned int min (const unsigned int a, const int b)

Calculate the minimum value of the input unsigned int and int arguments.

# Description

Calculate the minimum value of the arguments a and b, perform integer promotion first.

# \_\_device\_\_ unsigned int min (const int a, const unsigned int b)

Calculate the minimum value of the input int and unsigned int arguments.

### Description

Calculate the minimum value of the arguments a and b, perform integer promotion first.

# \_\_device\_\_ unsigned int min (const unsigned int a, const unsigned int b)

Calculate the minimum value of the input unsigned int arguments.

### Description

Calculate the minimum value of the arguments a and b.

\_\_device\_\_ int min (const int a, const int b)

Calculate the minimum value of the input int arguments.

### Description

Calculate the minimum value of the arguments a and b.

\_\_device\_\_ unsigned long long int ullmax (const unsigned long long int a, const unsigned long long int b)

Calculate the maximum value of the input unsigned long long int arguments.

### Description

Calculate the maximum value of the arguments a and b.

\_\_device\_\_ unsigned long long int ullmin (const unsigned long long int a, const unsigned long long int b)

Calculate the minimum value of the input unsigned long long int arguments.

# Description

Calculate the minimum value of the arguments a and b.

\_\_device\_\_ unsigned int umax (const unsigned int a, const unsigned int b)

Calculate the maximum value of the input unsigned int arguments.

### Description

Calculate the maximum value of the arguments a and b.

# \_\_device\_\_ unsigned int umin (const unsigned int a, const unsigned int b)

Calculate the minimum value of the input unsigned int arguments.

### Description

Calculate the minimum value of the arguments a and b.

# 1.7. Single Precision Intrinsics

This section describes single precision intrinsic functions that are only supported in device code. To use these functions you do not need to include any additional header files in your program.

# device float cosf (float x)

Calculate the fast approximate cosine of the input argument.

#### Returns

Returns the approximate cosine of x.

### Description

Calculate the fast approximate cosine of the input argument x, measured in radians.



### Note:

- For accuracy information for this function see the CUDA C++ Programming Guide, Appendix E.2, Table 9.
- Input and output in the denormal range is flushed to sign preserving 0.0.

# \_device\_\_float \_\_exp10f (float x)

Calculate the fast approximate base 10 exponential of the input argument.

#### Returns

Returns an approximation to  $10^x$ .

### Description

Calculate the fast approximate base 10 exponential of the input argument x,  $10^x$ .

### Note:

- For accuracy information for this function see the CUDA C++ Programming Guide, Appendix E.2, Table 9.
- Most input and output values around denormal range are flushed to sign preserving 0.0.

# \_device\_\_ float \_\_expf (float x)

Calculate the fast approximate base e exponential of the input argument.

#### Returns

Returns an approximation to  $e^x$ .

### Description

Calculate the fast approximate base e exponential of the input argument x,  $e^x$ .



### Note:

- For accuracy information for this function see the CUDA C++ Programming Guide, Appendix E.2, Table 9.
- Most input and output values around denormal range are flushed to sign preserving 0.0.

# \_\_device\_\_ float \_\_fadd\_rd (float x, float y)

Add two floating-point values in round-down mode.

### Returns

Returns x + y.

## Description

Compute the sum of x and y in round-down (to negative infinity) mode.



### Note:

- For accuracy information for this function see the CUDA C++ Programming Guide, Appendix E.1, Table 6.
- ▶ This operation will never be merged into a single multiply-add instruction.

# \_device\_\_ float \_\_fadd\_rn (float x, float y)

Add two floating-point values in round-to-nearest-even mode.

### Returns

Returns x + y.

### Description

Compute the sum of x and y in round-to-nearest-even rounding mode.



### Note:

- For accuracy information for this function see the CUDA C++ Programming Guide, Appendix E.1, Table 6.
- ▶ This operation will never be merged into a single multiply-add instruction.

# \_device\_\_ float \_\_fadd\_ru (float x, float y)

Add two floating-point values in round-up mode.

### Returns

Returns x + y.

### Description

Compute the sum of x and y in round-up (to positive infinity) mode.



#### Note:

- For accuracy information for this function see the CUDA C++ Programming Guide, Appendix E.1, Table 6.
- This operation will never be merged into a single multiply-add instruction.

# \_\_device\_\_\_ float \_\_\_fadd\_rz (float x, float y)

Add two floating-point values in round-towards-zero mode.

#### Returns

Returns x + y.

Compute the sum of x and y in round-towards-zero mode.



#### Note:

- For accuracy information for this function see the CUDA C++ Programming Guide, Appendix E.1, Table 6.
- This operation will never be merged into a single multiply-add instruction.

# \_\_\_device\_\_\_ float \_\_\_fdiv\_rd (float x, float y)

Divide two floating-point values in round-down mode.

### Returns

Returns x / y.

### Description

Divide two floating-point values x by y in round-down (to negative infinity) mode.



#### Note:

For accuracy information for this function see the CUDA C++ Programming Guide, Appendix E.1, Table 6.

# \_\_device\_\_ float \_\_fdiv\_rn (float x, float y)

Divide two floating-point values in round-to-nearest-even mode.

#### Returns

Returns x / y.

### Description

Divide two floating-point values x by y in round-to-nearest-even mode.



### Note:

For accuracy information for this function see the CUDA C++ Programming Guide, Appendix E.1, Table 6.

# \_device\_\_ float \_\_fdiv\_ru (float x, float y)

Divide two floating-point values in round-up mode.

### Returns

Returns x / y.

### Description

Divide two floating-point values x by y in round-up (to positive infinity) mode.



#### Note:

For accuracy information for this function see the CUDA C++ Programming Guide, Appendix E.1, Table 6.

# \_\_device\_\_ float \_\_fdiv\_rz (float x, float y)

Divide two floating-point values in round-towards-zero mode.

#### Returns

Returns x / y.

### Description

Divide two floating-point values x by y in round-towards-zero mode.



#### Note:

For accuracy information for this function see the CUDA C++ Programming Guide, Appendix E.1. Table 6.

# \_\_device\_\_ float \_\_fdividef (float x, float y)

Calculate the fast approximate division of the input arguments.

#### Returns

Returns x / y.

- fdividef( $\infty$ , y) returns NaN for  $2^{126} < |y| < 2^{128}$ .
- fdividef(x, y) returns 0 for  $2^{126} < |y| < 2^{128}$  and finite x.

Calculate the fast approximate division of x by y.



#### Note:

For accuracy information for this function see the CUDA C++ Programming Guide, Appendix E.2, Table 9.

# \_\_device\_\_ float \_\_fmaf\_ieee\_rd (float x, float y, float z)

Compute fused multiply-add operation in round-down mode, ignore -ftz=true compiler flag.

### Description

Behavior is the same as  $\underline{\text{fmaf rd}}(x, y, z)$ , the difference is in handling denormalized inputs and outputs: -ftz compiler flag has no effect.

Compute fused multiply-add operation in round-to-nearest-even mode, ignore -ftz=true compiler flag.

# Description

Behavior is the same as  $\underline{fmaf\ rn}(x, y, z)$ , the difference is in handling denormalized inputs and outputs: -ftz compiler flag has no effect.

Compute fused multiply-add operation in round-up mode, ignore -ftz=true compiler flag.

### Description

Behavior is the same as  $\underline{\text{fmaf}_{ru}}(x, y, z)$ , the difference is in handling denormalized inputs and outputs: -ftz compiler flag has no effect.

# \_\_device\_\_ float \_\_fmaf\_ieee\_rz (float x, float y, float z)

Compute fused multiply-add operation in round-towards-zero mode, ignore -ftz=true compiler flag.

### Description

Behavior is the same as  $\underline{\text{fmaf rz}}(x, y, z)$ , the difference is in handling denormalized inputs and outputs: -ftz compiler flag has no effect.

# \_\_device\_\_ float \_\_fmaf\_rd (float x, float y, float z)

Compute  $x \times y + z$  as a single operation, in round-down mode.

### Returns

Returns the rounded value of  $x \times y + z$  as a single operation.

- ► fmaf(  $\pm \infty$ ,  $\pm 0$ , z) returns NaN.
- ▶ fmaf(  $\pm 0$ ,  $\pm \infty$ , z) returns NaN.
- ▶ fmaf(x, y,  $-\infty$ ) returns NaN if  $x \times y$  is an exact  $+\infty$ .
- ▶ fmaf(x, y, +∞) returns NaN if  $x \times y$  is an exact -∞.

## Description

Computes the value of  $x \times y + z$  as a single ternary operation, rounding the result once in round-down (to negative infinity) mode.



#### Note:

For accuracy information for this function see the CUDA C++ Programming Guide, Appendix E.1, Table 6.

# \_device\_\_ float \_ fmaf\_rn (float x, float y, float z)

Compute  $x \times y + z$  as a single operation, in round-to-nearest-even mode.

### Returns

Returns the rounded value of  $x \times y + z$  as a single operation.

- ▶ fmaf(  $\pm \infty$ ,  $\pm 0$ , z) returns NaN.
- ▶ fmaf(  $\pm 0$ ,  $\pm \infty$ , z) returns NaN.
- ▶ fmaf(x, y,  $-\infty$ ) returns NaN if  $x \times y$  is an exact  $+\infty$ .
- ▶ fmaf(x, y, +∞) returns NaN if  $x \times y$  is an exact -∞.

Computes the value of  $x \times y + z$  as a single ternary operation, rounding the result once in round-to-nearest-even mode.



#### Note:

For accuracy information for this function see the CUDA C++ Programming Guide, Appendix E.1, Table 6.

# \_\_device\_\_ float \_\_fmaf\_ru (float x, float y, float z)

Compute  $x \times y + z$  as a single operation, in round-up mode.

#### Returns

Returns the rounded value of  $x \times y + z$  as a single operation.

- ▶ fmaf(  $\pm \infty$ ,  $\pm 0$ , z) returns NaN.
- ▶ fmaf(  $\pm 0$ ,  $\pm \infty$ , z) returns NaN.
- ▶ fmaf(x, y,  $-\infty$ ) returns NaN if  $x \times y$  is an exact  $+\infty$ .
- ▶ fmaf(x, y, +∞) returns NaN if  $x \times y$  is an exact -∞.

### Description

Computes the value of  $x \times y + z$  as a single ternary operation, rounding the result once in round-up (to positive infinity) mode.



### Note:

For accuracy information for this function see the CUDA C++ Programming Guide, Appendix E.1, Table 6.

# \_device\_\_ float \_\_fmaf\_rz (float x, float y, float z)

Compute  $x \times y + z$  as a single operation, in round-towards-zero mode.

### Returns

Returns the rounded value of  $x \times y + z$  as a single operation.

- ▶ fmaf(  $\pm \infty$ ,  $\pm 0$ , z) returns NaN.
- ▶ fmaf(  $\pm 0$ ,  $\pm \infty$ , z) returns NaN.
- ▶ fmaf(x, y,  $-\infty$ ) returns NaN if  $x \times y$  is an exact  $+\infty$ .
- ▶ fmaf(x, y, +∞) returns NaN if  $x \times y$  is an exact -∞.

Computes the value of  $x \times y + z$  as a single ternary operation, rounding the result once in round-towards-zero mode.



#### Note:

For accuracy information for this function see the CUDA C++ Programming Guide, Appendix E.1, Table 6.

# \_\_device\_\_ float \_\_fmul\_rd (float x, float y)

Multiply two floating-point values in round-down mode.

#### Returns

Returns x \* y.

### Description

Compute the product of x and y in round-down (to negative infinity) mode.



### Note:

- For accuracy information for this function see the CUDA C++ Programming Guide, Appendix E.1, Table 6.
- This operation will never be merged into a single multiply-add instruction.

# \_\_device\_\_ float \_\_fmul\_rn (float x, float y)

Multiply two floating-point values in round-to-nearest-even mode.

#### Returns

Returns x \* y.

### Description

Compute the product of x and y in round-to-nearest-even mode.



#### Note:

For accuracy information for this function see the CUDA C++ Programming Guide, Appendix E.1, Table 6.

This operation will never be merged into a single multiply-add instruction.

# \_\_device\_\_\_ float \_\_\_fmul\_ru (float x, float y)

Multiply two floating-point values in round-up mode.

### Returns

Returns x \* y.

### Description

Compute the product of x and y in round-up (to positive infinity) mode.



#### Note:

- For accuracy information for this function see the CUDA C++ Programming Guide, Appendix E.1, Table 6.
- ▶ This operation will never be merged into a single multiply-add instruction.

# \_\_device\_\_ float \_\_fmul\_rz (float x, float y)

Multiply two floating-point values in round-towards-zero mode.

#### Returns

Returns x \* y.

# Description

Compute the product of  ${\bf x}$  and  ${\bf y}$  in round-towards-zero mode.



### Note:

- For accuracy information for this function see the CUDA C++ Programming Guide, Appendix E.1, Table 6.
- This operation will never be merged into a single multiply-add instruction.

# \_\_device\_\_ float \_\_frcp\_rd (float x)

Compute  $\frac{1}{X}$  in round-down mode.

### Returns

Returns  $\frac{1}{x}$ .

Compute the reciprocal of x in round-down (to negative infinity) mode.



#### Note:

For accuracy information for this function see the CUDA C++ Programming Guide, Appendix E.1, Table 6.

# \_\_device\_\_ float \_\_frcp\_rn (float x)

Compute  $\frac{1}{X}$  in round-to-nearest-even mode.

### Returns

Returns  $\frac{1}{X}$ .

### Description

Compute the reciprocal of  $\mathbf{x}$  in round-to-nearest-even mode.



#### Note:

For accuracy information for this function see the CUDA C++ Programming Guide, Appendix E.1, Table 6.

# \_\_device\_\_ float \_\_frcp\_ru (float x)

Compute  $\frac{1}{X}$  in round-up mode.

### Returns

Returns  $\frac{1}{x}$ .

### Description

Compute the reciprocal of x in round-up (to positive infinity) mode.



### Note:

For accuracy information for this function see the CUDA C++ Programming Guide, Appendix E.1, Table 6.

\_\_device\_\_ float \_\_frcp\_rz (float x)

Compute  $\frac{1}{X}$  in round-towards-zero mode.

### Returns

Returns  $\frac{1}{X}$ .

# Description

Compute the reciprocal of x in round-towards-zero mode.



#### Note:

For accuracy information for this function see the CUDA C++ Programming Guide, Appendix E.1, Table 6.

\_\_device\_\_ float \_\_frsqrt\_rn (float x)

Compute  $1/\sqrt{x}$  in round-to-nearest-even mode.

### Returns

Returns  $1/\sqrt{x}$ .

# Description

Compute the reciprocal square root of x in round-to-nearest-even mode.



### Note:

For accuracy information for this function see the CUDA C++ Programming Guide, Appendix E.1, Table 6.

\_\_device\_\_ float \_\_fsqrt\_rd (float x)

Compute  $\sqrt{x}$  in round-down mode.

### Returns

Returns  $\sqrt{x}$ .

Compute the square root of x in round-down (to negative infinity) mode.



#### Note:

For accuracy information for this function see the CUDA C++ Programming Guide, Appendix E.1, Table 6.

# \_\_device\_\_ float \_\_fsqrt\_rn (float x)

Compute  $\sqrt{x}$  in round-to-nearest-even mode.

### Returns

Returns  $\sqrt{x}$ .

### Description

Compute the square root of x in round-to-nearest-even mode.



#### Note:

For accuracy information for this function see the CUDA C++ Programming Guide, Appendix E.1, Table 6.

# \_\_device\_\_ float \_\_fsqrt\_ru (float x)

Compute  $\sqrt{x}$  in round-up mode.

### Returns

Returns  $\sqrt{x}$ .

### Description

Compute the square root of x in round-up (to positive infinity) mode.



### Note:

For accuracy information for this function see the CUDA C++ Programming Guide, Appendix E.1, Table 6.

# \_\_device\_\_ float \_\_fsqrt\_rz (float x)

Compute  $\sqrt{x}$  in round-towards-zero mode.

#### Returns

Returns  $\sqrt{x}$ .

### Description

Compute the square root of x in round-towards-zero mode.



### Note:

For accuracy information for this function see the CUDA C++ Programming Guide, Appendix E.1, Table 6.

# \_\_device\_\_\_ float \_\_\_fsub\_\_rd (float x, float y)

Subtract two floating-point values in round-down mode.

### Returns

Returns x - y.

### Description

Compute the difference of x and y in round-down (to negative infinity) mode.



### Note:

- For accuracy information for this function see the CUDA C++ Programming Guide, Appendix E.1, Table 6.
- ▶ This operation will never be merged into a single multiply-add instruction.

# \_\_device\_\_ float \_\_fsub\_rn (float x, float y)

Subtract two floating-point values in round-to-nearest-even mode.

### Returns

Returns x - y.

# Description

Compute the difference of x and y in round-to-nearest-even rounding mode.

### Note:

- For accuracy information for this function see the CUDA C++ Programming Guide, Appendix E.1, Table 6.
- ▶ This operation will never be merged into a single multiply-add instruction.

# \_\_device\_\_ float \_\_fsub\_ru (float x, float y)

Subtract two floating-point values in round-up mode.

#### Returns

Returns x - y.

### Description

Compute the difference of x and y in round-up (to positive infinity) mode.



### Note:

- For accuracy information for this function see the CUDA C++ Programming Guide, Appendix E.1, Table 6.
- This operation will never be merged into a single multiply-add instruction.

# \_\_device\_\_ float \_\_fsub\_rz (float x, float y)

Subtract two floating-point values in round-towards-zero mode.

### Returns

Returns x - y.

# Description

Compute the difference of x and y in round-towards-zero mode.



### Note:

- For accuracy information for this function see the CUDA C++ Programming Guide, Appendix E.1, Table 6.
- This operation will never be merged into a single multiply-add instruction.

# \_device\_\_ float \_\_\_log10f (float x)

Calculate the fast approximate base 10 logarithm of the input argument.

### Returns

Returns an approximation to  $\log_{10}(x)$ .

### Description

Calculate the fast approximate base  $10 \log \operatorname{arithm}$  of the input argument x.



#### Note:

- For accuracy information for this function see the CUDA C++ Programming Guide, Appendix E.2, Table 9.
- Most input and output values around denormal range are flushed to sign preserving 0.0.

# \_\_device\_\_ float \_\_log2f (float x)

Calculate the fast approximate base 2 logarithm of the input argument.

#### Returns

Returns an approximation to  $\log_2(x)$ .

# Description

Calculate the fast approximate base 2 logarithm of the input argument x.



#### Note:

- For accuracy information for this function see the CUDA C++ Programming Guide, Appendix E.2, Table 9.
- Input and output in the denormal range is flushed to sign preserving 0.0.

# device float logf (float x)

Calculate the fast approximate base e logarithm of the input argument.

#### Returns

Returns an approximation to  $\log_{\rho}(x)$ .

Calculate the fast approximate base e logarithm of the input argument x.



#### Note:

- For accuracy information for this function see the CUDA C++ Programming Guide, Appendix E.2, Table 9.
- Most input and output values around denormal range are flushed to sign preserving 0.0.

# \_\_\_device\_\_\_ float \_\_\_powf (float x, float y)

Calculate the fast approximate of  $x^{y}$ .

### Returns

Returns an approximation to  $x^{y}$ .

### Description

Calculate the fast approximate of x, the first input argument, raised to the power of y, the second input argument,  $x^y$ .



#### Note:

- For accuracy information for this function see the CUDA C++ Programming Guide, Appendix E.2, Table 9.
- Most input and output values around denormal range are flushed to sign preserving 0.0.

# \_device\_\_ float \_\_saturatef (float x)

Clamp the input argument to [+0.0, 1.0].

### Returns

- saturatef(x) returns 0 if x < 0.
- ightharpoonup \_\_saturatef(x) returns 1 if x > 1.
- saturatef(x) returns x if  $0 \le x \le 1$ .
- \_\_saturatef(NaN) returns 0.

### Description

Clamp the input argument x to be within the interval [+0.0, 1.0].

# \_\_device\_\_ void \_\_sincosf (float x, float \*sptr, float \*cptr)

Calculate the fast approximate of sine and cosine of the first input argument.

#### Returns

none

### Description

Calculate the fast approximate of sine and cosine of the first input argument x (measured in radians). The results for sine and cosine are written into the second argument, sptr, and, respectively, third argument, cptr.



#### Note:

- For accuracy information for this function see the CUDA C++ Programming Guide, Appendix E.2, Table 9.
- Denorm input/output is flushed to sign preserving 0.0.

# \_\_device\_\_ float \_\_sinf (float x)

Calculate the fast approximate sine of the input argument.

### Returns

Returns the approximate sine of x.

### Description

Calculate the fast approximate sine of the input argument x, measured in radians.



#### Note:

- For accuracy information for this function see the CUDA C++ Programming Guide, Appendix E.2, Table 9.
- Input and output in the denormal range is flushed to sign preserving 0.0.

# device float tanf (float x)

Calculate the fast approximate tangent of the input argument.

### Returns

Returns the approximate tangent of x.

### Description

Calculate the fast approximate tangent of the input argument x, measured in radians.



#### Note:

- For accuracy information for this function see the CUDA C++ Programming Guide, Appendix E.2, Table 9.
- The result is computed as the fast divide of <u>sinf()</u> by <u>cosf()</u>. Denormal input and output are flushed to sign-preserving 0.0 at each step of the computation.

# 1.8. Double Precision Intrinsics

This section describes double precision intrinsic functions that are only supported in device code. To use these functions you do not need to include any additional header files in your program.

\_\_device\_\_ double \_\_dadd\_rd (double x, double y)

Add two floating-point values in round-down mode.

#### Returns

Returns x + y.

### Description

Adds two floating-point values x and y in round-down (to negative infinity) mode.



### Note:

- For accuracy information for this function see the CUDA C++ Programming Guide, Appendix E.1, Table 7.
- This operation will never be merged into a single multiply-add instruction.

# \_\_device\_\_\_ double \_\_\_dadd\_rn (double x, double y)

Add two floating-point values in round-to-nearest-even mode.

### Returns

Returns x + y.

### Description

Adds two floating-point values x and y in round-to-nearest-even mode.



### Note:

- For accuracy information for this function see the CUDA C++ Programming Guide, Appendix E.1, Table 7.
- ▶ This operation will never be merged into a single multiply-add instruction.

# \_device\_\_ double \_\_dadd\_ru (double x, double y)

Add two floating-point values in round-up mode.

### Returns

Returns x + y.

### Description

Adds two floating-point values x and y in round-up (to positive infinity) mode.



#### Note:

- For accuracy information for this function see the CUDA C++ Programming Guide, Appendix E.1, Table 7.
- ▶ This operation will never be merged into a single multiply-add instruction.

# \_device\_\_\_ double \_\_\_dadd\_rz (double x, double y)

Add two floating-point values in round-towards-zero mode.

#### Returns

Returns x + y.

Adds two floating-point values x and y in round-towards-zero mode.



#### Note:

- For accuracy information for this function see the CUDA C++ Programming Guide, Appendix E.1, Table 7.
- ▶ This operation will never be merged into a single multiply-add instruction.

# \_device\_\_\_ double \_\_\_ddiv\_rd (double x, double y)

Divide two floating-point values in round-down mode.

### Returns

Returns x / y.

### Description

Divides two floating-point values x by y in round-down (to negative infinity) mode.



#### Note:

- For accuracy information for this function see the CUDA C++ Programming Guide, Appendix E.1, Table 7.
- Requires compute capability >= 2.0.

# \_\_device\_\_ double \_\_ddiv\_rn (double x, double y)

Divide two floating-point values in round-to-nearest-even mode.

#### Returns

Returns x/y.

### Description

Divides two floating-point values x by y in round-to-nearest-even mode.



#### Note:

For accuracy information for this function see the CUDA C++ Programming Guide, Appendix E.1, Table 7.



Requires compute capability >= 2.0.

# \_device\_\_ double \_\_ddiv\_ru (double x, double y)

Divide two floating-point values in round-up mode.

### Returns

Returns x / y.

### Description

Divides two floating-point values x by y in round-up (to positive infinity) mode.



#### Note:

- For accuracy information for this function see the CUDA C++ Programming Guide, Appendix E.1, Table 7.
- ► Requires compute capability >= 2.0.

# \_\_device\_\_ double \_\_ddiv\_rz (double x, double y)

Divide two floating-point values in round-towards-zero mode.

#### Returns

Returns x / y.

### Description

Divides two floating-point values x by y in round-towards-zero mode.



### Note:

- For accuracy information for this function see the CUDA C++ Programming Guide, Appendix E.1, Table 7.
- ► Requires compute capability >= 2.0.

# \_device\_\_\_ double \_\_\_dmul\_rd (double x, double y)

Multiply two floating-point values in round-down mode.

#### Returns

Returns x \* y.

Multiplies two floating-point values x and y in round-down (to negative infinity) mode.



#### Note:

- For accuracy information for this function see the CUDA C++ Programming Guide, Appendix E.1, Table 7.
- This operation will never be merged into a single multiply-add instruction.

# \_device\_\_\_ double \_\_\_dmul\_rn (double x, double y)

Multiply two floating-point values in round-to-nearest-even mode.

#### Returns

Returns x \* y.

## Description

Multiplies two floating-point values x and y in round-to-nearest-even mode.



#### Note:

- For accuracy information for this function see the CUDA C++ Programming Guide, Appendix E.1, Table 7.
- ▶ This operation will never be merged into a single multiply-add instruction.

# \_\_device\_\_\_ double \_\_\_dmul\_ru (double x, double y)

Multiply two floating-point values in round-up mode.

#### Returns

Returns x \* y.

### Description

Multiplies two floating-point values x and y in round-up (to positive infinity) mode.



#### Note:

For accuracy information for this function see the CUDA C++ Programming Guide, Appendix E.1, Table 7.



This operation will never be merged into a single multiply-add instruction.

# \_device\_\_ double \_\_dmul\_rz (double x, double y)

Multiply two floating-point values in round-towards-zero mode.

#### Returns

Returns x \* y.

## Description

Multiplies two floating-point values x and y in round-towards-zero mode.



#### Note:

- For accuracy information for this function see the CUDA C++ Programming Guide, Appendix E.1, Table 7.
- This operation will never be merged into a single multiply-add instruction.

# \_\_device\_\_ double \_\_drcp\_rd (double x)

Compute  $\frac{1}{X}$  in round-down mode.

#### Returns

Returns  $\frac{1}{X}$ .

## Description

Compute the reciprocal of x in round-down (to negative infinity) mode.



#### Note:

- For accuracy information for this function see the CUDA C++ Programming Guide, Appendix E.1, Table 7.
- ► Requires compute capability >= 2.0.

# \_\_device\_\_ double \_\_drcp\_rn (double x)

Compute  $\frac{1}{X}$  in round-to-nearest-even mode.

#### Returns

Returns  $\frac{1}{X}$ .

## Description

Compute the reciprocal of x in round-to-nearest-even mode.



#### Note:

- For accuracy information for this function see the CUDA C++ Programming Guide, Appendix E.1, Table 7.
- ► Requires compute capability >= 2.0.

# \_\_device\_\_ double \_\_drcp\_ru (double x)

Compute  $\frac{1}{X}$  in round-up mode.

#### Returns

Returns  $\frac{1}{x}$ .

## Description

Compute the reciprocal of x in round-up (to positive infinity) mode.



### Note:

- For accuracy information for this function see the CUDA C++ Programming Guide, Appendix E.1, Table 7.
- Requires compute capability >= 2.0.

# \_\_device\_\_ double \_\_drcp\_rz (double x)

Compute  $\frac{1}{X}$  in round-towards-zero mode.

#### Returns

Returns  $\frac{1}{x}$ .

Compute the reciprocal of x in round-towards-zero mode.



#### Note:

- For accuracy information for this function see the CUDA C++ Programming Guide, Appendix E.1, Table 7.
- Requires compute capability >= 2.0.

# \_\_device\_\_ double \_\_dsqrt\_rd (double x)

Compute  $\sqrt{x}$  in round-down mode.

#### Returns

Returns  $\sqrt{x}$ .

## Description

Compute the square root of x in round-down (to negative infinity) mode.



#### Note:

- For accuracy information for this function see the CUDA C++ Programming Guide, Appendix E.1, Table 7.
- ► Requires compute capability >= 2.0.

# \_\_\_device\_\_ double \_\_\_dsqrt\_rn (double x)

Compute  $\sqrt{x}$  in round-to-nearest-even mode.

#### Returns

Returns  $\sqrt{x}$ .

### Description

Compute the square root of x in round-to-nearest-even mode.



#### Note:



- For accuracy information for this function see the CUDA C++ Programming Guide, Appendix E.1, Table 7.
- ► Requires compute capability >= 2.0.

# \_\_\_device\_\_ double \_\_\_dsqrt\_ru (double x)

Compute  $\sqrt{x}$  in round-up mode.

#### Returns

Returns  $\sqrt{x}$ .

## Description

Compute the square root of x in round-up (to positive infinity) mode.



#### Note:

- For accuracy information for this function see the CUDA C++ Programming Guide, Appendix E.1, Table 7.
- Requires compute capability >= 2.0.

# \_\_device\_\_ double \_\_\_dsqrt\_rz (double x)

Compute  $\sqrt{x}$  in round-towards-zero mode.

#### Returns

Returns  $\sqrt{x}$ .

## Description

Compute the square root of x in round-towards-zero mode.



#### Note:

- For accuracy information for this function see the CUDA C++ Programming Guide, Appendix E.1, Table 7.
- Requires compute capability >= 2.0.

# \_device\_\_ double \_\_dsub\_rd (double x, double y)

Subtract two floating-point values in round-down mode.

#### Returns

Returns x - y.

## Description

Subtracts two floating-point values x and y in round-down (to negative infinity) mode.



#### Note:

- For accuracy information for this function see the CUDA C++ Programming Guide, Appendix E.1, Table 7.
- ▶ This operation will never be merged into a single multiply-add instruction.

# \_device\_\_ double \_\_dsub\_rn (double x, double y)

Subtract two floating-point values in round-to-nearest-even mode.

#### Returns

Returns x - y.

## Description

Subtracts two floating-point values x and y in round-to-nearest-even mode.



#### Note:

- For accuracy information for this function see the CUDA C++ Programming Guide, Appendix E.1, Table 7.
- ▶ This operation will never be merged into a single multiply-add instruction.

# \_device\_\_ double \_\_dsub\_ru (double x, double y)

Subtract two floating-point values in round-up mode.

#### Returns

Returns x - y.

Subtracts two floating-point values x and y in round-up (to positive infinity) mode.



#### Note:

- For accuracy information for this function see the CUDA C++ Programming Guide, Appendix E.1, Table 7.
- This operation will never be merged into a single multiply-add instruction.

# \_\_device\_\_ double \_\_dsub\_rz (double x, double y)

Subtract two floating-point values in round-towards-zero mode.

#### Returns

Returns x - y.

## Description

Subtracts two floating-point values x and y in round-towards-zero mode.



#### Note:

- For accuracy information for this function see the CUDA C++ Programming Guide, Appendix E.1, Table 7.
- ▶ This operation will never be merged into a single multiply-add instruction.

# \_\_device\_\_ double \_\_fma\_rd (double x, double y, double z)

Compute  $x \times y + z$  as a single operation in round-down mode.

#### Returns

Returns the rounded value of  $x \times y + z$  as a single operation.

- fmaf(  $\pm \infty$ ,  $\pm 0$ , z) returns NaN.
- ▶ fmaf(  $\pm 0$ ,  $\pm \infty$ , z) returns NaN.
- ▶ fmaf(x, y,  $-\infty$ ) returns NaN if  $x \times y$  is an exact  $+\infty$
- ▶ fmaf(x, y, +∞) returns NaN if  $x \times y$  is an exact -∞

Computes the value of  $x \times y + z$  as a single ternary operation, rounding the result once in round-down (to negative infinity) mode.



#### Note:

For accuracy information for this function see the CUDA C++ Programming Guide, Appendix E.1, Table 7.

# \_\_device\_\_ double \_\_fma\_rn (double x, double y, double z)

Compute  $x \times y + z$  as a single operation in round-to-nearest-even mode.

#### Returns

Returns the rounded value of  $x \times y + z$  as a single operation.

- fmaf(  $\pm \infty$ ,  $\pm 0$ , z) returns NaN.
- ▶ fmaf(  $\pm 0$ ,  $\pm \infty$ , z) returns NaN.
- ▶ fmaf(x, y,  $-\infty$ ) returns NaN if  $x \times y$  is an exact  $+\infty$
- fmaf(x, y,  $+\infty$ ) returns NaN if  $x \times y$  is an exact  $-\infty$

## Description

Computes the value of  $x \times y + z$  as a single ternary operation, rounding the result once in round-to-nearest-even mode.



#### Note:

For accuracy information for this function see the CUDA C++ Programming Guide, Appendix E.1, Table 7.

# \_\_device\_\_ double \_\_fma\_ru (double x, double y, double z)

Compute  $x \times y + z$  as a single operation in round-up mode.

#### Returns

Returns the rounded value of  $x \times y + z$  as a single operation.

• fmaf(  $\pm \infty$ ,  $\pm 0$ , z) returns NaN.

- ▶ fmaf(  $\pm 0$ ,  $\pm \infty$ , z) returns NaN.
- ▶ fmaf(x, y,  $-\infty$ ) returns NaN if  $x \times y$  is an exact  $+\infty$
- ▶ fmaf(x, y,  $+\infty$ ) returns NaN if  $x \times y$  is an exact  $-\infty$

Computes the value of  $x \times y + z$  as a single ternary operation, rounding the result once in round-up (to positive infinity) mode.



#### Note:

For accuracy information for this function see the CUDA C++ Programming Guide, Appendix E.1, Table 7.

# \_\_device\_\_ double \_\_fma\_rz (double x, double y, double z)

Compute  $x \times y + z$  as a single operation in round-towards-zero mode.

#### Returns

Returns the rounded value of  $x \times y + z$  as a single operation.

- fmaf(  $\pm \infty$ ,  $\pm 0$ , z) returns NaN.
- ▶ fmaf(  $\pm 0$ ,  $\pm \infty$ , z) returns NaN.
- ▶ fmaf(x, y,  $-\infty$ ) returns NaN if  $x \times y$  is an exact  $+\infty$
- fmaf(x, y,  $+\infty$ ) returns NaN if  $x \times y$  is an exact  $-\infty$

## Description

Computes the value of  $x \times y + z$  as a single ternary operation, rounding the result once in round-towards-zero mode.



#### Note:

For accuracy information for this function see the CUDA C++ Programming Guide, Appendix E.1, Table 7.

# 1.9. Integer Intrinsics

This section describes integer intrinsic functions that are only supported in device code. To use these functions you do not need to include any additional header files in your program.

# \_\_\_device\_\_ unsigned int \_\_\_brev (unsigned int x)

Reverse the bit order of a 32-bit unsigned integer.

#### Returns

Returns the bit-reversed value of x. i.e. bit N of the return value corresponds to bit 31-N of x.

## Description

Reverses the bit order of the 32-bit unsigned integer x.

# \_\_device\_\_ unsigned long long int \_\_brevll (unsigned long long int x)

Reverse the bit order of a 64-bit unsigned integer.

#### Returns

Returns the bit-reversed value of x. i.e. bit N of the return value corresponds to bit 63-N of x.

## Description

Reverses the bit order of the 64-bit unsigned integer x.

# \_\_device\_\_ unsigned int \_\_byte\_perm (unsigned int x, unsigned int y, unsigned int s)

Return selected bytes from two 32-bit unsigned integers.

#### Returns

The returned value r is computed to be: result[n] := input[selector[n]] where result[n] is the nth byte of r.

## Description

byte\_perm(x,y,s) returns a 32-bit integer consisting of four bytes from eight input bytes provided in the two input integers x and y, as specified by a selector, s.

The input bytes are indexed as follows: input[0] = x<7:0> input[1] = x<15:8> input[2] = x<23:16> input[3] = x<31:24> input[4] = y<7:0> input[5] = y<15:8> input[6] = y<23:16> input[7] = y<31:24> The selector indices are as follows (the upper 16-bits of the selector are not used): selector[0] = x<2:0> selector[1] = x<6:4> selector[2] = x<10:8> selector[3] = x<10:8

# \_\_\_device\_\_\_ int \_\_\_clz (int x)

Return the number of consecutive high-order zero bits in a 32-bit integer.

#### Returns

Returns a value between 0 and 32 inclusive representing the number of zero bits.

## Description

Count the number of consecutive leading zero bits, starting at the most significant bit (bit 31) of x.

# \_\_device\_\_ int \_\_clzll (long long int x)

Count the number of consecutive high-order zero bits in a 64-bit integer.

#### Returns

Returns a value between 0 and 64 inclusive representing the number of zero bits.

### Description

Count the number of consecutive leading zero bits, starting at the most significant bit (bit 63) of x.

# device int ffs (int x)

Find the position of the least significant bit set to 1 in a 32-bit integer.

#### Returns

Returns a value between 0 and 32 inclusive representing the position of the first bit set.

ffs(0) returns 0.

## Description

Find the position of the first (least significant) bit set to 1 in x, where the least significant bit position is 1.

# \_device\_\_ int \_\_ffsll (long long int x)

Find the position of the least significant bit set to 1 in a 64-bit integer.

#### Returns

Returns a value between 0 and 64 inclusive representing the position of the first bit set.

\_\_ffsll(0) returns 0.

Find the position of the first (least significant) bit set to 1 in x, where the least significant bit position is 1.

# \_\_device\_\_ unsigned int \_\_funnelshift\_l (unsigned int lo, unsigned int hi, unsigned int shift)

Concatenate hi: lo, shift left by shift & 31 bits, return the most significant 32 bits.

#### Returns

Returns the most significant 32 bits of the shifted 64-bit value.

## Description

Shift the 64-bit value formed by concatenating argument 10 and hi left by the amount specified by the argument shift. Argument 10 holds bits 31:0 and argument hi holds bits 63:32 of the 64-bit source value. The source is shifted left by the wrapped value of shift (shift & 31). The most significant 32-bits of the result are returned.

# \_\_device\_\_ unsigned int \_\_funnelshift\_lc (unsigned int lo, unsigned int hi, unsigned int shift)

Concatenate hi: lo, shift left by min(shift, 32) bits, return the most significant 32 bits.

#### Returns

Returns the most significant 32 bits of the shifted 64-bit value.

## Description

Shift the 64-bit value formed by concatenating argument 10 and hi left by the amount specified by the argument shift. Argument 10 holds bits 31:0 and argument hi holds bits 63:32 of the 64-bit source value. The source is shifted left by the clamped value of shift (min(shift, 32)). The most significant 32-bits of the result are returned.

# \_\_device\_\_ unsigned int \_\_funnelshift\_r (unsigned int lo, unsigned int hi, unsigned int shift)

Concatenate hi: lo, shift right by shift & 31 bits, return the least significant 32 bits.

#### Returns

Returns the least significant 32 bits of the shifted 64-bit value.

Shift the 64-bit value formed by concatenating argument 10 and hi right by the amount specified by the argument shift. Argument 10 holds bits 31:0 and argument hi holds bits 63:32 of the 64-bit source value. The source is shifted right by the wrapped value of shift (shift & 31). The least significant 32-bits of the result are returned.

# \_\_device\_\_ unsigned int \_\_funnelshift\_rc (unsigned int lo, unsigned int hi, unsigned int shift)

Concatenate hi: lo, shift right by min(shift, 32) bits, return the least significant 32 bits.

#### Returns

Returns the least significant 32 bits of the shifted 64-bit value.

## Description

Shift the 64-bit value formed by concatenating argument 10 and hi right by the amount specified by the argument shift. Argument 10 holds bits 31:0 and argument hi holds bits 63:32 of the 64-bit source value. The source is shifted right by the clamped value of shift (min(shift, 32)). The least significant 32-bits of the result are returned.

# \_\_device\_\_ int \_\_hadd (int, int)

Compute average of signed input arguments, avoiding overflow in the intermediate sum.

#### Returns

Returns a signed integer value representing the signed average value of the two inputs.

## Description

Compute average of signed input arguments x and y as (x + y) >> 1, avoiding overflow in the intermediate sum.

# \_\_device\_\_ int \_\_mul24 (int x, int y)

Calculate the least significant 32 bits of the product of the least significant 24 bits of two integers.

#### Returns

Returns the least significant 32 bits of the product x \* y.

Calculate the least significant 32 bits of the product of the least significant 24 bits of x and y. The high order 8 bits of x and y are ignored.

# \_\_device\_\_ long long int \_\_mul64hi (long long int x, long long int y)

Calculate the most significant 64 bits of the product of the two 64-bit integers.

#### Returns

Returns the most significant 64 bits of the product x \* y.

## Description

Calculate the most significant 64 bits of the 128-bit product x \* y, where x and y are 64-bit integers.

# \_\_device\_\_ int \_\_mulhi (int x, int y)

Calculate the most significant 32 bits of the product of the two 32-bit integers.

#### Returns

Returns the most significant 32 bits of the product x \* y.

## Description

Calculate the most significant 32 bits of the 64-bit product x \* y, where x and y are 32-bit integers.

# \_\_device\_\_ int \_\_popc (unsigned int x)

Count the number of bits that are set to 1 in a 32-bit integer.

#### Returns

Returns a value between 0 and 32 inclusive representing the number of set bits.

# Description

Count the number of bits that are set to 1 in x.

# \_\_device\_\_ int \_\_popcll (unsigned long long int x)

Count the number of bits that are set to 1 in a 64-bit integer.

#### Returns

Returns a value between 0 and 64 inclusive representing the number of set bits.

## Description

Count the number of bits that are set to 1 in x.

# \_\_\_device\_\_\_ int \_\_\_rhadd (int, int)

Compute rounded average of signed input arguments, avoiding overflow in the intermediate sum.

#### Returns

Returns a signed integer value representing the signed rounded average value of the two inputs.

## Description

Compute average of signed input arguments x and y as ( x + y + 1 ) >> 1, avoiding overflow in the intermediate sum.

# \_\_device\_\_ unsigned int \_\_sad (int x, int y, unsigned int z)

Calculate |x-y|+z, the sum of absolute difference.

### Returns

Returns |x-y|+z.

## Description

Calculate |x-y|+z, the 32-bit sum of the third argument z plus and the absolute value of the difference between the first argument, x, and second argument, y.

Inputs x and y are signed 32-bit integers, input z is a 32-bit unsigned integer.

# \_\_device\_\_ unsigned int \_\_uhadd (unsigned int, unsigned int)

Compute average of unsigned input arguments, avoiding overflow in the intermediate sum.

#### Returns

Returns an unsigned integer value representing the unsigned average value of the two inputs.

## Description

Compute average of unsigned input arguments x and y as (x + y) >> 1, avoiding overflow in the intermediate sum.

# \_\_device\_\_ unsigned int \_\_umul24 (unsigned int x, unsigned int y)

Calculate the least significant 32 bits of the product of the least significant 24 bits of two unsigned integers.

#### Returns

Returns the least significant 32 bits of the product x \* y.

## Description

Calculate the least significant 32 bits of the product of the least significant 24 bits of x and y. The high order 8 bits of x and y are ignored.

# \_\_device\_\_ unsigned long long int \_\_umul64hi (unsigned long long int x, unsigned long long int y)

Calculate the most significant 64 bits of the product of the two 64 unsigned bit integers.

#### Returns

Returns the most significant 64 bits of the product x \* y.

## Description

Calculate the most significant 64 bits of the 128-bit product x \* y, where x = 64-bit unsigned integers.

# \_\_device\_\_ unsigned int \_\_umulhi (unsigned int x, unsigned int y)

Calculate the most significant 32 bits of the product of the two 32-bit unsigned integers.

#### Returns

Returns the most significant 32 bits of the product x \* y.

## Description

Calculate the most significant 32 bits of the 64-bit product x \* y, where x = 32-bit unsigned integers.

# \_\_device\_\_ unsigned int \_\_urhadd (unsigned int, unsigned int)

Compute rounded average of unsigned input arguments, avoiding overflow in the intermediate sum.

#### Returns

Returns an unsigned integer value representing the unsigned rounded average value of the two inputs.

# Description

Compute average of unsigned input arguments x and y as (x + y + 1) >> 1, avoiding overflow in the intermediate sum.

# \_\_device\_\_ unsigned int \_\_usad (unsigned int x, unsigned int y, unsigned int z)

Calculate |x-y|+z, the sum of absolute difference.

#### Returns

Returns |x-y|+z.

## Description

Calculate |x-y|+z, the 32-bit sum of the third argument z plus and the absolute value of the difference between the first argument, x, and second argument, y.

Inputs x, y, and z are unsigned 32-bit integers.

# 1.10. Type Casting Intrinsics

This section describes type casting intrinsic functions that are only supported in device code. To use these functions you do not need to include any additional header files in your program.

# device float double2float rd (double x)

Convert a double to a float in round-down mode.

#### Returns

Returns converted value.

## Description

Convert the double-precision floating-point value  $\mathbf{x}$  to a single-precision floating-point value in round-down (to negative infinity) mode.

# \_\_device\_\_float \_\_double2float\_rn (double x)

Convert a double to a float in round-to-nearest-even mode.

#### Returns

Returns converted value.

## Description

Convert the double-precision floating-point value  $\mathbf{x}$  to a single-precision floating-point value in round-to-nearest-even mode.

# \_\_\_device\_\_\_ float \_\_\_double2float\_ru (double x)

Convert a double to a float in round-up mode.

#### Returns

Returns converted value.

## Description

Convert the double-precision floating-point value x to a single-precision floating-point value in round-up (to positive infinity) mode.

# \_\_\_device\_\_\_float \_\_\_double2float\_rz (double x)

Convert a double to a float in round-towards-zero mode.

#### Returns

Returns converted value.

## Description

Convert the double-precision floating-point value  ${\bf x}$  to a single-precision floating-point value in round-towards-zero mode.

# \_\_device\_\_ int \_\_double2hiint (double x)

Reinterpret high 32 bits in a double as a signed integer.

#### Returns

Returns reinterpreted value.

## Description

Reinterpret the high 32 bits in the double-precision floating-point value x as a signed integer.

# \_\_\_device\_\_ int \_\_\_double2int\_rd (double x)

Convert a double to a signed int in round-down mode.

#### Returns

Returns converted value.

## **Description**

Convert the double-precision floating-point value  $\mathbf{x}$  to a signed integer value in round-down (to negative infinity) mode.

# \_\_device\_\_ int \_\_double2int\_rn (double x)

Convert a double to a signed int in round-to-nearest-even mode.

#### Returns

Returns converted value.

Convert the double-precision floating-point value  $\mathbf{x}$  to a signed integer value in round-to-nearest-even mode.

# \_\_\_device\_\_ int \_\_\_double2int\_ru (double x)

Convert a double to a signed int in round-up mode.

#### Returns

Returns converted value.

## Description

Convert the double-precision floating-point value  $\mathbf{x}$  to a signed integer value in round-up (to positive infinity) mode.

# \_\_\_device\_\_ int \_\_\_double2int\_rz (double)

Convert a double to a signed int in round-towards-zero mode.

#### Returns

Returns converted value.

## Description

Convert the double-precision floating-point value  ${\bf x}$  to a signed integer value in round-towards-zero mode.

# \_\_device\_\_ long long int \_\_double2ll\_rd (double x)

Convert a double to a signed 64-bit int in round-down mode.

#### Returns

Returns converted value.

## Description

Convert the double-precision floating-point value  $\mathbf{x}$  to a signed 64-bit integer value in round-down (to negative infinity) mode.

# \_\_device\_\_ long long int \_\_double2ll\_rn (double x)

Convert a double to a signed 64-bit int in round-to-nearest-even mode.

#### Returns

Returns converted value.

## Description

Convert the double-precision floating-point value  $\mathbf{x}$  to a signed 64-bit integer value in round-to-nearest-even mode.

# \_\_device\_\_ long long int \_\_double2ll\_ru (double x)

Convert a double to a signed 64-bit int in round-up mode.

#### Returns

Returns converted value.

### Description

Convert the double-precision floating-point value x to a signed 64-bit integer value in round-up (to positive infinity) mode.

# \_\_\_device\_\_\_ long long int \_\_\_double2ll\_rz (double)

Convert a double to a signed 64-bit int in round-towards-zero mode.

#### Returns

Returns converted value.

## Description

Convert the double-precision floating-point value x to a signed 64-bit integer value in round-towards-zero mode.

# \_\_\_device\_\_ int \_\_\_double2loint (double x)

Reinterpret low 32 bits in a double as a signed integer.

#### Returns

Returns reinterpreted value.

Reinterpret the low 32 bits in the double-precision floating-point value x as a signed integer.

# \_\_\_device\_\_\_ unsigned int \_\_\_double2uint\_rd (double x)

Convert a double to an unsigned int in round-down mode.

#### Returns

Returns converted value.

## Description

Convert the double-precision floating-point value  ${\bf x}$  to an unsigned integer value in round-down (to negative infinity) mode.

# \_\_device\_\_ unsigned int \_\_\_double2uint\_rn (double x)

Convert a double to an unsigned int in round-to-nearest-even mode.

#### Returns

Returns converted value.

## Description

Convert the double-precision floating-point value  $\mathbf{x}$  to an unsigned integer value in round-to-nearest-even mode.

# \_\_device\_\_ unsigned int \_\_double2uint\_ru (double x)

Convert a double to an unsigned int in round-up mode.

#### Returns

Returns converted value.

## **Description**

Convert the double-precision floating-point value  $\mathbf{x}$  to an unsigned integer value in round-up (to positive infinity) mode.

# \_\_device\_\_ unsigned int \_\_double2uint\_rz (double)

Convert a double to an unsigned int in round-towards-zero mode.

#### Returns

Returns converted value.

## Description

Convert the double-precision floating-point value  ${\bf x}$  to an unsigned integer value in round-towards-zero mode.

# \_\_device\_\_ unsigned long long int \_\_double2ull\_rd (double x)

Convert a double to an unsigned 64-bit int in round-down mode.

#### Returns

Returns converted value.

### Description

Convert the double-precision floating-point value x to an unsigned 64-bit integer value in round-down (to negative infinity) mode.

# \_\_device\_\_ unsigned long long int \_\_double2ull\_rn (double x)

Convert a double to an unsigned 64-bit int in round-to-nearest-even mode.

#### Returns

Returns converted value.

## Description

Convert the double-precision floating-point value  $\mathbf{x}$  to an unsigned 64-bit integer value in round-to-nearest-even mode.

# \_\_device\_\_ unsigned long long int \_\_double2ull\_ru (double x)

Convert a double to an unsigned 64-bit int in round-up mode.

#### Returns

Returns converted value.

### Description

Convert the double-precision floating-point value x to an unsigned 64-bit integer value in round-up (to positive infinity) mode.

# \_\_device\_\_ unsigned long long int \_\_double2ull\_rz (double)

Convert a double to an unsigned 64-bit int in round-towards-zero mode.

#### Returns

Returns converted value.

## Description

Convert the double-precision floating-point value  $\mathbf{x}$  to an unsigned 64-bit integer value in round-towards-zero mode.

# \_\_device\_\_ long long int \_\_double\_as\_longlong (double x)

Reinterpret bits in a double as a 64-bit signed integer.

#### Returns

Returns reinterpreted value.

## Description

Reinterpret the bits in the double-precision floating-point value x as a signed 64-bit integer.

# \_device\_\_ int \_\_float2int\_rd (float x)

Convert a float to a signed integer in round-down mode.

#### Returns

Returns converted value.

## Description

Convert the single-precision floating-point value  $\mathbf{x}$  to a signed integer in round-down (to negative infinity) mode.

# \_\_device\_\_ int \_\_float2int\_rn (float x)

Convert a float to a signed integer in round-to-nearest-even mode.

#### Returns

Returns converted value.

## Description

Convert the single-precision floating-point value  $\mathbf{x}$  to a signed integer in round-to-nearest-even mode.

# device int float2int ru (float)

Convert a float to a signed integer in round-up mode.

#### Returns

Returns converted value.

# Description

Convert the single-precision floating-point value  ${\bf x}$  to a signed integer in round-up (to positive infinity) mode.

# \_\_\_device\_\_\_ int \_\_\_float2int\_rz (float x)

Convert a float to a signed integer in round-towards-zero mode.

#### Returns

Returns converted value.

Convert the single-precision floating-point value  ${\bf x}$  to a signed integer in round-towards-zero mode.

# \_\_\_device\_\_\_ long long int \_\_\_float2ll\_rd (float x)

Convert a float to a signed 64-bit integer in round-down mode.

#### Returns

Returns converted value.

## Description

Convert the single-precision floating-point value  $\mathbf{x}$  to a signed 64-bit integer in round-down (to negative infinity) mode.

# \_\_\_device\_\_\_ long long int \_\_\_float2ll\_rn (float x)

Convert a float to a signed 64-bit integer in round-to-nearest-even mode.

#### Returns

Returns converted value.

## Description

Convert the single-precision floating-point value  $\mathbf{x}$  to a signed 64-bit integer in round-to-nearest-even mode.

# \_\_device\_\_ long long int \_\_float2ll\_ru (float x)

Convert a float to a signed 64-bit integer in round-up mode.

#### Returns

Returns converted value.

## Description

Convert the single-precision floating-point value x to a signed 64-bit integer in round-up (to positive infinity) mode.

# \_\_device\_\_ long long int \_\_float2ll\_rz (float x)

Convert a float to a signed 64-bit integer in round-towards-zero mode.

#### Returns

Returns converted value.

## Description

Convert the single-precision floating-point value  ${\bf x}$  to a signed 64-bit integer in round-towards-zero mode.

# \_\_device\_\_ unsigned int \_\_float2uint\_rd (float x)

Convert a float to an unsigned integer in round-down mode.

#### Returns

Returns converted value.

### Description

Convert the single-precision floating-point value x to an unsigned integer in round-down (to negative infinity) mode.

# \_\_\_device\_\_ unsigned int \_\_\_float2uint\_rn (float x)

Convert a float to an unsigned integer in round-to-nearest-even mode.

#### Returns

Returns converted value.

## Description

Convert the single-precision floating-point value  $\mathbf{x}$  to an unsigned integer in round-to-nearest-even mode.

# \_\_\_device\_\_\_ unsigned int \_\_\_float2uint\_ru (float x)

Convert a float to an unsigned integer in round-up mode.

#### Returns

Returns converted value.

Convert the single-precision floating-point value x to an unsigned integer in round-up (to positive infinity) mode.

# \_\_\_device\_\_\_ unsigned int \_\_\_float2uint\_rz (float x)

Convert a float to an unsigned integer in round-towards-zero mode.

#### Returns

Returns converted value.

## Description

Convert the single-precision floating-point value  ${\bf x}$  to an unsigned integer in round-towards-zero mode.

# \_\_device\_\_ unsigned long long int \_\_float2ull\_rd (float x)

Convert a float to an unsigned 64-bit integer in round-down mode.

#### Returns

Returns converted value.

## Description

Convert the single-precision floating-point value  $\mathbf{x}$  to an unsigned 64-bit integer in round-down (to negative infinity) mode.

# \_\_device\_\_ unsigned long long int \_\_float2ull\_rn (float x)

Convert a float to an unsigned 64-bit integer in round-to-nearest-even mode.

#### Returns

Returns converted value.

## Description

Convert the single-precision floating-point value  ${\bf x}$  to an unsigned 64-bit integer in round-to-nearest-even mode.

# \_\_device\_\_ unsigned long long int \_\_float2ull\_ru (float x)

Convert a float to an unsigned 64-bit integer in round-up mode.

#### Returns

Returns converted value.

### Description

Convert the single-precision floating-point value x to an unsigned 64-bit integer in round-up (to positive infinity) mode.

# \_\_device\_\_ unsigned long long int \_\_float2ull\_rz (float x)

Convert a float to an unsigned 64-bit integer in round-towards-zero mode.

#### Returns

Returns converted value.

## Description

Convert the single-precision floating-point value  $\mathbf{x}$  to an unsigned 64-bit integer in round-towards-zero mode.

# \_\_\_device\_\_ int \_\_float\_as\_int (float x)

Reinterpret bits in a float as a signed integer.

#### Returns

Returns reinterpreted value.

## Description

Reinterpret the bits in the single-precision floating-point value x as a signed integer.

# \_\_\_device\_\_ unsigned int \_\_\_float\_as\_uint (float x)

Reinterpret bits in a float as a unsigned integer.

#### Returns

Returns reinterpreted value.

Reinterpret the bits in the single-precision floating-point value x as a unsigned integer.

# \_\_device\_\_ double \_\_hiloint2double (int hi, int lo)

Reinterpret high and low 32-bit integer values as a double.

#### Returns

Returns reinterpreted value.

### Description

Reinterpret the integer value of hi as the high 32 bits of a double-precision floating-point value and the integer value of lo as the low 32 bits of the same double-precision floating-point value.

# \_\_\_device\_\_ double \_\_int2double\_rn (int x)

Convert a signed int to a double.

#### Returns

Returns converted value.

## Description

Convert the signed integer value x to a double-precision floating-point value.

# \_\_device\_\_ float \_\_int2float\_rd (int x)

Convert a signed integer to a float in round-down mode.

#### Returns

Returns converted value.

# Description

Convert the signed integer value x to a single-precision floating-point value in round-down (to negative infinity) mode.

# device float int2float rn (int x)

Convert a signed integer to a float in round-to-nearest-even mode.

#### Returns

Returns converted value.

## Description

Convert the signed integer value  $\mathbf{x}$  to a single-precision floating-point value in round-to-nearest-even mode.

# \_\_\_device\_\_\_float \_\_\_int2float\_ru (int x)

Convert a signed integer to a float in round-up mode.

#### Returns

Returns converted value.

### Description

Convert the signed integer value x to a single-precision floating-point value in round-up (to positive infinity) mode.

# device float int2float rz (int x)

Convert a signed integer to a float in round-towards-zero mode.

#### Returns

Returns converted value.

## Description

Convert the signed integer value  $\mathbf{x}$  to a single-precision floating-point value in round-towards-zero mode.

# \_\_\_device\_\_\_ float \_\_\_int\_as\_float (int x)

Reinterpret bits in an integer as a float.

#### Returns

Returns reinterpreted value.

Reinterpret the bits in the signed integer value x as a single-precision floating-point value.

# \_\_\_device\_\_ double \_\_ll2double\_rd (long long int x)

Convert a signed 64-bit int to a double in round-down mode.

#### Returns

Returns converted value.

## Description

Convert the signed 64-bit integer value x to a double-precision floating-point value in round-down (to negative infinity) mode.

# \_\_device\_\_ double \_\_ll2double\_rn (long long int x)

Convert a signed 64-bit int to a double in round-to-nearest-even mode.

#### Returns

Returns converted value.

## Description

Convert the signed 64-bit integer value  $\mathbf{x}$  to a double-precision floating-point value in round-to-nearest-even mode.

# \_\_\_device\_\_ double \_\_\_ll2double\_ru (long long int x)

Convert a signed 64-bit int to a double in round-up mode.

#### Returns

Returns converted value.

## **Description**

Convert the signed 64-bit integer value x to a double-precision floating-point value in round-up (to positive infinity) mode.

# \_\_device\_\_double \_\_ll2double\_rz (long long int x)

Convert a signed 64-bit int to a double in round-towards-zero mode.

#### Returns

Returns converted value.

## Description

Convert the signed 64-bit integer value  $\mathbf{x}$  to a double-precision floating-point value in round-towards-zero mode.

# \_\_device\_\_ float \_\_ll2float\_rd (long long int x)

Convert a signed integer to a float in round-down mode.

#### Returns

Returns converted value.

### Description

Convert the signed integer value x to a single-precision floating-point value in round-down (to negative infinity) mode.

# \_\_device\_\_ float \_\_ll2float\_rn (long long int x)

Convert a signed 64-bit integer to a float in round-to-nearest-even mode.

#### Returns

Returns converted value.

## Description

Convert the signed 64-bit integer value  $\mathbf{x}$  to a single-precision floating-point value in round-to-nearest-even mode.

# \_\_\_device\_\_\_ float \_\_\_ll2float\_ru (long long int x)

Convert a signed integer to a float in round-up mode.

#### Returns

Returns converted value.

Convert the signed integer value  $\mathbf{x}$  to a single-precision floating-point value in round-up (to positive infinity) mode.

Convert a signed integer to a float in round-towards-zero mode.

#### Returns

Returns converted value.

## Description

Convert the signed integer value  $\mathbf{x}$  to a single-precision floating-point value in round-towards-zero mode.

# \_\_device\_\_ double \_\_longlong\_as\_double (long long int x)

Reinterpret bits in a 64-bit signed integer as a double.

#### Returns

Returns reinterpreted value.

## Description

Reinterpret the bits in the 64-bit signed integer value  $\mathbf{x}$  as a double-precision floating-point value.

# \_\_device\_\_ double \_\_uint2double\_rn (unsigned int x)

Convert an unsigned int to a double.

#### Returns

Returns converted value.

## Description

Convert the unsigned integer value x to a double-precision floating-point value.

# \_\_\_device\_\_\_ float \_\_\_uint2float\_rd (unsigned int x)

Convert an unsigned integer to a float in round-down mode.

#### Returns

Returns converted value.

## Description

Convert the unsigned integer value x to a single-precision floating-point value in round-down (to negative infinity) mode.

# \_\_device\_\_ float \_\_uint2float\_rn (unsigned int x)

Convert an unsigned integer to a float in round-to-nearest-even mode.

#### Returns

Returns converted value.

### Description

Convert the unsigned integer value  $\mathbf{x}$  to a single-precision floating-point value in round-to-nearest-even mode.

# \_\_\_device\_\_\_ float \_\_\_uint2float\_ru (unsigned int x)

Convert an unsigned integer to a float in round-up mode.

#### Returns

Returns converted value.

## Description

Convert the unsigned integer value  ${\bf x}$  to a single-precision floating-point value in round-up (to positive infinity) mode.

# \_\_\_device\_\_\_ float \_\_\_uint2float\_rz (unsigned int x)

Convert an unsigned integer to a float in round-towards-zero mode.

#### Returns

Returns converted value.

Convert the unsigned integer value x to a single-precision floating-point value in round-towards-zero mode.

Reinterpret bits in an unsigned integer as a float.

#### Returns

Returns reinterpreted value.

## Description

Reinterpret the bits in the unsigned integer value  ${\bf x}$  as a single-precision floating-point value.

# \_\_device\_\_ double \_\_ull2double\_rd (unsigned long long int x)

Convert an unsigned 64-bit int to a double in round-down mode.

#### Returns

Returns converted value.

## Description

Convert the unsigned 64-bit integer value  $\mathbf{x}$  to a double-precision floating-point value in round-down (to negative infinity) mode.

# \_\_device\_\_ double \_\_ull2double\_rn (unsigned long long int x)

Convert an unsigned 64-bit int to a double in round-to-nearest-even mode.

#### Returns

Returns converted value.

## Description

Convert the unsigned 64-bit integer value  $\mathbf{x}$  to a double-precision floating-point value in round-to-nearest-even mode.

# \_\_device\_\_ double \_\_ull2double\_ru (unsigned long long int x)

Convert an unsigned 64-bit int to a double in round-up mode.

#### Returns

Returns converted value.

### Description

Convert the unsigned 64-bit integer value x to a double-precision floating-point value in round-up (to positive infinity) mode.

# \_\_device\_\_ double \_\_ull2double\_rz (unsigned long long int x)

Convert an unsigned 64-bit int to a double in round-towards-zero mode.

#### Returns

Returns converted value.

## Description

Convert the unsigned 64-bit integer value  $\mathbf{x}$  to a double-precision floating-point value in round-towards-zero mode.

# \_\_device\_\_ float \_\_ull2float\_rd (unsigned long long int x)

Convert an unsigned integer to a float in round-down mode.

#### Returns

Returns converted value.

## Description

Convert the unsigned integer value  $\mathbf{x}$  to a single-precision floating-point value in round-down (to negative infinity) mode.

# \_\_device\_\_ float \_\_ull2float\_rn (unsigned long long int x)

Convert an unsigned integer to a float in round-to-nearest-even mode.

### Returns

Returns converted value.

### Description

Convert the unsigned integer value x to a single-precision floating-point value in round-to-nearest-even mode.

# \_\_device\_\_ float \_\_ull2float\_ru (unsigned long long int x)

Convert an unsigned integer to a float in round-up mode.

#### Returns

Returns converted value.

## Description

Convert the unsigned integer value x to a single-precision floating-point value in round-up (to positive infinity) mode.

# \_\_device\_\_ float \_\_ull2float\_rz (unsigned long long int x)

Convert an unsigned integer to a float in round-towards-zero mode.

#### Returns

Returns converted value.

## Description

Convert the unsigned integer value  ${\bf x}$  to a single-precision floating-point value in round-towards-zero mode.

## 1.11. SIMD Intrinsics

This section describes SIMD intrinsic functions that are only supported in device code. To use these functions you do not need to include any additional header files in your program.

## \_\_device\_\_ unsigned int \_\_vabs2 (unsigned int a)

Computes per-halfword absolute value.

### Returns

Returns computed value.

### Description

Splits 4 bytes of argument into 2 parts, each consisting of 2 bytes, then computes absolute value for each of parts. Partial results are recombined and returned as unsigned int.

## \_\_device\_\_ unsigned int \_\_vabs4 (unsigned int a)

Computes per-byte absolute value.

### Returns

Returns computed value.

## Description

Splits argument by bytes. Computes absolute value of each byte. Partial results are recombined and returned as unsigned int.

# \_\_device\_\_ unsigned int \_\_vabsdiffs2 (unsigned int a, unsigned int b)

Computes per-halfword sum of absolute difference of signed integer.

#### Returns

Returns computed value.

### Description

Splits 4 bytes of each into 2 parts, each consisting of 2 bytes. For corresponding parts function computes absolute difference. Partial results are recombined and returned as unsigned int.

# \_\_device\_\_ unsigned int \_\_vabsdiffs4 (unsigned int a, unsigned int b)

Computes per-byte absolute difference of signed integer.

#### Returns

Returns computed value.

### Description

Splits 4 bytes of each into 4 parts, each consisting of 1 byte. For corresponding parts function computes absolute difference. Partial results are recombined and returned as unsigned int.

# \_\_device\_\_ unsigned int \_\_vabsdiffu2 (unsigned int a, unsigned int b)

Performs per-halfword absolute difference of unsigned integer computation: |a - b|.

#### Returns

Returns computed value.

## Description

Splits 4 bytes of each argument into 2 parts, each consisting of 2 bytes. For corresponding parts function computes absolute difference. Partial results are recombined and returned as unsigned int.

# \_\_device\_\_ unsigned int \_\_vabsdiffu4 (unsigned int a, unsigned int b)

Computes per-byte absolute difference of unsigned integer.

#### Returns

Returns computed value.

### Description

Splits 4 bytes of each argument into 4 parts, each consisting of 1 byte. For corresponding parts function computes absolute difference. Partial results are recombined and returned as unsigned int.

## \_\_device\_\_\_ unsigned int \_\_\_vabsss2 (unsigned int a)

Computes per-halfword absolute value with signed saturation.

### Returns

Returns computed value.

### Description

Splits 4 bytes of argument into 2 parts, each consisting of 2 bytes, then computes absolute value with signed saturation for each of parts. Partial results are recombined and returned as unsigned int.

# \_\_device\_\_ unsigned int \_\_\_vabsss4 (unsigned int a)

Computes per-byte absolute value with signed saturation.

#### Returns

Returns computed value.

### Description

Splits 4 bytes of argument into 4 parts, each consisting of 1 byte, then computes absolute value with signed saturation for each of parts. Partial results are recombined and returned as unsigned int.

# \_\_device\_\_ unsigned int \_\_vadd2 (unsigned int a, unsigned int b)

Performs per-halfword (un)signed addition, with wrap-around: a + b.

### Returns

Returns computed value.

### Description

Splits 4 bytes of each argument into 2 parts, each consisting of 2 bytes, then performs unsigned addition on corresponding parts. Partial results are recombined and returned as unsigned int.

# \_\_device\_\_ unsigned int \_\_vadd4 (unsigned int a, unsigned int b)

Performs per-byte (un)signed addition.

### Returns

Returns computed value.

### Description

Splits 'a' into 4 bytes, then performs unsigned addition on each of these bytes with the corresponding byte from 'b', ignoring overflow. Partial results are recombined and returned as unsigned int.

# \_\_device\_\_ unsigned int \_\_vaddss2 (unsigned int a, unsigned int b)

Performs per-halfword addition with signed saturation.

### Returns

Returns computed value.

## Description

Splits 4 bytes of each argument into 2 parts, each consisting of 2 bytes, then performs addition with signed saturation on corresponding parts. Partial results are recombined and returned as unsigned int.

# \_\_device\_\_ unsigned int \_\_vaddss4 (unsigned int a, unsigned int b)

Performs per-byte addition with signed saturation.

### Returns

Returns computed value.

## Description

Splits 4 bytes of each argument into 4 parts, each consisting of 1 byte, then performs addition with signed saturation on corresponding parts. Partial results are recombined and returned as unsigned int.

# \_\_device\_\_ unsigned int \_\_vaddus2 (unsigned int a, unsigned int b)

Performs per-halfword addition with unsigned saturation.

#### Returns

Returns computed value.

### Description

Splits 4 bytes of each argument into 2 parts, each consisting of 2 bytes, then performs addition with unsigned saturation on corresponding parts.

# \_\_device\_\_ unsigned int \_\_vaddus4 (unsigned int a, unsigned int b)

Performs per-byte addition with unsigned saturation.

#### Returns

Returns computed value.

## Description

Splits 4 bytes of each argument into 4 parts, each consisting of 1 byte, then performs addition with unsigned saturation on corresponding parts.

# \_\_device\_\_ unsigned int \_\_vavgs2 (unsigned int a, unsigned int b)

Performs per-halfword signed rounded average computation.

#### Returns

Returns computed value.

## Description

Splits 4 bytes of each argument into 2 parts, each consisting of 2 bytes, then computes signed rounded average of corresponding parts. Partial results are recombined and returned as unsigned int.

# \_\_device\_\_ unsigned int \_\_vavgs4 (unsigned int a, unsigned int b)

Computes per-byte signed rounded average.

### Returns

Returns computed value.

### Description

Splits 4 bytes of each argument into 4 parts, each consisting of 1 byte. then computes signed rounded average of corresponding parts. Partial results are recombined and returned as unsigned int.

# \_\_device\_\_ unsigned int \_\_vavgu2 (unsigned int a, unsigned int b)

Performs per-halfword unsigned rounded average computation.

#### Returns

Returns computed value.

## Description

Splits 4 bytes of each argument into 2 parts, each consisting of 2 bytes, then computes unsigned rounded average of corresponding parts. Partial results are recombined and returned as unsigned int.

# \_\_device\_\_ unsigned int \_\_vavgu4 (unsigned int a, unsigned int b)

Performs per-byte unsigned rounded average.

### Returns

Returns computed value.

## Description

Splits 4 bytes of each argument into 4 parts, each consisting of 1 byte. then computes unsigned rounded average of corresponding parts. Partial results are recombined and returned as unsigned int.

# \_\_device\_\_ unsigned int \_\_vcmpeq2 (unsigned int a, unsigned int b)

Performs per-halfword (un)signed comparison.

#### Returns

Returns Oxffff computed value.

### Description

Splits 4 bytes of each argument into 2 parts, each consisting of 2 bytes. For corresponding parts result is ffff if they are equal, and 0000 otherwise. For example \_\_vcmpeq2(0x1234aba5, 0x1234aba6) returns 0xffff0000.

# \_\_device\_\_ unsigned int \_\_vcmpeq4 (unsigned int a, unsigned int b)

Performs per-byte (un)signed comparison.

#### Returns

Returns 0xff if a = b, else returns 0.

### Description

Splits 4 bytes of each argument into 4 parts, each consisting of 1 byte. For corresponding parts result is ff if they are equal, and 00 otherwise. For example \_\_vcmpeq4(0x1234aba5, 0x1234aba6) returns 0xffffff00.

# \_\_device\_\_ unsigned int \_\_vcmpges2 (unsigned int a, unsigned int b)

Performs per-halfword signed comparison: a >= b ? 0xffff : 0.

### Returns

Returns 0xffff if a >= b, else returns 0.

### Description

Splits 4 bytes of each argument into 2 parts, each consisting of 2 bytes. For corresponding parts result is ffff if 'a' part >= 'b' part, and 0000 otherwise. For example \_\_vcmpges2(0x1234aba5, 0x1234aba6) returns 0xffff0000.

# \_\_device\_\_ unsigned int \_\_vcmpges4 (unsigned int a, unsigned int b)

Performs per-byte signed comparison.

### Returns

Returns 0xff if a >= b, else returns 0.

### Description

Splits 4 bytes of each argument into 4 parts, each consisting of 1 byte. For corresponding parts result is ff if 'a' part >= 'b' part, and 00 otherwise. For example \_\_vcmpges4(0x1234aba5, 0x1234aba6) returns 0xffffff00.

# \_\_device\_\_ unsigned int \_\_vcmpgeu2 (unsigned int a, unsigned int b)

Performs per-halfword unsigned comparison: a >= b ? 0xffff : 0.

#### Returns

Returns 0xffff if a >= b, else returns 0.

### Description

Splits 4 bytes of each argument into 2 parts, each consisting of 2 bytes. For corresponding parts result is ffff if 'a' part >= 'b' part, and 0000 otherwise. For example \_\_vcmpgeu2(0x1234aba5, 0x1234aba6) returns 0xffff0000.

# \_\_device\_\_ unsigned int \_\_vcmpgeu4 (unsigned int a, unsigned int b)

Performs per-byte unsigned comparison.

### Returns

Returns 0xff if a = b, else returns 0.

## Description

Splits 4 bytes of each argument into 4 parts, each consisting of 1 byte. For corresponding parts result is ff if 'a' part >= 'b' part, and 00 otherwise. For example \_\_vcmpgeu4(0x1234aba5, 0x1234aba6) returns 0xffffff00.

# \_\_device\_\_ unsigned int \_\_vcmpgts2 (unsigned int a, unsigned int b)

Performs per-halfword signed comparison: a > b ? 0xffff : 0.

#### Returns

Returns 0xffff if a > b, else returns 0.

### Description

Splits 4 bytes of each argument into 2 parts, each consisting of 2 bytes. For corresponding parts result is ffff if 'a' part > 'b' part, and 0000 otherwise. For example \_\_vcmpqts2(0x1234aba5, 0x1234aba6) returns 0x00000000.

# \_\_device\_\_ unsigned int \_\_vcmpgts4 (unsigned int a, unsigned int b)

Performs per-byte signed comparison.

#### Returns

Returns 0xff if a > b, else returns 0.

### Description

Splits 4 bytes of each argument into 4 parts, each consisting of 1 byte. For corresponding parts result is ff if 'a' part > 'b' part, and 00 otherwise. For example \_\_vcmpgts4(0x1234aba5, 0x1234aba6) returns 0x00000000.

# \_\_device\_\_ unsigned int \_\_vcmpgtu2 (unsigned int a, unsigned int b)

Performs per-halfword unsigned comparison: a > b ? 0xffff : 0.

### Returns

Returns 0xffff if a > b, else returns 0.

## Description

Splits 4 bytes of each argument into 2 parts, each consisting of 2 bytes. For corresponding parts result is ffff if 'a' part > 'b' part, and 0000 otherwise. For example \_\_vcmpgtu2(0x1234aba5, 0x1234aba6) returns 0x00000000.

# \_\_device\_\_ unsigned int \_\_vcmpgtu4 (unsigned int a, unsigned int b)

Performs per-byte unsigned comparison.

### Returns

Returns 0xff if a > b. else returns 0.

### Description

Splits 4 bytes of each argument into 4 parts, each consisting of 1 byte. For corresponding parts result is ff if 'a' part > 'b' part, and 00 otherwise. For example \_\_vcmpgtu4(0x1234aba5, 0x1234aba6) returns 0x00000000.

# \_\_device\_\_ unsigned int \_\_vcmples2 (unsigned int a, unsigned int b)

Performs per-halfword signed comparison: a <= b ? 0xffff : 0.

#### Returns

Returns 0xffff if a <= b, else returns 0.

## Description

Splits 4 bytes of each argument into 2 parts, each consisting of 2 bytes. For corresponding parts result is ffff if 'a' part <= 'b' part, and 0000 otherwise. For example \_\_vcmples2(0x1234aba5, 0x1234aba6) returns 0xfffffff.

# \_\_device\_\_ unsigned int \_\_vcmples4 (unsigned int a, unsigned int b)

Performs per-byte signed comparison.

### Returns

Returns 0xff if a <= b, else returns 0.

## Description

Splits 4 bytes of each argument into 4 parts, each consisting of 1 byte. For corresponding parts result is ff if 'a' part <= 'b' part, and 00 otherwise. For example \_\_vcmples4(0x1234aba5, 0x1234aba6) returns 0xffffffff.

# \_\_device\_\_ unsigned int \_\_vcmpleu2 (unsigned int a, unsigned int b)

Performs per-halfword unsigned comparison: a <= b ? 0xffff : 0.

#### Returns

Returns 0xffff if a <= b, else returns 0.

### Description

Splits 4 bytes of each argument into 2 parts, each consisting of 2 bytes. For corresponding parts result is ffff if 'a' part <= 'b' part, and 0000 otherwise. For example \_\_vcmpleu2(0x1234aba5, 0x1234aba6) returns 0xfffffff.

# \_\_device\_\_ unsigned int \_\_vcmpleu4 (unsigned int a, unsigned int b)

Performs per-byte unsigned comparison.

#### Returns

Returns 0xff if a <= b, else returns 0.

## Description

Splits 4 bytes of each argument into 4 parts, each consisting of 1 byte. For corresponding parts result is ff if 'a' part <= 'b' part, and 00 otherwise. For example \_\_vcmpleu4(0x1234aba5, 0x1234aba6) returns 0xffffffff.

# \_\_device\_\_ unsigned int \_\_vcmplts2 (unsigned int a, unsigned int b)

Performs per-halfword signed comparison: a < b ? 0xffff : 0.

### Returns

Returns 0xffff if a < b, else returns 0.

### Description

Splits 4 bytes of each argument into 2 parts, each consisting of 2 bytes. For corresponding parts result is ffff if 'a' part < 'b' part, and 0000 otherwise. For example \_\_vcmplts2(0x1234aba5, 0x1234aba6) returns 0x0000ffff.

# \_\_device\_\_ unsigned int \_\_vcmplts4 (unsigned int a, unsigned int b)

Performs per-byte signed comparison.

#### Returns

Returns 0xff if a < b. else returns 0.

### Description

Splits 4 bytes of each argument into 4 parts, each consisting of 1 byte. For corresponding parts result is ff if 'a' part < 'b' part, and 00 otherwise. For example \_\_vcmplts4(0x1234aba5, 0x1234aba6) returns 0x000000ff.

# \_\_device\_\_ unsigned int \_\_vcmpltu2 (unsigned int a, unsigned int b)

Performs per-halfword unsigned comparison: a < b ? 0xffff : 0.

#### Returns

Returns 0xffff if a < b, else returns 0.

### Description

Splits 4 bytes of each argument into 2 parts, each consisting of 2 bytes. For corresponding parts result is ffff if 'a' part < 'b' part, and 0000 otherwise. For example \_\_vcmpltu2(0x1234aba5, 0x1234aba6) returns 0x0000ffff.

# \_\_device\_\_ unsigned int \_\_vcmpltu4 (unsigned int a, unsigned int b)

Performs per-byte unsigned comparison.

### Returns

Returns 0xff if a < b, else returns 0.

### Description

Splits 4 bytes of each argument into 4 parts, each consisting of 1 byte. For corresponding parts result is ff if 'a' part < 'b' part, and 00 otherwise. For example \_\_vcmpltu4(0x1234aba5, 0x1234aba6) returns 0x000000ff.

# \_\_device\_\_ unsigned int \_\_vcmpne2 (unsigned int a, unsigned int b)

Performs per-halfword (un)signed comparison: a != b ? 0xffff : 0.

### Returns

Returns 0xffff if a != b, else returns 0.

### Description

Splits 4 bytes of each argument into 2 parts, each consisting of 2 bytes. For corresponding parts result is ffff if 'a' part != 'b' part, and 0000 otherwise. For example \_\_vcmplts2(0x1234aba5, 0x1234aba6) returns 0x0000ffff.

# \_\_device\_\_ unsigned int \_\_vcmpne4 (unsigned int a, unsigned int b)

Performs per-byte (un)signed comparison.

#### Returns

Returns 0xff if a != b, else returns 0.

### Description

Splits 4 bytes of each argument into 4 parts, each consisting of 1 byte. For corresponding parts result is ff if 'a' part != 'b' part, and 00 otherwise. For example \_\_vcmplts4(0x1234aba5, 0x1234aba6) returns 0x000000ff.

# \_\_device\_\_ unsigned int \_\_vhaddu2 (unsigned int a, unsigned int b)

Performs per-halfword unsigned average computation.

### Returns

Returns computed value.

## Description

Splits 4 bytes of each argument into 2 parts, each consisting of 2 bytes, then computes unsigned average of corresponding parts. Partial results are recombined and returned as unsigned int.

# \_\_device\_\_ unsigned int \_\_vhaddu4 (unsigned int a, unsigned int b)

Computes per-byte unsigned average.

### Returns

Returns computed value.

### Description

Splits 4 bytes of each argument into 4 parts, each consisting of 1 byte. then computes unsigned average of corresponding parts. Partial results are recombined and returned as unsigned int.

# \_\_device\_\_ unsigned int \_\_vmaxs2 (unsigned int a, unsigned int b)

Performs per-halfword signed maximum computation.

#### Returns

Returns computed value.

## Description

Splits 4 bytes of each argument into 2 parts, each consisting of 2 bytes. For corresponding parts function computes signed maximum. Partial results are recombined and returned as unsigned int.

# \_\_device\_\_ unsigned int \_\_vmaxs4 (unsigned int a, unsigned int b)

Computes per-byte signed maximum.

### Returns

Returns computed value.

### Description

Splits 4 bytes of each argument into 4 parts, each consisting of 1 byte. For corresponding parts function computes signed maximum. Partial results are recombined and returned as unsigned int.

# \_\_device\_\_ unsigned int \_\_vmaxu2 (unsigned int a, unsigned int b)

Performs per-halfword unsigned maximum computation.

### Returns

Returns computed value.

### Description

Splits 4 bytes of each argument into 2 parts, each consisting of 2 bytes. For corresponding parts function computes unsigned maximum. Partial results are recombined and returned as unsigned int.

# \_\_device\_\_ unsigned int \_\_vmaxu4 (unsigned int a, unsigned int b)

Computes per-byte unsigned maximum.

#### Returns

Returns computed value.

## Description

Splits 4 bytes of each argument into 4 parts, each consisting of 1 byte. For corresponding parts function computes unsigned maximum. Partial results are recombined and returned as unsigned int.

# \_\_device\_\_ unsigned int \_\_vmins2 (unsigned int a, unsigned int b)

Performs per-halfword signed minimum computation.

### Returns

Returns computed value.

## Description

Splits 4 bytes of each argument into 2 parts, each consisting of 2 bytes. For corresponding parts function computes signed minimum. Partial results are recombined and returned as unsigned int.

# \_\_device\_\_ unsigned int \_\_vmins4 (unsigned int a, unsigned int b)

Computes per-byte signed minimum.

### Returns

Returns computed value.

### Description

Splits 4 bytes of each argument into 4 parts, each consisting of 1 byte. For corresponding parts function computes signed minimum. Partial results are recombined and returned as unsigned int

# \_\_device\_\_ unsigned int \_\_vminu2 (unsigned int a, unsigned int b)

Performs per-halfword unsigned minimum computation.

#### Returns

Returns computed value.

## Description

Splits 4 bytes of each argument into 2 parts, each consisting of 2 bytes. For corresponding parts function computes unsigned minimum. Partial results are recombined and returned as unsigned int.

# \_\_device\_\_ unsigned int \_\_vminu4 (unsigned int a, unsigned int b)

Computes per-byte unsigned minimum.

### Returns

Returns computed value.

## Description

Splits 4 bytes of each argument into 4 parts, each consisting of 1 byte. For corresponding parts function computes unsigned minimum. Partial results are recombined and returned as unsigned int.

## \_\_device\_\_ unsigned int \_\_vneg2 (unsigned int a)

Computes per-halfword negation.

#### Returns

Returns computed value.

### Description

Splits 4 bytes of argument into 2 parts, each consisting of 2 bytes. For each part function computes negation. Partial results are recombined and returned as unsigned int.

# \_\_\_device\_\_ unsigned int \_\_\_vneg4 (unsigned int a)

Performs per-byte negation.

#### Returns

Returns computed value.

### Description

Splits 4 bytes of argument into 4 parts, each consisting of 1 byte. For each part function computes negation. Partial results are recombined and returned as unsigned int.

# \_\_device\_\_ unsigned int \_\_vnegss2 (unsigned int a)

Computes per-halfword negation with signed saturation.

### Returns

Returns computed value.

## Description

Splits 4 bytes of argument into 2 parts, each consisting of 2 bytes. For each part function computes negation. Partial results are recombined and returned as unsigned int.

# \_\_device\_\_ unsigned int \_\_vnegss4 (unsigned int a)

Performs per-byte negation with signed saturation.

#### Returns

Returns computed value.

Splits 4 bytes of argument into 4 parts, each consisting of 1 byte. For each part function computes negation. Partial results are recombined and returned as unsigned int.

# \_\_device\_\_ unsigned int \_\_vsads2 (unsigned int a, unsigned int b)

Performs per-halfword sum of absolute difference of signed.

### Returns

Returns computed value.

### Description

Splits 4 bytes of each argument into 2 parts, each consisting of 2 bytes. For corresponding parts function computes absolute difference and sum it up. Partial results are recombined and returned as unsigned int.

# \_\_device\_\_ unsigned int \_\_vsads4 (unsigned int a, unsigned int b)

Computes per-byte sum of abs difference of signed.

#### Returns

Returns computed value.

## Description

Splits 4 bytes of each argument into 4 parts, each consisting of 1 byte. For corresponding parts function computes absolute difference and sum it up. Partial results are recombined and returned as unsigned int.

# \_\_device\_\_ unsigned int \_\_vsadu2 (unsigned int a, unsigned int b)

Computes per-halfword sum of abs diff of unsigned.

### Returns

Returns computed value.

Splits 4 bytes of each argument into 2 parts, each consisting of 2 bytes. For corresponding parts function computes absolute differences and returns sum of those differences.

# \_\_device\_\_ unsigned int \_\_vsadu4 (unsigned int a, unsigned int b)

Computes per-byte sum of abs difference of unsigned.

### Returns

Returns computed value.

### Description

Splits 4 bytes of each argument into 4 parts, each consisting of 1 byte. For corresponding parts function computes absolute differences and returns sum of those differences.

# \_\_device\_\_ unsigned int \_\_vseteq2 (unsigned int a, unsigned int b)

Performs per-halfword (un)signed comparison.

#### Returns

Returns 1 if a = b, else returns 0.

## Description

Splits 4 bytes of each argument into 2 parts, each consisting of 2 bytes. For corresponding parts function performs comparison 'a' part == 'b' part. If both equalities are satisfied, function returns 1.

# \_\_device\_\_ unsigned int \_\_vseteq4 (unsigned int a, unsigned int b)

Performs per-byte (un)signed comparison.

#### Returns

Returns 1 if a = b, else returns 0.

Splits 4 bytes of each argument into 4 parts, each consisting of 1 byte. For corresponding parts function performs comparison 'a' part == 'b' part. If both equalities are satisfied, function returns 1.

# \_\_device\_\_ unsigned int \_\_vsetges2 (unsigned int a, unsigned int b)

Performs per-halfword signed comparison.

#### Returns

Returns 1 if a >= b, else returns 0.

### Description

Splits 4 bytes of each argument into 2 parts, each consisting of 2 bytes. For corresponding parts function performs comparison 'a' part >= 'b' part. If both inequalities are satisfied, function returns 1.

# \_\_device\_\_ unsigned int \_\_vsetges4 (unsigned int a, unsigned int b)

Performs per-byte signed comparison.

### Returns

Returns 1 if  $a \ge b$ , else returns 0.

### Description

Splits 4 bytes of each argument into 4 parts, each consisting of 1 byte. For corresponding parts function performs comparison 'a' part >= 'b' part. If both inequalities are satisfied, function returns 1.

# \_\_device\_\_ unsigned int \_\_vsetgeu2 (unsigned int a, unsigned int b)

Performs per-halfword unsigned minimum unsigned comparison.

#### Returns

Returns 1 if a >= b, else returns 0.

Splits 4 bytes of each argument into 2 parts, each consisting of 2 bytes. For corresponding parts function performs comparison 'a' part >= 'b' part. If both inequalities are satisfied, function returns 1.

# \_\_device\_\_ unsigned int \_\_vsetgeu4 (unsigned int a, unsigned int b)

Performs per-byte unsigned comparison.

#### Returns

Returns 1 if a >= b, else returns 0.

### Description

Splits 4 bytes of each argument into 4 parts, each consisting of 1 byte. For corresponding parts function performs comparison 'a' part >= 'b' part. If both inequalities are satisfied, function returns 1.

# \_\_device\_\_ unsigned int \_\_vsetgts2 (unsigned int a, unsigned int b)

Performs per-halfword signed comparison.

### Returns

Returns 1 if a > b, else returns 0.

### Description

Splits 4 bytes of each argument into 2 parts, each consisting of 2 bytes. For corresponding parts function performs comparison 'a' part > 'b' part. If both inequalities are satisfied, function returns 1.

# \_\_device\_\_ unsigned int \_\_vsetgts4 (unsigned int a, unsigned int b)

Performs per-byte signed comparison.

#### Returns

Returns 1 if a > b, else returns 0.

Splits 4 bytes of each argument into 4 parts, each consisting of 1 byte. For corresponding parts function performs comparison 'a' part > 'b' part. If both inequalities are satisfied, function returns 1.

# \_\_device\_\_ unsigned int \_\_vsetgtu2 (unsigned int a, unsigned int b)

Performs per-halfword unsigned comparison.

#### Returns

Returns 1 if a > b, else returns 0.

### Description

Splits 4 bytes of each argument into 2 parts, each consisting of 2 bytes. For corresponding parts function performs comparison 'a' part > 'b' part. If both inequalities are satisfied, function returns 1.

# \_\_device\_\_ unsigned int \_\_vsetgtu4 (unsigned int a, unsigned int b)

Performs per-byte unsigned comparison.

### Returns

Returns 1 if a > b, else returns 0.

### Description

Splits 4 bytes of each argument into 4 parts, each consisting of 1 byte. For corresponding parts function performs comparison 'a' part > 'b' part. If both inequalities are satisfied, function returns 1.

# \_\_device\_\_ unsigned int \_\_vsetles2 (unsigned int a, unsigned int b)

Performs per-halfword unsigned minimum computation.

#### Returns

Returns 1 if a <= b, else returns 0.

Splits 4 bytes of each argument into 2 parts, each consisting of 2 bytes. For corresponding parts function performs comparison 'a' part <= 'b' part. If both inequalities are satisfied, function returns 1.

# \_\_device\_\_ unsigned int \_\_vsetles4 (unsigned int a, unsigned int b)

Performs per-byte signed comparison.

### Returns

Returns 1 if a <= b, else returns 0.

### Description

Splits 4 bytes of each argument into 4 parts, each consisting of 1 byte. For corresponding parts function performs comparison 'a' part <= 'b' part. If both inequalities are satisfied, function returns 1.

# \_\_device\_\_ unsigned int \_\_vsetleu2 (unsigned int a, unsigned int b)

Performs per-halfword signed comparison.

### Returns

Returns 1 if a <= b, else returns 0.

### Description

Splits 4 bytes of each argument into 2 parts, each consisting of 2 bytes. For corresponding parts function performs comparison 'a' part <= 'b' part. If both inequalities are satisfied, function returns 1.

# \_\_device\_\_ unsigned int \_\_vsetleu4 (unsigned int a, unsigned int b)

Performs per-byte unsigned comparison.

#### Returns

Returns 1 if a <= b, else returns 0.

Splits 4 bytes of each argument into 4 part, each consisting of 1 byte. For corresponding parts function performs comparison 'a' part <= 'b' part. If both inequalities are satisfied, function returns 1.

# \_\_device\_\_ unsigned int \_\_vsetlts2 (unsigned int a, unsigned int b)

Performs per-halfword signed comparison.

#### Returns

Returns 1 if a < b, else returns 0.

### Description

Splits 4 bytes of each argument into 2 parts, each consisting of 2 bytes. For corresponding parts function performs comparison 'a' part <= 'b' part. If both inequalities are satisfied, function returns 1.

# \_\_device\_\_ unsigned int \_\_vsetlts4 (unsigned int a, unsigned int b)

Performs per-byte signed comparison.

### Returns

Returns 1 if a < b, else returns 0.

### Description

Splits 4 bytes of each argument into 4 parts, each consisting of 1 byte. For corresponding parts function performs comparison 'a' part <= 'b' part. If both inequalities are satisfied, function returns 1.

# \_\_device\_\_ unsigned int \_\_vsetltu2 (unsigned int a, unsigned int b)

Performs per-halfword unsigned comparison.

#### Returns

Returns 1 if a < b, else returns 0.

Splits 4 bytes of each argument into 2 parts, each consisting of 2 bytes. For corresponding parts function performs comparison 'a' part <= 'b' part. If both inequalities are satisfied, function returns 1.

# \_\_device\_\_ unsigned int \_\_vsetltu4 (unsigned int a, unsigned int b)

Performs per-byte unsigned comparison.

#### Returns

Returns 1 if a < b, else returns 0.

### Description

Splits 4 bytes of each argument into 4 parts, each consisting of 1 byte. For corresponding parts function performs comparison 'a' part <= 'b' part. If both inequalities are satisfied, function returns 1.

# \_\_device\_\_ unsigned int \_\_vsetne2 (unsigned int a, unsigned int b)

Performs per-halfword (un)signed comparison.

### Returns

Returns 1 if a != b, else returns 0.

### Description

Splits 4 bytes of each argument into 2 parts, each consisting of 2 bytes. For corresponding parts function performs comparison 'a' part != 'b' part. If both conditions are satisfied, function returns 1.

# \_\_device\_\_ unsigned int \_\_vsetne4 (unsigned int a, unsigned int b)

Performs per-byte (un)signed comparison.

#### Returns

Returns 1 if a != b, else returns 0.

Splits 4 bytes of each argument into 4 parts, each consisting of 1 byte. For corresponding parts function performs comparison 'a' part != 'b' part. If both conditions are satisfied, function returns 1.

# \_\_device\_\_ unsigned int \_\_vsub2 (unsigned int a, unsigned int b)

Performs per-halfword (un)signed subtraction, with wrap-around.

#### Returns

Returns computed value.

### Description

Splits 4 bytes of each argument into 2 parts, each consisting of 2 bytes. For corresponding parts function performs subtraction. Partial results are recombined and returned as unsigned int.

# \_\_device\_\_ unsigned int \_\_vsub4 (unsigned int a, unsigned int b)

Performs per-byte subtraction.

### Returns

Returns computed value.

### Description

Splits 4 bytes of each argument into 4 parts, each consisting of 1 byte. For corresponding parts function performs subtraction. Partial results are recombined and returned as unsigned int.

# \_\_device\_\_ unsigned int \_\_vsubss2 (unsigned int a, unsigned int b)

Performs per-halfword (un)signed subtraction, with signed saturation.

### Returns

Returns computed value.

Splits 4 bytes of each argument into 2 parts, each consisting of 2 bytes. For corresponding parts function performs subtraction with signed saturation. Partial results are recombined and returned as unsigned int.

# \_\_device\_\_ unsigned int \_\_vsubss4 (unsigned int a, unsigned int b)

Performs per-byte subtraction with signed saturation.

#### Returns

Returns computed value.

### Description

Splits 4 bytes of each argument into 4 parts, each consisting of 1 byte. For corresponding parts function performs subtraction with signed saturation. Partial results are recombined and returned as unsigned int.

# \_\_device\_\_ unsigned int \_\_vsubus2 (unsigned int a, unsigned int b)

Performs per-halfword subtraction with unsigned saturation.

### Returns

Returns computed value.

### Description

Splits 4 bytes of each argument into 2 parts, each consisting of 2 bytes. For corresponding parts function performs subtraction with unsigned saturation. Partial results are recombined and returned as unsigned int.

# \_\_device\_\_ unsigned int \_\_vsubus4 (unsigned int a, unsigned int b)

Performs per-byte subtraction with unsigned saturation.

#### Returns

Returns computed value.

Splits 4 bytes of each argument into 4 parts, each consisting of 1 byte. For corresponding parts function performs subtraction with unsigned saturation. Partial results are recombined and returned as unsigned int.

#### Notice

This document is provided for information purposes only and shall not be regarded as a warranty of a certain functionality, condition, or quality of a product. NVIDIA Corporation ("NVIDIA") makes no representations or warranties, expressed or implied, as to the accuracy or completeness of the information contained in this document and assumes no responsibility for any errors contained herein. NVIDIA shall have no liability for the consequences or use of such information or for any infringement of patents or other rights of third parties that may result from its use. This document is not a commitment to develop, release, or deliver any Material (defined below), code, or functionality.

NVIDIA reserves the right to make corrections, modifications, enhancements, improvements, and any other changes to this document, at any time without notice.

Customer should obtain the latest relevant information before placing orders and should verify that such information is current and complete.

NVIDIA products are sold subject to the NVIDIA standard terms and conditions of sale supplied at the time of order acknowledgement, unless otherwise agreed in an individual sales agreement signed by authorized representatives of NVIDIA and customer ("Terms of Sale"). NVIDIA hereby expressly objects to applying any customer general terms and conditions with regards to the purchase of the NVIDIA product referenced in this document. No contractual obligations are formed either directly or indirectly by this document.

#### VESA DisplayPort

DisplayPort and DisplayPort Compliance Logo, DisplayPort Compliance Logo for Dual-mode Sources, and DisplayPort Compliance Logo for Active Cables are trademarks owned by the Video Electronics Standards Association in the United States and other countries.

#### HDMI

HDMI, the HDMI logo, and High-Definition Multimedia Interface are trademarks or registered trademarks of HDMI Licensing LLC.

#### OpenCL

OpenCL is a trademark of Apple Inc. used under license to the Khronos Group Inc.

#### Trademarks

NVIDIA and the NVIDIA logo are trademarks or registered trademarks of NVIDIA Corporation in the U.S. and other countries. Other company and product names may be trademarks of the respective companies with which they are associated.

### Copyright

© 2007-2021 NVIDIA Corporation. All rights reserved.

