1.b)
$$125Hz^{2} = \frac{16MHz}{4} \cdot \frac{1}{(P+1)}$$
, $P = \frac{10MHz}{125Hz} \cdot 4 - 1 = \boxed{19999}$

1.C)
$$D=1$$
 requires $P=\frac{10MHz}{125Hz}-1=79999$.

A 16 bit register can only hold P= 2 = (= 65536. So using D=1 may be a balidea for this frequency because we would have to ose an extra 16 bit register to make a 32 bit register.

2.4)
$$1Hz = \frac{16MHz}{250} \cdot \frac{1}{(1+1)}$$
 Dinax = 256

$$P = \frac{16MHz}{256Hz} - 1 = 39062$$

$$p = \frac{10MH^2}{50Hz.250} - 1 = 780.3 \le 780$$

$$P = \frac{10MH^{2}}{50Hz \cdot 250} - 1 = \frac{786.35.780}{50Hz \cdot 250} = \frac{16.10^{-3} Hz}{256} = \frac{16.10^{-3} Hz}{256} = \frac{16.10^{-3} Hz}{256}$$