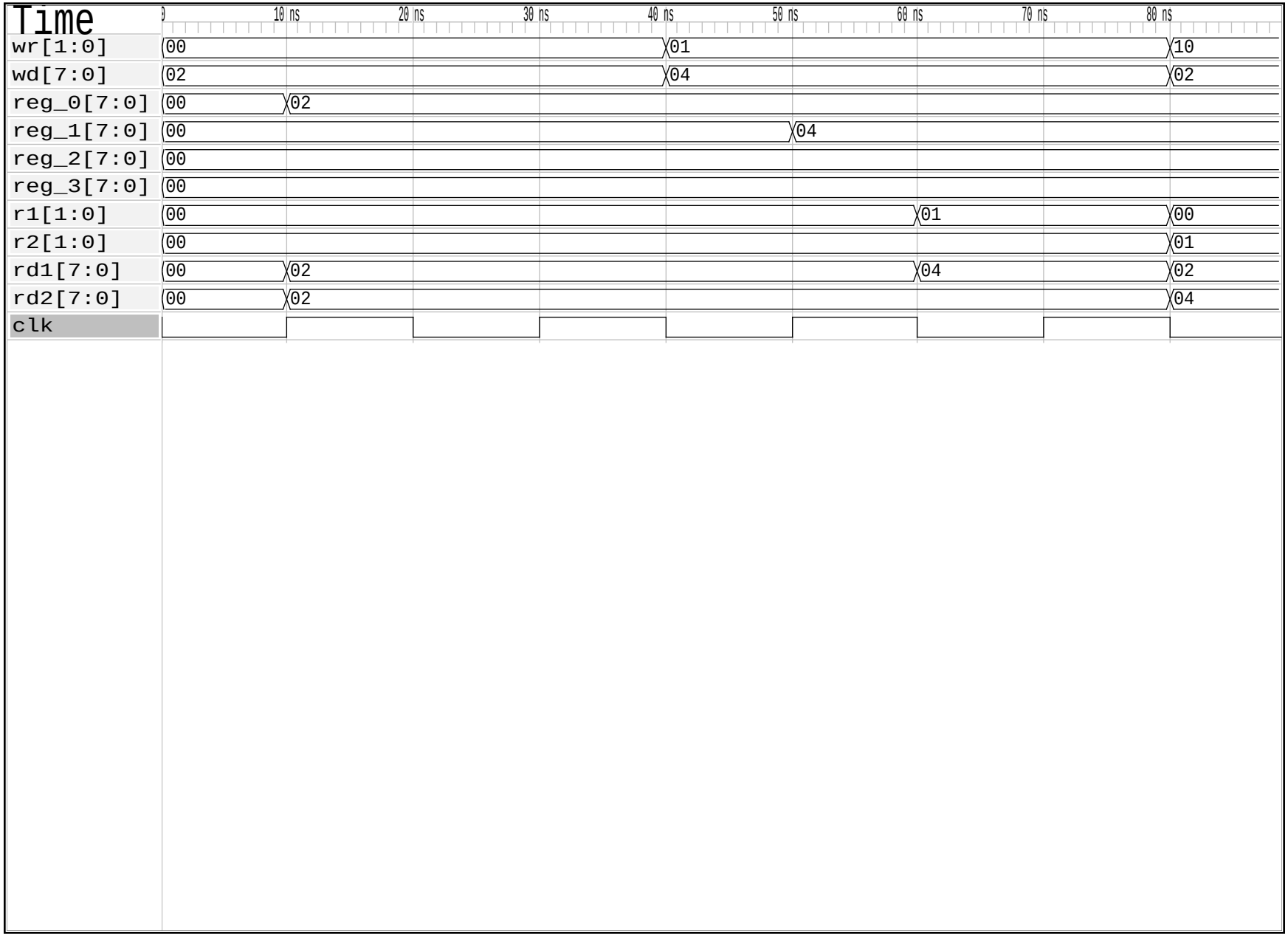
Time
0
10 ns
20 ns
30 ns
40 ns
50 ns
60 ns
70 ns
80 ns
wr[1:0]
00
01
10
wd[7:0]
02
04
02
reg_0[7:0]
00
02
reg_1[7:0]
00
04
reg_2[7:0]
00
reg_3[7:0]
00
r1[1:0]
00
01
00
r2[1:0]
00
01
rd1[7:0]
00
02
04
02
rd2[7:0]
00
02
04
clk