# CHIP-FIRING AND THE SANDPILE GROUP OF THE $R_{10}$ MATROID

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ABSTRACT. A celebrated result of Seymour is that all *regular matroids* are built up from *graphic matroids*, *cographic matroids*, and a specific 10 element rank 5 matroid called $R_{10}$. In this article, we give a simple description of chip-firing on $R_{10}$ using complex numbers on the vertices of a pentagon, and link to an app where readers can play around with the combinatorial dynamics of the system. We also provide an easy to describe set of representatives for each of the 162 equivalence classes that make up the *sandpile group* of $R_{10}$.

> ALEX'S NOTE: We still need to link the app.
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> ALEX'S NOTE: Also, I think it might be nice to have a screenshot from the app somewhere for people too lazy to click the link.

## 1. INTRODUCTION

Several foundational results in *chip-firing*, most notably the *matrix-tree theorem*, have been shown to generalize naturally from graphs to *regular matroids* [?]. This is possible through the perspective of *circuit* and *cocircuit* lattices. However, even in articles where the results generalize to regular matroids, most, if not all, of the worked-out examples tend to be of *graphic matroids*.

In 1980, Seymour showed that every regular matroid can be formed from *1-sums*, *2-sums*, and *3-sums* of graphic matroids, co-graphic matroids, and a specific matroid called $R_{10}$ [?]. For this reason, we suspect that understanding chip-firing on $R_{10}$ could be the key to understanding nuances of chip-firing on regular matroids in general.

The term *chip-firing* can mean different things to different authors, but it generally refers to a discrete dynamical system that can be described in terms of a *configuration* of objects (often visualized as poker chips) which can be rearranged through operations called *firing moves*. See [?] for many more details about the history and applications of chip-firing.

For this article, we use chip-firing to understand equivalence classes of the *sandpile group* of $R_{10}$. The *sandpile group* of a graph, (which also goes by many other names, such as the *critical group*, or *Jacobian*) is a finite abelian group whose cardinality is equal to the number of spanning trees [?]. The sandpile group of a *regular matroid* is a finite abelian group whose cardinality is equal to the number of *bases* [?]. To compute the sandpile group of a regular matroid, we quotient the free abelian group on the ground set by an equivalence relation given by a specific collection of firing moves.

Using linear algebra, one can show that the sandpile group of $R_{10}$ is equal to $(\mathbb{Z}/3\mathbb{Z})^3 \oplus (\mathbb{Z}/6\mathbb{Z})$, a finite abelian group of order 162, which contains exactly one element of order 2. Furthermore, the matroid $R_{10}$ is equipped with certain symmetries that allow for an elegant chip-firing description using the *Gaussian integers* $\mathbb{Z}[i]$. Using this description, we give a set of representatives for all of the equivalence classes of $S(R_{10})$, as well as an algorithm from an arbitrary configuration to one of these representatives.

## 2. BACKGROUND

For this article, we do not need the full generality of matroid theory. Instead, we can restrict to *regular matroids*, which can be described in terms of a matrix.

Let $A$ be a matrix, $n$ be the number of columns of $A$, and $F$ be a field. The *ground set* $E$ is a set whose elements correspond to the columns of $A$. If a subset of $E$ corresponds to columns which are linearly independent over $F$, then it is called an *independent* set. The maximal independent sets are called *bases*, and the collection of bases is called $\mathbf{B}$.

A matroid is *representable* if it can be represented by some matrix over some field. The classical definition of a *regular matroid* is a matroid that is representable over *any* field. We will use the following definition, which was proven equivalent by Tutte [**?**].

**Definition 2.1.** A *regular matroid* is a matroid that is representable over $\mathbb{R}$ by a *totally unimodular* matrix $A$, where a *totally unimodular* matrix is an integer matrix such that every minor is in the set $\{0, 1, -1\}$.

It is well known that any graph $G$ corresponds to a regular matroid $M(G)$, whose ground set corresponds to the edges of $G$, and whose bases correspond to *spanning trees* of $G$ [**?**, Section 5.1]. However, the other direction does not hold: there exist regular matroids whose bases do not correspond to the spanning trees of a graph. One such matroid is $R_{10}$, which will be the main focus of Section 3.

Let $M = (E, \mathbf{B})$ be a regular matroid represented by a totally unimodular matrix $A$ over $\mathbb{R}$. Suppose that we assign an integer number of *chips* to each element of the ground set of $E$. We call such an assignment a *chip configuration*, and represent it as an element of the lattice $\mathbb{Z}^E$.

Let $\mathrm{im}_{\mathbb{Z}} A$ be the sublattice of $\mathbb{Z}^E$ made up of integer linear combinations of rows of $A$. Let $\ker_{\mathbb{Z}} A$ be the sublattice of $\mathbb{Z}^E$ made up of vectors mapped to the all zeros vector by $A$.

**Definition 2.2.** Suppose that $M$ is a matroid represented by a totally unimodular matrix $A$ over $\mathbb{R}$. The *sandpile group* of $M$ is given by:

$$S(M) := \frac{\mathbb{Z}^E}{\mathrm{im}_{\mathbb{Z}} A \oplus \ker_{\mathbb{Z}} A}$$

Note that Definition 2.2 does not meaningfully depend on the choice of totally unimodular matrix $A$ representing $M$, beyond reordering or reversing the orientation of the elements of the ground set. For further discussion, see [**?**, Section 2.1] and [**?**].

**Remark 2.3.** There are a lot of different names for the group given in Definition 2.2, and these names are not always used completely interchangeably. For example, the main group considered in [**?**] is called the *Jacobian group*, which is defined as the *determinant group* of $\mathrm{im}_{\mathbb{Z}} A$. Nevertheless, this group is shown to be isomorphic to the group in Definition 2.2. In [**?**], the authors generalize to work with *representable matroids* which may not be regular. In this context, Definition 2.2 is called the *cutflow group*, while the determinant of $\mathrm{im}_{\mathbb{Z}} A$ is called the *critical group*. Without the assumption of regularity, these groups are related, but distinct.

It can be a bit cumbersome to work with both the image and the kernel of a matrix, but there is a trick to rewrite $\mathrm{im}_{\mathbb{Z}} A \oplus \ker_{\mathbb{Z}} A$ as the image of a larger matrix. Throughout this article, we use $n$ for the size of the ground set of a regular matroid $M$, and $r$ for the *rank* of $M$, which is equal to the minimal number of rows of a matroid representing $M$.

Suppose that an $r \times n$ totally unimodular matrix $A$ is of the form

$$(2.1) \qquad A = \begin{bmatrix} I_r & D \end{bmatrix},$$

where $I_r$ is the $r \times r$ identity matrix and $D$ is an $r \times (n-r)$ integer matrix. Note that $A$ can always be put in this form by choosing a basis for the first $r$ columns, and that applying row operations. Then, we define

$$(2.2) \qquad A^* = \begin{bmatrix} -D^t & I_{n-r} \end{bmatrix},$$

where $D^t$ is the transpose of $D$ and $I_{n-r}$ is again the identity matrix. Additionally, we define

$$(2.3) \qquad K = \begin{bmatrix} A \\ A^* \end{bmatrix} = \begin{bmatrix} I_r & D \\ -D^t & I_{n-r} \end{bmatrix},$$

It is well-known and straightforward to verify that $\ker_{\mathbb{Z}} A = \mathrm{im}_{\mathbb{Z}} A^*$ [**?**, Corollary 4.6.6]. In particular, this means that if $A$ represents a regular matroid $M$, then

$$(2.4) \qquad S(M) = \frac{\mathbb{Z}^E}{\mathrm{im}_{\mathbb{Z}} A \oplus \ker_{\mathbb{Z}} A} = \frac{\mathbb{Z}^E}{\mathrm{im}_{\mathbb{Z}} A \oplus \mathrm{im}_{\mathbb{Z}} A^*} = \frac{\mathbb{Z}^E}{\mathrm{im}_{\mathbb{Z}} K} \quad, \text{ where } K = \begin{bmatrix} I_r & D \\ -D^t & I_{n-r} \end{bmatrix}.$$

This quotient group $\mathbb{Z}^E / \mathrm{im}_{\mathbb{Z}} K$ is called the *cokernel* of $K$ over $\mathbb{Z}$, and is written $\mathrm{coker}_{\mathbb{Z}} K$. The following proposition gives a summary of our observations after Remark 2.3.

**Proposition 2.4.** *Let $M$ be a regular matroid that is represented over $\mathbb{R}$ by a totally unimodular matrix* $A = \begin{bmatrix} I_r & D \end{bmatrix}$. *Furthermore, define* $A^* = \begin{bmatrix} -D^t & I_{n-r} \end{bmatrix}$ *and* $K = \begin{bmatrix} A \\ A^* \end{bmatrix} = \begin{bmatrix} I_r & D \\ -D^t & I_{n-r} \end{bmatrix}$. *Then, the* sandpile *group $S(M)$ is equal to* $\operatorname{coker}_{\mathbb{Z}} K$.

One way to think of the sandpile group of $M$ is in terms of *chip configurations* and *firings moves*. Begin by thinking of an element $C \in \mathbb{Z}^E$ as an assignment of an integer number of *chips* assigned to each element of $E$. If we add or subtract a row of $K$, this redistributes the chips to give a new chip configuration $C'$ such that $C$ and $C'$ are equivalent as elements of $S(M)$. We say that adding or subtracting a row of $K$ is a *firing move* on the chip configuration, and we say that two chip configurations are *firing equivalent* if one can be reached from the other by a sequence of firing moves. Algebraically speaking, chip configurations $C$ and $C'$ are firing equivalent if and only if $C - C' \in \operatorname{im}_{\mathbb{Z}} K$. Equivalently, this holds precisely when $K^{-1}(C - C')$ is an integer vector.

**Remark 2.5.** Typically, *chip configurations* refer to a collection of chips on the vertices of a graph, instead of on the ground set of a regular matroid. In this context, a *firing move* removes $\deg(v)$ chips from a vertex $v$, and sends one to each neighbor of $v$. In this context, it is common to disallow firings which result in a negative number of chips on the firing vertex. For more information about this more traditional perspective, see [?].

In our construction, firings are more abstract, and the total number of chips is not necessarily preserved. Furthermore, we place no restrictions on when a firing is allowed, and it is not a problem for elements of $E$ to have negative chips.

Our interest in the sandpile group is primarily motivated by the following variation of *Kirchhoff's matrix-tree theorem*, which is proven using the *Cauchy-Binet formula*.

**Theorem 2.6.** [?, Theorem 3.3.2] *For any regular matroid $M$, the size of the sandpile group $S(M)$ is equal to the number of bases of $M$.*

Using linear algebra techniques, we can calculate $S(M)$ precisely.

**Proposition 2.7.** [?, Section 8] *For any square integer matrix $K$, the group $\operatorname{coker}_{\mathbb{Z}}(K)$ has cardinality equal to $|\det(K)|$. More precisely, let $a_1, a_2, \ldots, a_n$ be the diagonal entries of the* smith normal form *of $K$. Then,* $\operatorname{coker}_{\mathbb{Z}}(K) \cong (\mathbb{Z}/a_1\mathbb{Z}) \oplus (\mathbb{Z}/a_2\mathbb{Z}) \oplus \cdots \oplus (\mathbb{Z}/a_n\mathbb{Z})$.

In the next section, we will focus on specific properties of $R_{10}$, but first let us give a simple example of chip-firing on a regular matroid.

**Example 2.8.** Let $M$ be the regular matroid represented by the matrix

$$ A = \begin{bmatrix} 1 & 0 & -1 \\ 0 & 1 & -1 \end{bmatrix}. \quad \text{This means that } A^* = \begin{bmatrix} 1 & 1 & 1 \end{bmatrix} \quad \text{and} \quad K = \begin{bmatrix} 1 & 0 & -1 \\ 0 & 1 & -1 \\ 1 & 1 & 1 \end{bmatrix}. $$

By computing the Smith normal form of $K$, one can calculate that $S(M) = \mathbb{Z}/3\mathbb{Z}$, which means that there are 3 firing equivalence classes of chip configurations.

A chip configuration on $M$ can be written as a triple of integers $(a, b, c)$. We claim that each chip configuration is firing equivalent to exactly one of the chip configurations $(0, 0, 0)$, $(1, 0, 0)$, or $(2, 0, 0)$. In other words, these form a set of representatives for the elements of $S(M)$. One way to prove this claim is through linear algebra, noting that chip configurations $C$ and $C'$ are firing equivalent if and only if $K^{-1}(C - C') \in \mathbb{Z}^3$.

Alternatively, we can also prove the claim by showing how to go from an arbitrary configuration to one of the three representatives using a sequence of firings. In particular, suppose that we are given an arbitrary chip configuration $(a, b, c)$. First, we can apply the $(1, 1, 1)$ firing move a total of $-\lfloor (a+b+c)/3 \rfloor$ times. Since each firing move subtracts 3 chips, the resulting configuration will have a total of 0, 1, or 2 chips. Next, we can add or subtract $(0, 1, -1)$ until no chips remain in the second position. Finally, we can add or subtract $(1, 0, -1)$ until no chips remain in the third position. These firing moves do not change the total number of chips, so we end up with 0, 1, or 2 chips in the first position, and no chips on the other two positions.

## 3. Chip-firing on $R_{10}$

Consider the totally unimodular matrix

(3.1)
$$\mathcal{A} = \begin{bmatrix} 1 & 0 & 0 & 0 & 0 & -1 & 1 & 0 & 0 & 1 \\ 0 & 1 & 0 & 0 & 0 & 1 & -1 & 1 & 0 & 0 \\ 0 & 0 & 1 & 0 & 0 & 0 & 1 & -1 & 1 & 0 \\ 0 & 0 & 0 & 1 & 0 & 0 & 0 & 1 & -1 & 1 \\ 0 & 0 & 0 & 0 & 1 & 1 & 0 & 0 & 1 & -1 \end{bmatrix}.$$

**Definition 3.1.** The matroid represented by $\mathcal{A}$ over $\mathbb{R}$ is called $R_{10}$.

We can write $\mathcal{A}$ as the block matrix $\begin{bmatrix} I_5 & \mathcal{D} \end{bmatrix}$, where $\mathcal{D}$ is the $5 \times 5$ matrix defined by:

$$\mathcal{D}_{ij} = \begin{cases} 1 & \text{if } i = j, \\ -1 & \text{if } i - j \equiv 1 \pmod 5, \\ 0 & \text{otherwise.} \end{cases}$$

Since $\mathcal{A}$ is in the format from (2.1), we can use (2.2) to write $\mathcal{A}^* = \begin{bmatrix} -\mathcal{D}^t & I_5 \end{bmatrix} = \begin{bmatrix} -\mathcal{D} & I_5 \end{bmatrix}$, where the final equality holds because $\mathcal{D}$ is symmetric. Let $\mathcal{K}$ be the matrix

$$\mathcal{K} = \begin{bmatrix} \mathcal{A} \\ \mathcal{A}^* \end{bmatrix} = \begin{bmatrix} I_5 & \mathcal{D} \\ -\mathcal{D} & I_5 \end{bmatrix} = \begin{bmatrix} 1 & 0 & 0 & 0 & 0 & -1 & 1 & 0 & 0 & 1 \\ 0 & 1 & 0 & 0 & 0 & 1 & -1 & 1 & 0 & 0 \\ 0 & 0 & 1 & 0 & 0 & 0 & 1 & -1 & 1 & 0 \\ 0 & 0 & 0 & 1 & 0 & 0 & 0 & 1 & -1 & 1 \\ 0 & 0 & 0 & 0 & 1 & 1 & 0 & 0 & 1 & -1 \\ 1 & -1 & 0 & 0 & -1 & 1 & 0 & 0 & 0 & 0 \\ -1 & 1 & -1 & 0 & 0 & 0 & 1 & 0 & 0 & 0 \\ 0 & -1 & 1 & -1 & 0 & 0 & 0 & 1 & 0 & 0 \\ 0 & 0 & -1 & 1 & -1 & 0 & 0 & 0 & 1 & 0 \\ -1 & 0 & 0 & -1 & 1 & 0 & 0 & 0 & 0 & 1 \end{bmatrix}$$

By Proposition 2.7, and the help of a computer algebra system, we can calculate that $|S(R_{10})| = 162$, which is also the number of bases of $R_{10}$. More precisely, after computing the Smith normal form, we find that $S(R_{10}) \cong (\mathbb{Z}/3\mathbb{Z})^3 \oplus (\mathbb{Z}/6\mathbb{Z})$.

While these calculations can be done for any regular matroid, the fact that $\mathcal{D}$ is not only square, but also symmetric, allows for an alternate perspective using the *Gaussian integers* $\mathbb{Z}[i]$.

Let $\varphi$ be the bijection between $\mathbb{Z}^{10}$ and $\mathbb{Z}[i]^5$ such that

$$\varphi((v_0, v_1, v_2, v_3, v_4, v_5, v_6, v_7, v_8, v_9)^t) \to (v_0 - v_5 i, \ v_1 - v_6 i, \ v_2 - v_7 i, \ v_3 - v_8 i, \ v_4 - v_9 i)^t.$$

Furthermore, let $\overline{\mathcal{K}}$ be the $5 \times 5$ matrix in $\mathbb{Z}[i]^5$ given by

$$\overline{\mathcal{K}} = I_5 - \mathcal{D}i = \begin{bmatrix} 1+i & -i & 0 & 0 & -i \\ -i & 1+i & -i & 0 & 0 \\ 0 & -i & 1+i & -i & 0 \\ 0 & 0 & -i & 1+i & -i \\ -i & 0 & 0 & -i & 1+i \end{bmatrix}$$

From this construction, it is straightforward to verify the following equality.

**Proposition 3.2.** *Consider the matrices $\mathcal{K}$ and $\overline{\mathcal{K}}$ from above. For any vector $\mathbf{v} \in \mathbb{Z}^{10}$, we have $\varphi(\mathcal{K}\mathbf{v}) = \overline{\mathcal{K}}\varphi(\mathbf{v})$.*

This proposition tells us that $\mathrm{im}_{\mathbb{Z}}(\mathcal{K})$ and $\mathrm{im}_{\mathbb{Z}[i]}(\overline{\mathcal{K}})$ are isomorphic, which also means that $\mathrm{coker}_{\mathbb{Z}}(\mathcal{K})$ and $\mathrm{coker}_{\mathbb{Z}[i]}(\overline{\mathcal{K}})$ are isomorphic. This fact implies the following corollary.

**Corollary 3.3.** *The sandpile group $S(R_{10})$ is equal to $\mathrm{coker}_{\mathbb{Z}[i]}(\overline{\mathcal{K}})$.*
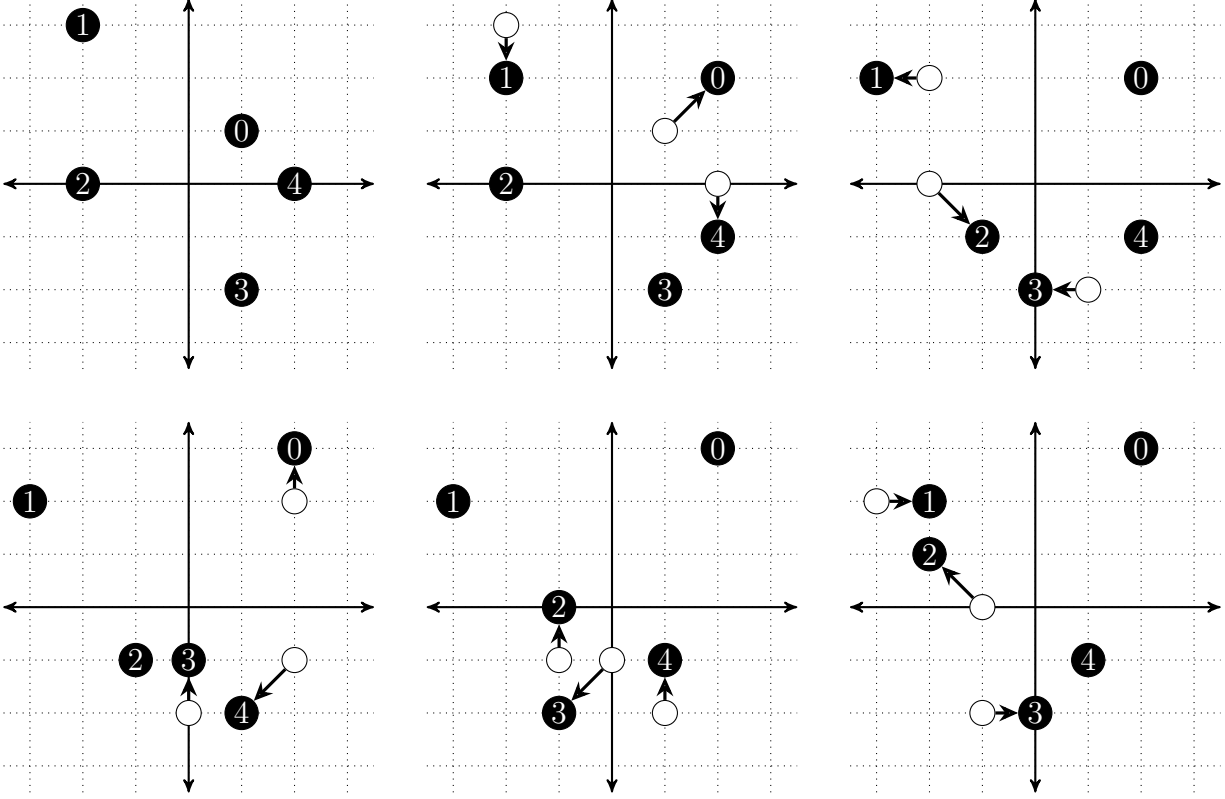
FIGURE 1. This figure shows one way to visualize chip-firing on $R_{10}$. Chip-configurations correspond to 5 labeled nodes on lattice points on the complex plane. For example, the initial configuration is $(1+i, -2+3i, -2, 1-2i, 2)$ and the final configuration is $(2+3i, -2+2i, -2+i, -2i, 1-i)$. This final configuration is reached after an A-firing of 0, a B-firing of 2, a (-A)-firing of 4, a (-A)-firing of 3, and a (-B)-firing of 2. Note that there are no restrictions in this construction about multiple nodes occupying the same position; we only avoided this for the example to make things easier to draw.

This corollary introduces an approachable way to interpret $S(R_{10})$. Imagine a pentagon such that each node can have two kinds of chips, real and imaginary.[1] There are also four kinds of firing moves, which only differ by multiplication by $i$. Each of these moves impacts the chips on three adjacent nodes.

(A) Add $1+i$ to a node and add $-i$ to each neighboring node.
(B) Add $-1+i$ to a node and add $1$ to each neighboring node.
(-A) Add $-1-i$ to a node and add $i$ to each neighboring node.
(-B) Add $1-i$ to a node and add $-1$ to each neighboring node.

We will call these moves *A-firings*, *B-firings*, *(-A)-firings*, and *(-B)-firings* respectively. Two chip-configurations on the pentagon are firing equivalent if and only if one can be reached from the other by some sequence of A, B, (-A), and (-B) firings.

Instead of a collection of real and imaginary chips on a pentagon, chip configurations on $R_{10}$ can also be visualized as collections of nodes labeled 0 through 4 on lattice points on the complex plane (see Figure 1). A firing move consists of moving one node diagonally $\sqrt{2}$ units to another lattice point, and moving the two nodes with adjacent labels (mod 5) one unit in a direction $3\pi/4$ clockwise from the chosen node.

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[1] We use the term *node* in place of *vertex* to help distinguish our version of chip-firing from the more traditional version on a graph (which is mentioned in Remark 2.5). Each node of the pentagon corresponds to a pair of elements of the ground set of $R_{10}$. Note that the ground set of a matroid can be thought of as an abstraction of the *edges* of a graph, not the *vertices*.

3.1. **Representatives of $S(R_{10})$.** Now, we will find a nice set of representatives for each equivalence class of $S(R_{10})$.

**Lemma 3.4.** *Every equivalence class of $S(R_{10})$ contains chip configurations which do not have any imaginary chips.*

*Proof.* Suppose that a node has $a$ real chips and $b$ imaginary chips. If $b$ is positive, we perform a (-B)-firing to the node $b$ times. If $b$ is negative, we perform a type B firing to the node $-b$ times. After this, the node will have no imaginary chips, and these firings do not impact the number of imaginary chips on any other node. After going through the process for all five of the vertices, there will be no imaginary chips remaining. □

Now that we have shown that we can get rid of imaginary chips, we can find chip-configurations with specified restrictions on the number of real chips at each node. One useful fact that will assist in this technique is the following.

**Lemma 3.5.** *There exists a unique element of $S(R_{10})$ with order 2.*

*Proof.* This follows quickly from the fact that $S(R_{10}) \cong (\mathbb{Z}/3\mathbb{Z})^3 \oplus (\mathbb{Z}/6\mathbb{Z})$. Using the notation of this decomposition, the only element with order 2 is $[0, 0, 0, 3]$. □

For a chip configuration $C$, we will write $C \sim H$ if the equivalence class containing $C$ is the element of order 2 in $S(R_{10})$. For chip configurations $C$ and $C'$, we will also write $C \sim C'$ if $C$ and $C'$ are firing equivalent.

**Lemma 3.6.** *Let $C$ be the chip configuration which places 1 chip on each node. Let $C'$ be the chip configuration that places 3 chips on one node and 0 chips on the other node. Then, $C \sim H$ and $C' \sim H$.*

*Proof.* One can verify that

$$(3.2) \qquad \overline{\mathcal{K}}^{-1}(C) = \frac{1}{2} \begin{bmatrix} 1+i \\ 1+i \\ 1+i \\ 1+i \\ 1+i \end{bmatrix} \text{ and } \overline{\mathcal{K}}^{-1}(C') = \frac{1}{2} \begin{bmatrix} 3-i \\ 1+i \\ -1+i \\ -1+i \\ 1+i \end{bmatrix}.$$

In both cases, we obtain a vector that is half of an element of $\mathbb{Z}[i]^5$. □

**Remark 3.7.** We can also use (3.2) to determine which firings are required to add 2 chips to every node, or to add 6 chips to a specific node.

For the first case, we do an A-firing and a B-firing at every node. For the second case, we do three A-firings and a (-B)-firing on the specified node, one A-firing and one B-firing on each adjacent node, and one (-A)-firing and one B-firing on each non-adjacent node.

**Theorem 3.8.** *Consider the five node model for chip-firing on $R_{10}$ and choose one particular node to act as the distinguished node. We obtain one representative for each equivalence class of $S(R_{10})$ by placing either 0 or 3 chips on the distinguished node, and either 0, 1, or 2 chips on every other node.*

*Proof.* To prove the theorem, we will show that every chip-configuration is firing equivalent to one of the given form. Since there are 162 total chip configurations of this form, and this is also the size of $S(R_{10})$, we can then conclude that each representative must be in a different firing equivalence class.

Begin with an arbitrary chip-configuration $C$. By Lemma 3.4, there exists some $C'$ with no imaginary chips such that $C' \sim C$. Suppose that $C'$ has $a$ chips on the distinguished node. We can subtract one chip from every node $a$ times to get a new configuration $C''$. By Lemma 3.6, we know that $C'' \sim (C' + aH)$.

Next, for any node that doesn't have 0, 1, or 2 chips, we can add or subtract 3 chips until it does. This gives a new configuration $C'''$ which has no chips on the distinguished node, and 0, 1, or 2 chips on each other node. By Lemma 3.6, if $b$ is the total number of times that we add/subtract 3 chips, then

$$C''' \sim (C'' + bH) \sim (C' + (a+b)H) \sim (C + (a+b)H).$$

Finally, since $2H$ is equivalent to the identity, we must either have $C''' \sim C$ or $C''' \sim (C + H)$, depending on the parity of $a + b$. In the first case, we are done, and $C'''$ is the desired representative. In the second case, we obtain the desired representative from $C'''$ after adding 3 chips to the distinguished node. □

The last step in the proof of Theorem 3.8 required determining whether $C''' \sim C$ or $C''' \sim (C + H)$. One approach would be to check if $\overline{\mathcal{K}}^{-1}(C''' - C) \in \mathbb{Z}[i]^5$, but there is an even simpler way using the following lemma.

**Lemma 3.9.** *Let $C$ and $C'$ be chip configurations such that $C \sim C' + H$. Then, the total number of chips on $C$ and the total number of chips on $C'$ must have opposite parity (counting both real and imaginary chips).*

*Proof.* Each of the four kinds of firings has a net effect of adding or subtracting exactly 2 chips to the configuration. In particular, a sequence of firings cannot change the parity of the total number of chips. Furthermore, both of the chip configurations identified in Lemma 3.6 have an odd number of total chips. The result follows. $\square$

The following algorithm gives a simple procedure to determine a representative from Theorem 3.8 for each equivalence class of $S(R_{10})$. For the most part, the algorithm follows the proof of Theorem 3.8, but we avoid firing language to make the argument easier to parse, and we take advantage of Lemma 3.9.

**Algorithm 3.10.** *Input: An element of $\mathbb{Z}[i]^5$, thought of as a chip configuration.*

1. *Label the vertices $v_0, v_1, v_2, v_3, v_4$ and let $\psi(v_k)$ be the number of imaginary chips at node $v_k$.*
2. *At each node $v_k$, add $\psi(v_k) - \psi(v_{k+1}) - \psi(v_{k-1})$ real chips (where indices are mod 5).*
3. *Remove all imaginary chips.*
4. *Introduce a boolean variable isEven.*
5. *Count the total chips. If this total is even, set isEven = True, otherwise, set isEven = False.*
6. *Subtract the number of chips at the distinguished node from every node*
7. *At each node, set the number of chips to the remainder after dividing by 3.*
8. *Count the total chips. If the total is even and isEven = False, or if the total is odd and isEven = True, then add 3 chips to the distinguished node.*

*Output: the resulting configuration.*

*Proof.* The first 3 steps use $B$- firings and $(-B)$-firings to get rid of all imaginary chips (see Lemma 3.4). Steps 4 and 5 keep track of the parity of the configuration so that we can use Lemma 3.9 to distinguish between chip-configurations that differ by $H$. Steps 6 and 7 use Lemma 3.6 to add chip configurations equal to $H$. Finally, Step 8 applies our earlier parity reasoning. $\square$

**Example 3.11.** Let us apply Algorithm 3.10 to find a nice representative for the equivalence class of $S(R_{10})$ that contains the chip configuration $(3 + i, 4 - 6i, 7 + i, -8 - 8i, 3)$ (with $v_0$ as our distinguished node).

In the initial configuration, we have $\psi(v_0) = 1$, $\psi(v_1) = -6$, $\psi(v_2) = 1$, and $\psi(v_4) = -8$, $\psi(v_5) = 0$. In Step 2, we add 7 chips to $v_0$, $-8$ chips to $v_1$, 15 chips to $v_2$, $-9$ chips to $v_3$, and 7 chips to $v_4$. After Step 3, we have the chip configuration $(10, -4, 22, -17, 10)$. In Steps 4 and 5, we set isEven = False. After Steps 6 and 7, we have the chip configuration $(0, 1, 0, 0, 0)$. Finally, since the total number of chips is still odd, we do not change anything in Step 8. Thus, $(0, 1, 0, 0, 0)$ is our representative configuration.

We can verify that this configuration is in the same equivalence class as our original configuration by computing

$$\overline{\mathcal{K}}^{-1}\left(\begin{bmatrix} 0 \\ 1 \\ 0 \\ 0 \\ 0 \end{bmatrix} - \begin{bmatrix} 3 + i \\ 4 - 6i \\ 7 + i \\ -8 - 8i \\ 3 \end{bmatrix}\right) = \begin{bmatrix} -5 - i \\ -4 + i \\ -4 + 3i \\ 4 - i \\ -1 \end{bmatrix}$$

The resulting vector is in $\mathbb{Z}[i]^5$, so the two chip configurations must be firing equivalent. More explicitly, the real part of the vector tells the number of $A$-firings required at each node, while the imaginary part tells the number of $B$-firings required at each node, in order to get from the initial configuration to $(0, 1, 0, 0, 0)$.

> ALEX'S NOTE: The end of this section would talk about sandpile torsor stuff, but I don't know if I like it. You can ignore the rest of the paper for now, and I'll decide in the future how to address this stuff (if at all). Maybe, I'll just throw a little something in Remark **??**.

3.2. **The orientation perspective.** To end this article, we want to briefly comment on an alternate approach to defining sandpile groups of regular matroids, which can also be used to produce different sets of representatives of $S(R_{10})$. Instead of assigning an integer to each element of the ground set, one can instead work with *orientations* on the ground set. This perspective is equivalent to requiring each node of the pentagon to have either 0 or 1 real chips and either 0 or 1 imaginary chips.

One difference with this alternate perspective is that any "firing move" must maintain the restriction that every node is assigned either 0 or 1 real chips and either 0 or 1 imaginary chips. However, this is not always possible when the only firing moves allowed are $A$, $B$, $(-A)$ and $(-B)$ firings. For example, since every move has both positive and negative coefficients, no such move would be possible from the all zeros configuration.

Nevertheless, this problem goes away if we also allow for simultaneous moves. For example, if we perform all 5 $B$ moves simultaneously, this adds 1+i moves to each node, which is valid in this framework from the all zeros configuration. Furthermore, the allowable moves correspond to reversing *circuits* or *cocircuits* of the matroid. For many more details on this perspective, see [**?**].

> ALEX'S NOTE: I might add something here about how future work could find relationships between bases and equivalence classes of $S(R_{10})$, while also citing a few approaches that can be used.

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