# Gregory Ka'ulukoakūpono Ng

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#### Education: —

**University of Portland** – Bachelor of Science Elec. Engineering

- GPA: 3.6 | Graduating: May 2021
- Major: Electrical Engineering, Minor: Computer Science Candidate
- Relevant Coursework: Embedded Systems Design, Digital Systems Design, Advanced Analog Electronics
- Relevant Coursework: Object Oriented Design, Data Structures, UNIX Tools Lab

## Work Experience: —

# **Physics Lab Instructor and Teaching Assistant**

August 2018 – Present

University of Portland: Portland, OR

- Instructed, inspired, and adapted to 20-30 students every semester to suit various learning styles
- Improved physics curriculum by exercising hands-on learning and individualized teaching methods
- Mentored fellow new physics peer leaders to help optimize their teaching strategy/style

# **Technical Projects:** -

# **Tektronix Remotely Aimed Radio Frequency and Video Monitor Project**

Group Project | Ongoing

University of Portland: Portland, OR

- Producing a local and remote system to detect RF signals via local field device and display RSA spectrum data alongside camera feed on remote interface.
- Developing local system remote controls, by spearheading research and implementation of remote motor control signals.

## **Exploding Kittens App Project**

Group Project | Fall 2020

University of Portland: Portland, OR

- Developed functional "Exploding Kittens" game by utilizing object-oriented design in Java with a strong understanding of a game framework.
- Led team of 3 engineers in app design, development, and testing.

### **Mastermind Game Project**

Group Project | Fall 2019

University of Portland: Portland, OR

• Created Mastermind game in Assembly Code on a PIC 18 microcontroller utilizing Beta ALU instructions and indirect addressing with LCD interactable interface

### **Serial Smart Sorter Project**

Group Project | Spring 2019

University of Portland: Portland, OR

• Developed 16 digital serial sorting cells used for sorting 16 8-bit numbers using an FPGA Zedboard while utilizing structural Verilog on Vivado.

### Technical Skills: —

Assembly Code	PSpice	Verilog	MATLAB
Java	C++	С	Microsoft Excel