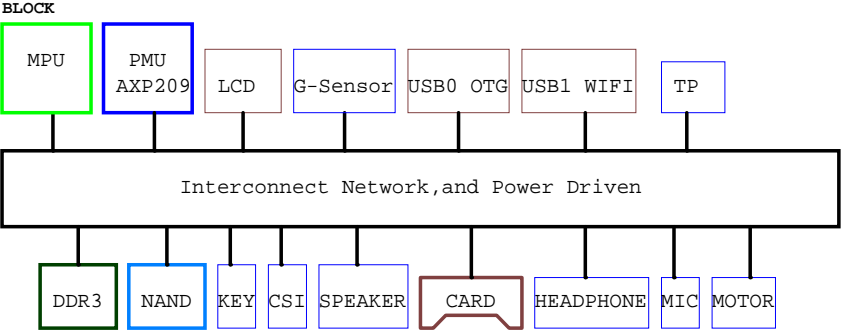


COVER



GPIO ASSIGNMENT

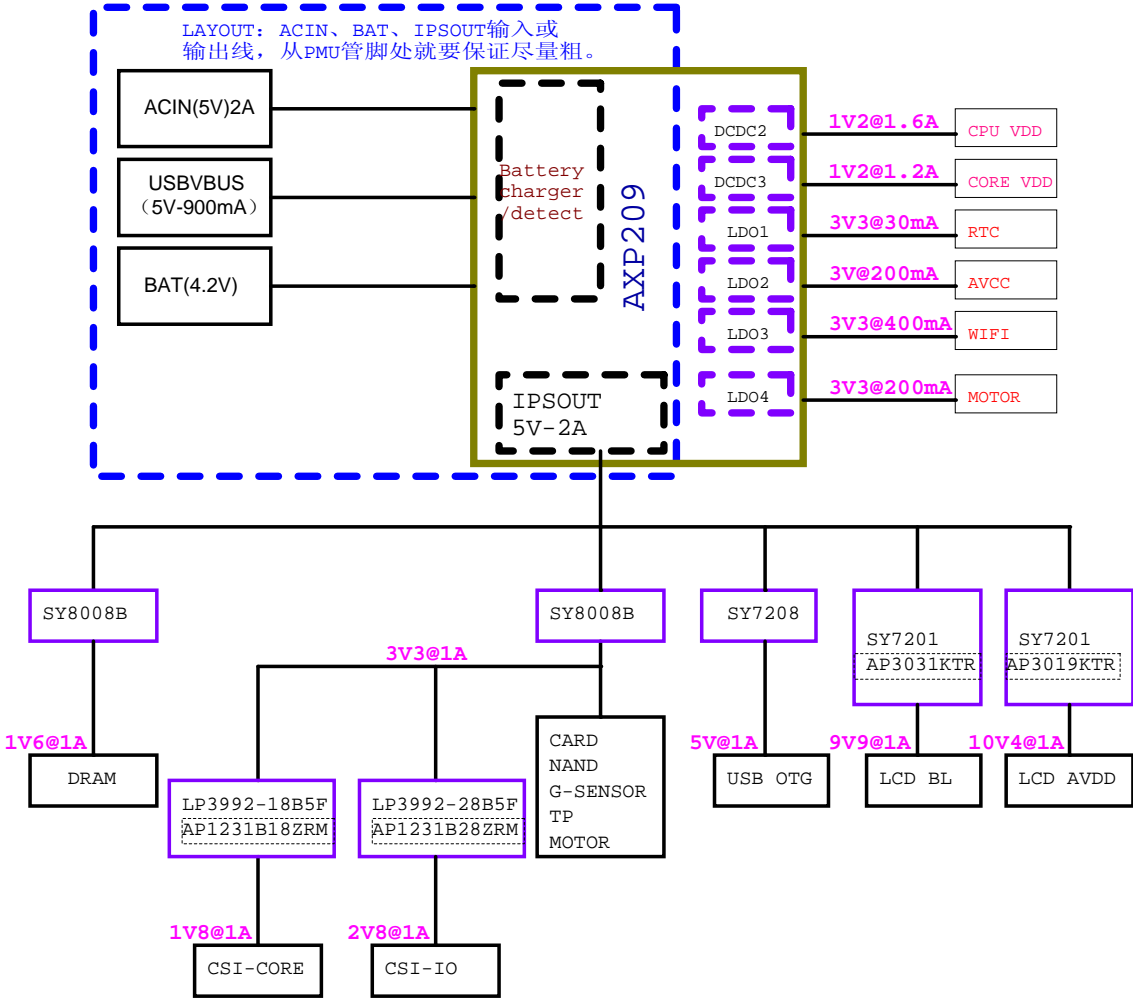
CPU	Define	Function
PB0	TWIO_SCK	TWIO
PB1	TWIO_SDA	TWIO
PB2	PWM0	LCD
PB3	GPIO-OUT	TP-WAKE-RST
PB4	NC	
PB10	GPIO-OUT	CSI-STY
PB15	TWII_SCK	TWII
PB16	TWII_SDA	TWII
PB17	TWII_SCK	TWII
PB18	TWII_SDA	TWII
PG0	INPUT	SD0-DET-N
PG1	GPIO-IN	USB0-VBUSDET
PG2	GPIO-IN	USB0-IDDET
PG3	UART-TX	UART-TX
PG4	UART-RX	UART-RX
PG9	GPIO-OUT	MT-EN
PG10	GPIO-OUT	PA-SHDN
PG11	EINT	TP-INT
PG12	GPIO-OUT	USB0-DRV
PMU	Define	Function
GPIO0	GPIO-OUT	LCD-PWR
GPIO1	GPIO-OUT	LCD-BL-EN
GPIO2	GPIO-OUT	CSI-PWR-EN
GPIO3	GPIO-OUT	CSI-RST

特别提醒：
1: PG0 / PG1 / PG2这三个PIN脚
只具有INPUT/中断/专有功能
2: PMU的GPIO0/1/2/3这四个PIN脚
只做GPIO-OUTPUT功能
3: PG10 / PG11 / PG12这三个PIN脚
的功能不可改变

REVISION HISTORY

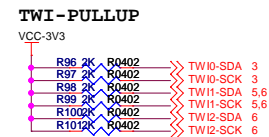
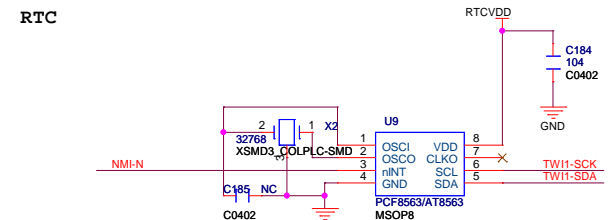
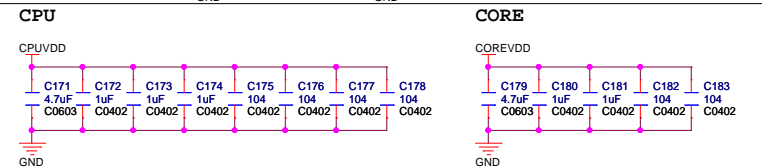
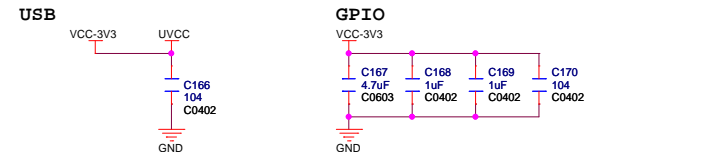
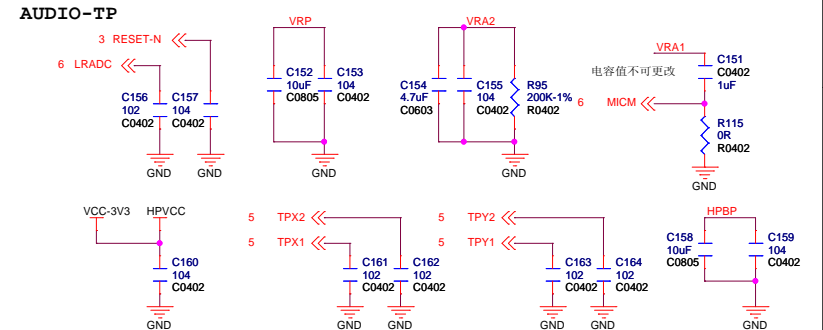
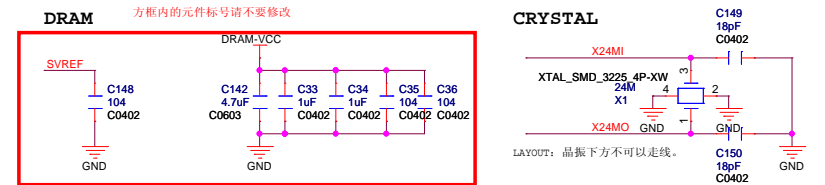
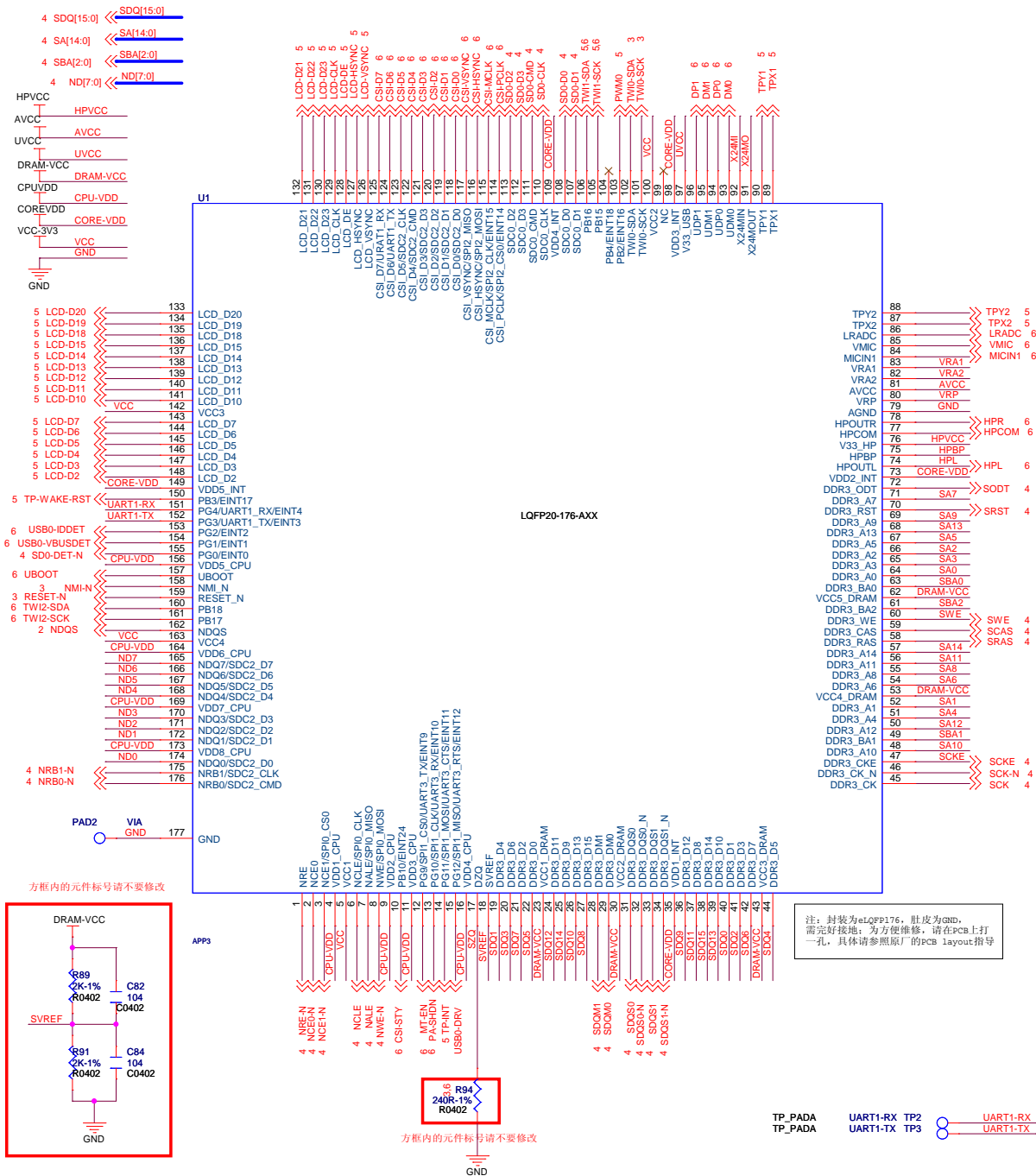
Revision	Description	Date	Drawn	Checked	Approved
APP3_PAD_DDR3_V1_10	version 1.10		CPL		

POWER TREE



Title APP3_PAD_DDR3		
Size A3	Document Name COVER	Rev
Date:	Sheet 1	of 6

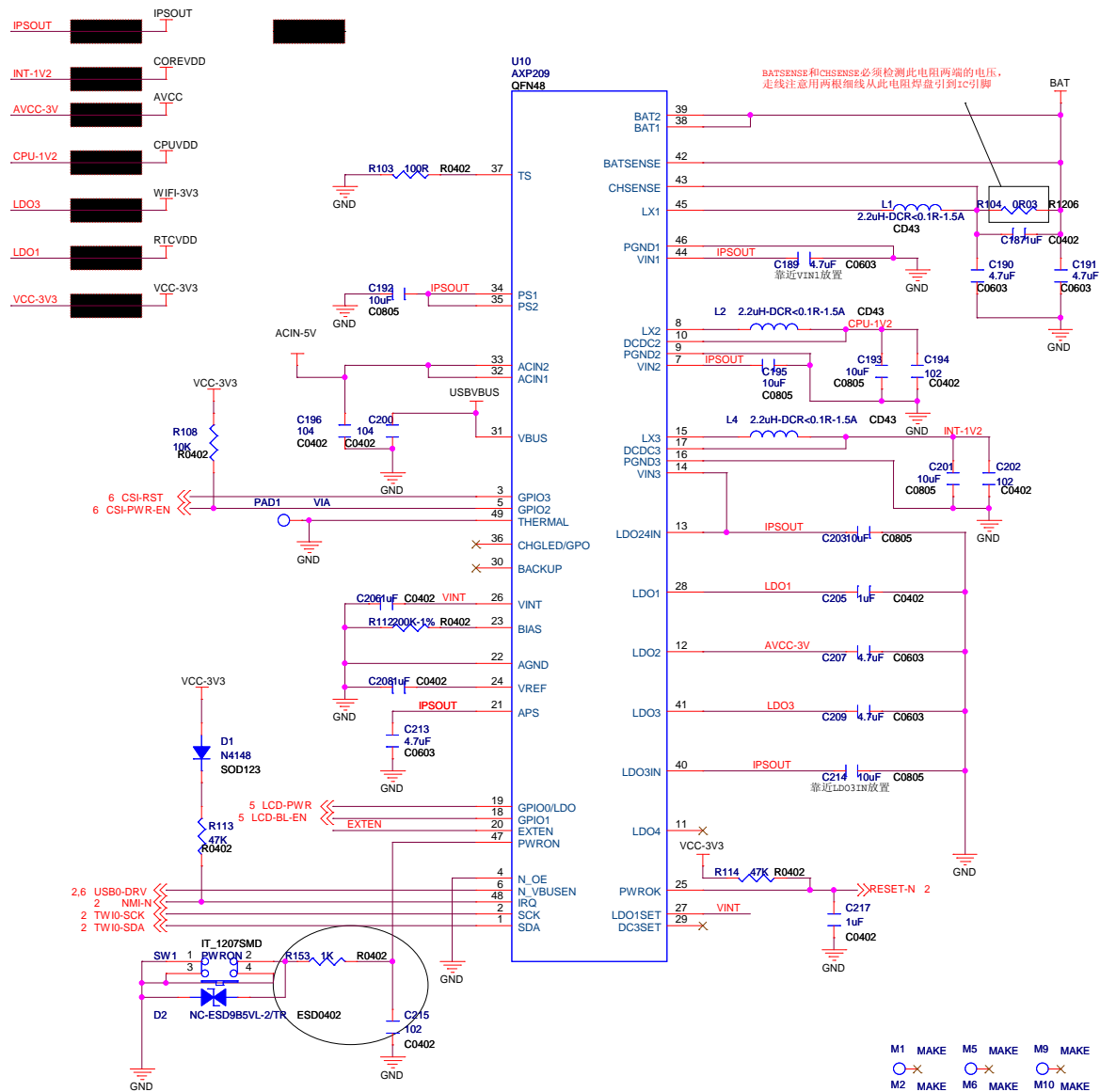
CPU



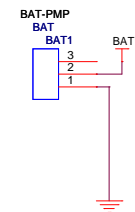
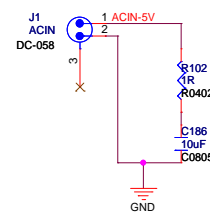
Title APP3_PAD_DDR3			
Size A3	Document Name CPU		Rev
Date:	Sheet	2 of 6	

POWER

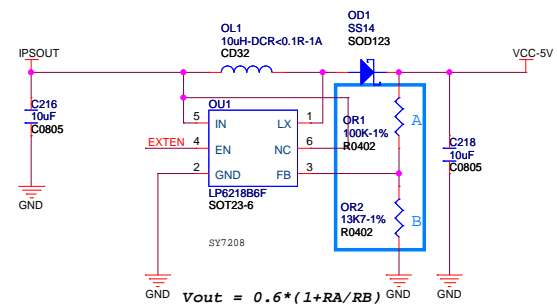
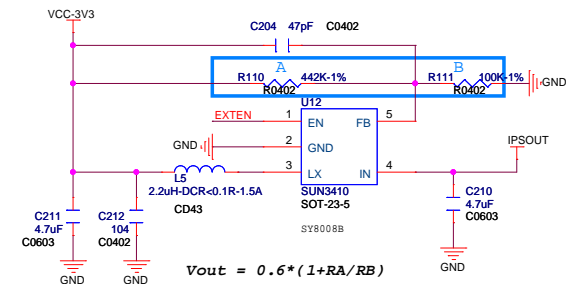
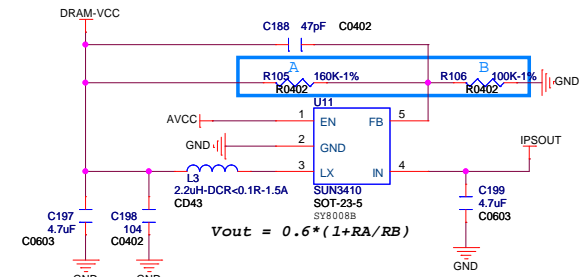
POWER LINE:Width>=60mil



注：4.7uH-DCR<0.1R-1.5A
表示在1.5A的电流下电感感量还满足4.7uH，DCR<0.1R表示电感直流电阻要小于0.1欧姆



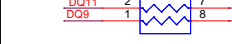
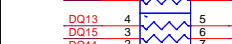
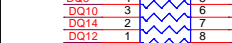
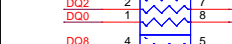
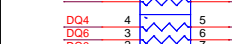
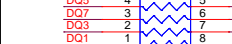
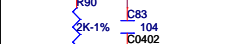
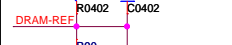
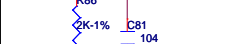
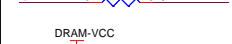
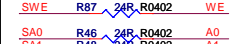
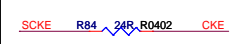
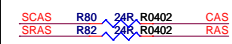
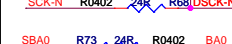
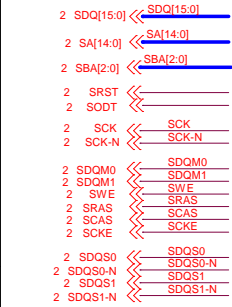
此时不支持电池温度检测



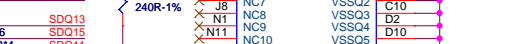
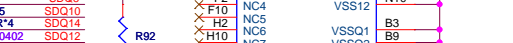
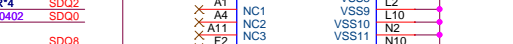
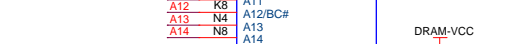
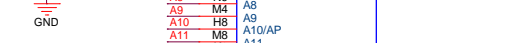
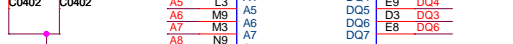
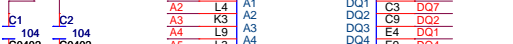
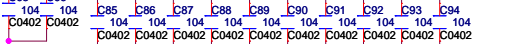
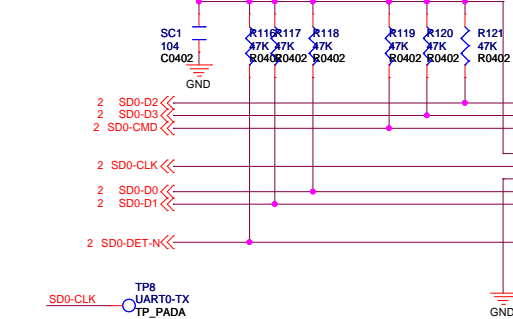
Title APP3_PAD_DDR3			
Size A3	Document Name POWER		Rev
Date:	Sheet	3 of 6	

MEMORY

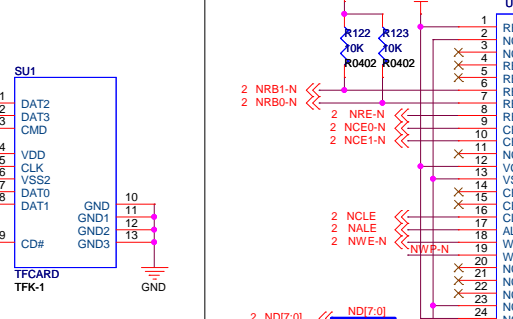
DDR3



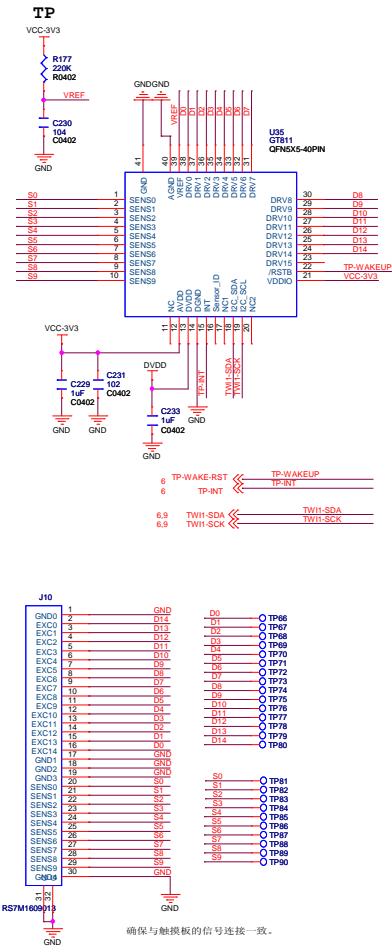
CARD



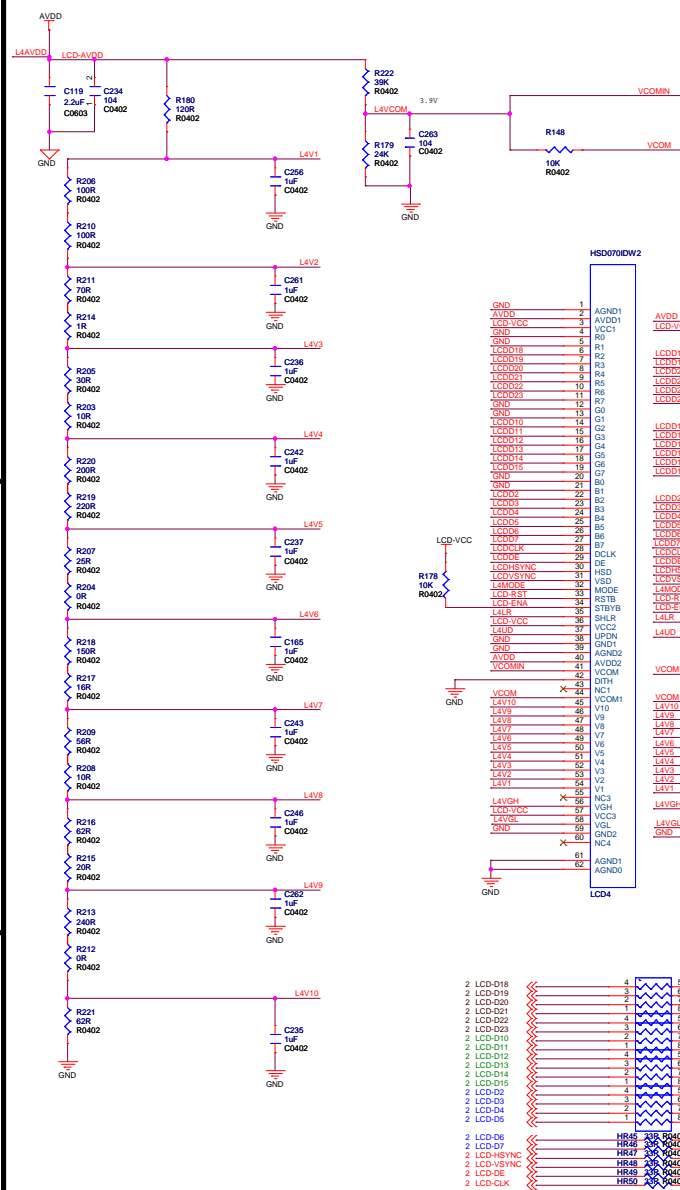
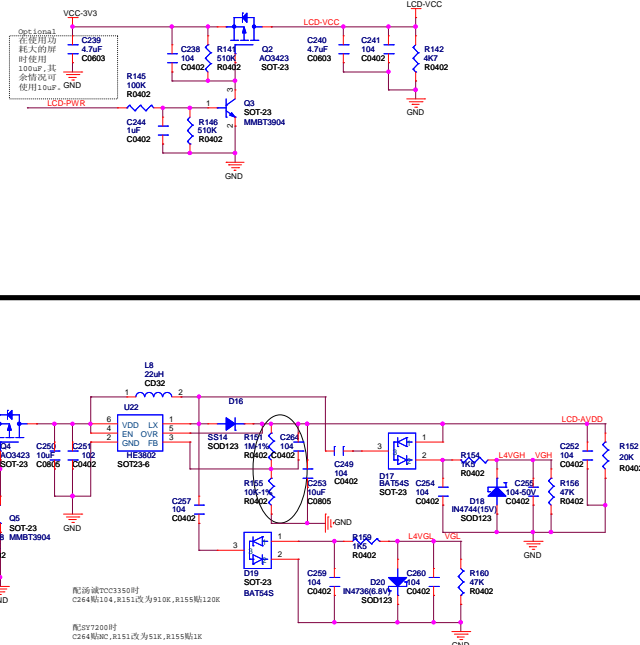
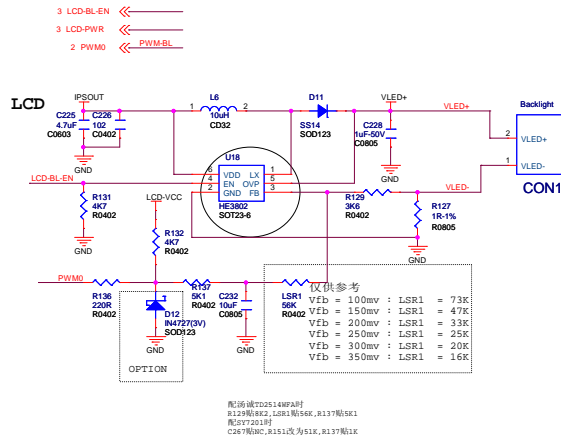
NAND



DISPLAY



确保与触摸板的信号连接一致。



MODE: H=DE mode, L=H/V mode
UD: H=up shift, L=down shift
LR: H=shift right, L=shift left

Title			
APP3_PAD_DDR3			
Size	Document Name	Rev	
A2	DISPLAY		
Date:	Sheet	5	of 6

CSI

