

## Mixed Signal SoC design Marathon using eSim & SKY130



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## Certificate of Participation

This is to certify that Milad Vafaieenezhad from shahed university has submitted the circuit Window Comparator Along with MOD-16 Counter for Counting Based Data Line Selection Operation in the Mixed Signal SoC design Marathon using eSim and SKY130. The submission was judged under the Outstanding\* category. The work done is available at https://esim.fossee.in.

The Marathon was conducted in the month of **September - October 2022** for a duration of 3 weeks. This was an initiative of the FOSSEE (Free/Libre and Open Source Software in Education) Project, IIT Bombay in association with VLSI System Design Corp. Pvt. Ltd. and Google Open Source.

Prof. Kannan Moudgalya
Principal Investigator - FOSSEE
IIT Bombay





## Kunal P Ghosh

Co-Founder, VLSI System Design Corp. Pvt. Ltd.









\* The winners were grouped into 4 categories: Outstanding/ Excellent/ Very Good/ Good. This is a computer generated certificate and requires no signature. To verify, scan the QR code or visit: https://fossee.in/certificates/verify

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