

**GOVT. POLYTECHNIC, PALANPUR**  
**Electronics & Communication Department**

**MALP**  
**Assignment – I**  
**Microprocessor Architecture & Instruction Set**

1. Draw the PIN DIAGRAM of 8085 & explain the detail of each pin.
2. Describe the functions of 8085 microprocessor Pins SOD, SID, HOLD, HLDA, READY, ALE, TRAP in short.
3. Draw and explain the internal architecture of 8085 microprocessor.
4. Draw Flag Register of 8085 microprocessor & explain it.
5. Draw and explain the 8085 microprocessor bus organization.
6. Explain with diagram the demultiplexing of multiplexed address/data bus of 8085 microprocessor.
7. Define (1)Bus(2) Opcode(3) Machine cycle(4)Memory map(5)instruction cycle(6)operand (7)Architecture
8. Classify instruction set of 8085 microprocessor on the basis of their operation. Give one example of each one
9. Explain below instructions with suitable example.

LDA	LDAX	LXI	LHLD	SHLD
XTHL	XCHG	PCHL	SPHL	DAD
DAA	CMA	CMC	STC	RLC
RAL	RRC	RAR	PUSH	POP
CALL	RET	SIM	RIM	EI
DI	IN	OUT	NOP	HLT

**Note: Date of Submission – On or Before 3<sup>rd</sup> February, 2017.**