

Memories

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graph TD; Memories --> RAM["RAM (Volatile)"]; Memories --> ROM["ROM (Non-volatile)"]; RAM --- RAM_Examples["SRAM, DRAM"]; ROM --- ROM_Examples["PROM, EPROM, EEPROM"];
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RAM (Volatile)

ROM (Non-volatile)

SRAM, DRAM

PROM, EPROM, EEPROM