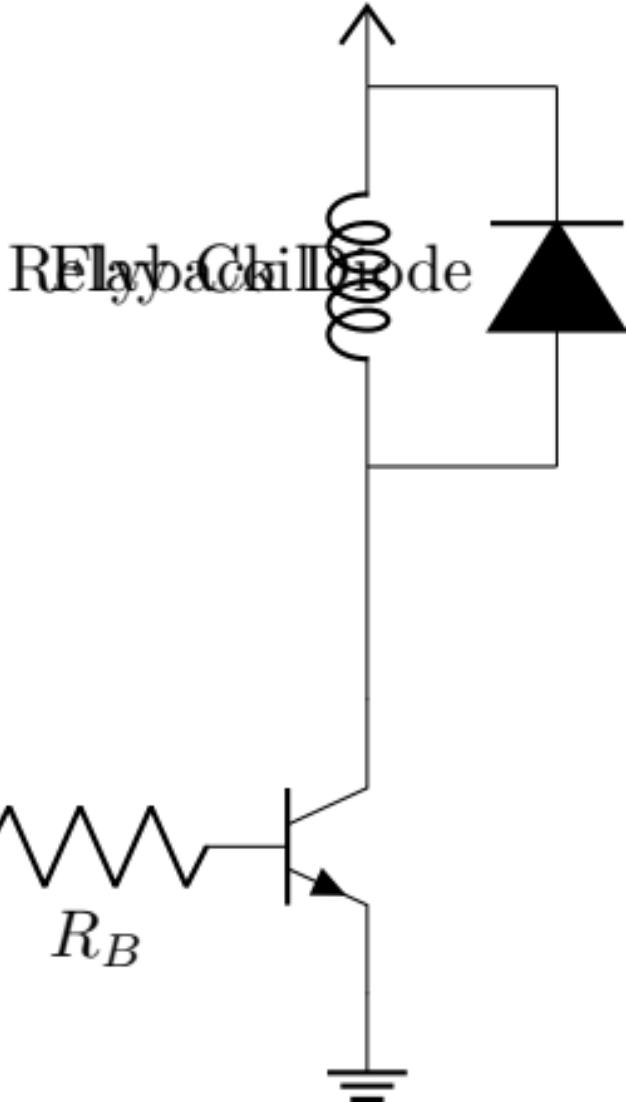


$+V_{CC}$



Relayback Child Node

Input Logic

$R_B$