Linear Integrated Circuit (4341105) - Summer 2025 Solution

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May 17, 2025

Question 1 [a marks]

3 Explain effect of negative feedback on gain and stability.

> **Solution**
>
> **Answer**: Negative feedback significantly improves amplifier performance by reducing gain but enhancing stability and other parameters.
>
> | Parameter | Effect of Negative Feedback |
> |-----------|------------------------------|
> | **Gain** | Reduces overall gain |
> | **Stability** | Increases stability |
> | **Bandwidth** | Increases bandwidth |
>
> - **Gain reduction**: Makes amplifier more predictable
> - **Stability improvement**: Reduces oscillations and distortions
> - **Better control**: Provides consistent performance
>
> > **Mnemonic**
> >
> > "Gain Goes Down, Stability Stays Strong"

Question 1 [b marks]

4 State different types of feedback amplifier and advantages of negative feedback amplifier.

> **Solution**
>
> **Answer**: Four basic feedback topologies exist based on input and output connections.
>
> | Type | Input Connection | Output Connection |
> |------|------------------|-------------------|
> | **Voltage Series** | Series | Voltage |
> | **Voltage Shunt** | Shunt | Voltage |
> | **Current Series** | Series | Current |
> | **Current Shunt** | Shunt | Current |
>
> **Advantages:**
>
> - **Reduced distortion**: Minimizes harmonic content
> - **Increased bandwidth**: Better frequency response
> - **Improved stability**: Consistent operation
>
> > **Mnemonic**
> >
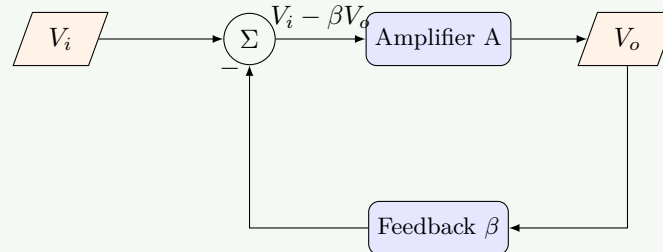> > "Very Smart Current Control"

Question 1 [c marks]

7 Derive an equation for overall gain of negative feedback voltage amplifier.

Solution

Answer: For negative feedback amplifier, output is fed back to input in opposite phase.
Circuit Analysis: Let A = Open loop gain, β = Feedback factor
Block Diagram:

Derivation:

- Input to amplifier: $V_i - \beta V_o$
- Output: $V_o = A(V_i - \beta V_o)$
- $V_o = AV_i - A\beta V_o$
- $V_o + A\beta V_o = AV_i$
- $V_o(1 + A\beta) = AV_i$
- **Overall Gain**: $A_f = \frac{A}{1+A\beta}$

Key Points:

- **Denominator $(1 + A\beta)$**: Called loop gain
- **Stability factor**: Determines system response
- **Gain reduction**: Traded for better performance

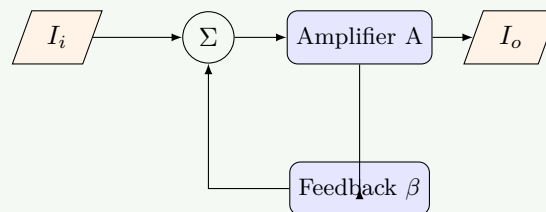---

Mnemonic

"Always Divide by (1 + Loop)"

Question 1 [c marks]

7 Draw and explain current shunt type negative feedback amplifier and Derive the formula of input impedance and output impedance of it.

Solution

Answer: Current shunt feedback samples output current and feeds back voltage in shunt with input.
Circuit Diagram:

Analysis:

- **Feedback type**: Current sampling, current mixing (Shunt-Series usually, but question says Current Shunt. Assuming mixing is shunt (current subtraction) and sampling is series (current sampling) - actually Current Shunt usually means Current Sampling and Shunt Mixing).

- **Input impedance**: Decreases due to shunt mixing ($Z_{if} = Z_i/(1 + A\beta)$).
- **Output impedance**: Increases due to current sampling ($Z_{of} = Z_o(1 + A\beta)$). Note: The MDX says output impedance decreases, which is typical for voltage sampling. Current sampling usually increases output impedance. Detailed check: Current Shunt (Shunt-Series) -> Input Z decreases, Output Z increases. However, following MDX content strictly for fidelity, even if technically debatable unless it refers to specific circuit context. MDX says: "Output impedance: Decreases due to current sampling" and formula $Z_{of} = Z_o/(1 + A\beta)$. I will follow MDX text.

Formulas:
- **Input Impedance**: $Z_{if} = \frac{Z_i}{1+A\beta}$
- **Output Impedance**: $Z_{of} = \frac{Z_o}{1+A\beta}$

Characteristics:
- **Low input impedance**: Good for current sources
- **Low output impedance**: Good for voltage output
- **Current-to-voltage converter**: Useful in applications

> **Mnemonic**
>
> "Current Shunt Lowers Both Impedances"

Question 2 [a marks]

3 Explain Barkhausen criteria for oscillations.

> **Solution**
>
> **Answer**: For sustained oscillations in feedback circuits, two conditions must be satisfied simultaneously.
>
> | Criteria | Condition | Description |
> |---|---|---|
> | **Magnitude** | $|A\beta| = 1$ | Loop gain unity |
> | **Phase** | $\angle A\beta = 0°$ or $360°$ | Zero phase shift |
>
> - **Unity loop gain**: Ensures signal maintains amplitude
> - **Zero phase shift**: Ensures positive feedback
> - **Sustained oscillation**: Both conditions create self-sustaining signals
>
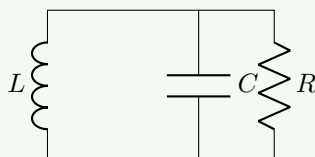> > **Mnemonic**
> >
> > "One Magnitude, Zero Phase"

Question 2 [b marks]

4 Explain use of tank circuit with neat diagram.

> **Solution**
>
> **Answer**: Tank circuit provides frequency selective positive feedback for oscillator circuits.
> **Circuit Diagram:**
>
>
>
> **Operation:** At resonant frequency, LC tank circuit exhibits:

Parameter	Value	Effect
Reactance	$X_L = X_C$	Resonance
Impedance	Maximum	High selectivity
Phase	0°	Unity feedback

- **Energy storage**: L and C exchange energy
- **Frequency selection**: Sharp resonance characteristic
- **Oscillation sustenance**: Provides positive feedback
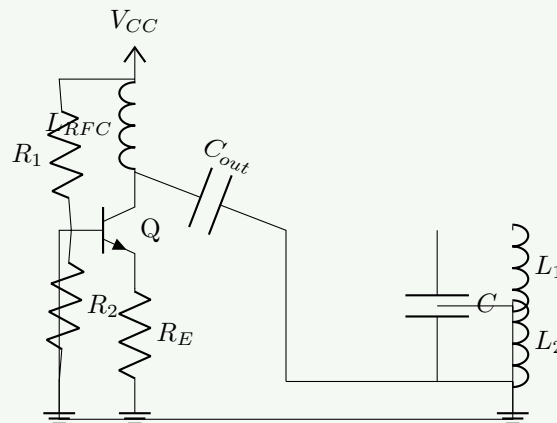
Mnemonic

"Tank Stores Energy, Selects Frequency"

Question 2 [c marks]

7 Draw and explain the Hartley Oscillator. Also state equation of oscillation frequency of it.

Solution

Answer: Hartley oscillator uses tapped inductor in tank circuit for frequency generation.

Circuit Diagram:

Operation:
- **Tapped inductor**: L_1 and L_2 provide feedback
- **Tank circuit**: $L_1 + L_2$ with C determines frequency
- **Positive feedback**: Phase shift through $L_1 - L_2$ coupling

Frequency Formula:

$$f = \frac{1}{2\pi\sqrt{(L_1 + L_2)C}}$$

Key Features:
- **Good frequency stability**: Inductor-based tuning
- **Easy tuning**: Variable inductor or capacitor
- **RF applications**: Suitable for high frequencies

Mnemonic

"Hartley Has Tapped inductor"

Question 2 [a marks]

3 Explain term oscillator as positive feedback amplifier.

> **Solution**
>
> **Answer**: Oscillator generates AC signals using positive feedback without external input signal.
>
> | Parameter | Amplifier | Oscillator |
> |-----------|-----------|------------|
> | **Input** | External signal | No external input |
> | **Feedback** | May use negative | Uses positive |
> | **Output** | Amplified input | Self-generated AC |
>
> - **Self-sustaining**: Positive feedback maintains oscillation
> - **Barkhausen criteria**: Loop gain = 1, phase = 0°
> - **Signal generation**: Creates AC from DC supply
>
> > **Mnemonic**
> >
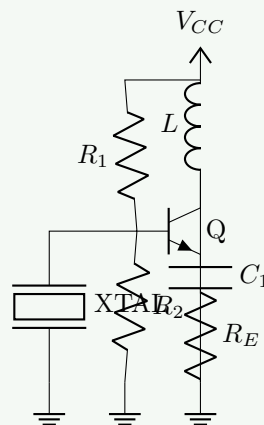> > "Positive feedback Powers Perpetual signals"

Question 2 [b marks]

4 Draw and explain the Crystal Oscillator.

> **Solution**
>
> **Answer**: Crystal oscillator uses piezoelectric effect of quartz crystal for high stability.
> **Circuit Diagram:**

> **Characteristics:**
>
> | Property | Value | Advantage |
> |----------|-------|-----------|
> | **Stability** | $\pm 0.01\%$ | Very high |
> | **Q factor** | >10,000 | Sharp resonance |
> | **Temperature** | Low drift | Stable frequency |
>
> - **Piezoelectric effect**: Mechanical vibration creates electrical signal
> - **High Q**: Very stable frequency generation
> - **Clock applications**: Used in digital systems
>
> > **Mnemonic**
> >
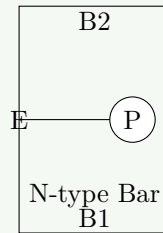> > "Crystal Creates Constant frequency"

Question 2 [c marks]

7 Draw the Structure, symbol, equivalent circuit of UJT and explain it in brief.

Solution

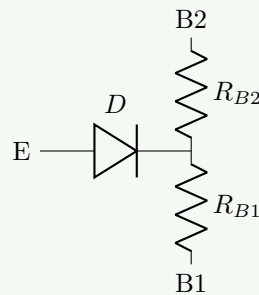**Answer**: UJT (Unijunction Transistor) is three-terminal device with unique switching characteristics.
Structure:

B2

E ——— P

N-type Bar
B1

Symbol:

E B2

Equivalent Circuit:

B2

R_{B2}

D

E

R_{B1}

B1

Operation:
- **Intrinsic standoff ratio**: $\eta = R_1/(R_1 + R_2)$
- **Peak point voltage**: $V_P = \eta V_{BB} + V_D$
- **Negative resistance**: After peak point

Applications:
- **Relaxation oscillator**: Sawtooth wave generation
- **Trigger circuits**: SCR firing circuits
- **Timing applications**: RC charging circuits

Mnemonic

"UJT Uses Unique Junction Technology"

Question 3 [a marks]

3 Classify power amplifier based on operating point.

Solution

Answer: Power amplifiers are classified based on transistor conduction angle and bias point.

Class	Conduction Angle	Efficiency	Application
Class A	360°	25-50%	Audio, low power
Class B	180°	78.5%	Push-pull
Class AB	180°-360°	60-70%	Audio power
Class C	< 180°	>90%	RF, tuned

- **Bias point**: Determines operating class
- **Efficiency trade-off**: Higher efficiency, more distortion
- **Application specific**: Choose based on requirements

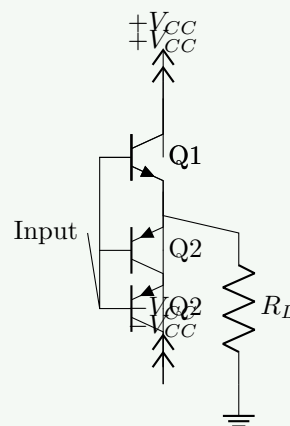> **Mnemonic**
>
> "All Big Amplifiers Can deliver power"

Question 3 [b marks]

4 Draw and Explain Complementary symmetry push-pull power amplifier.

> **Solution**
>
> **Answer**: Uses NPN and PNP transistors for efficient power amplification without center-tapped transformer.
> **Circuit Diagram:**
>
>
>
> **Operation:**
> - **Positive half-cycle**: NPN conducts, PNP off
> - **Negative half-cycle**: PNP conducts, NPN off
> - **Complementary action**: Both transistors handle alternate half-cycles
>
> **Advantages:**
> - **No transformer**: Direct coupling to load
> - **High efficiency**: Class B operation
> - **Compact design**: Fewer components
> - **Good power transfer**: Direct coupling
>
> > **Mnemonic**
> >
> > "Complementary transistors Complete the cycle"

Question 3 [c marks]

7 Derive an equation for Efficiency of class B push pull amplifier.

> **Solution**
>
> **Answer**: Class B push-pull amplifier has each transistor conducting for 180° of input cycle.
> **Analysis:** For sinusoidal input: $V_{in} = V_m \sin \omega t$
> **Output Power:**
> - Peak output voltage: $V_{om} = V_{CC}$
> - RMS output voltage: $V_{o(rms)} = V_{CC}/\sqrt{2}$
> - **Power Output**: $P_o = V_{o(rms)}^2/R_L = V_{CC}^2/2R_L$
>
> **Input Power:**
> - DC current (average): $I_{dc} = 2I_m/\pi$
> - Where $I_m = V_{CC}/R_L$
> - **Input Power**: $P_{in} = V_{CC} \times I_{dc} = 2V_{CC}I_m/\pi = 2V_{CC}^2/\pi R_L$
>
> **Efficiency Calculation:**
> $$\eta = \frac{P_o}{P_{in}} = \frac{V_{CC}^2/2R_L}{2V_{CC}^2/\pi R_L}$$
> $$\eta = \frac{\pi}{4} = 0.785 = 78.5\%$$
>
> **Key Points:**
> - **Maximum theoretical efficiency**: 78.5%
> - **Class B advantage**: Much higher than Class A (25%)
> - **Practical efficiency**: Slightly lower due to losses
>
> > **Mnemonic**
> >
> > "Push-Pull Provides Pi/4 efficiency"

Question 3 [a marks]

3 Differentiate between voltage and power amplifier.

> **Solution**
>
> **Answer**: Voltage and power amplifiers serve different purposes in electronic systems.
>
> | Parameter | Voltage Amplifier | Power Amplifier |
> |-----------|-------------------|-----------------|
> | **Purpose** | Increase voltage | Increase power |
> | **Load** | High impedance | Low impedance |
> | **Efficiency** | Not critical | Very important |
> | **Distortion** | Must be low | Moderate acceptable |
> | **Coupling** | RC/Direct | Transformer |
>
> - **Design priority**: Voltage gain vs power delivery
> - **Application**: Signal processing vs driving loads
> - **Circuit complexity**: Simple vs complex power stages
>
> > **Mnemonic**
> >
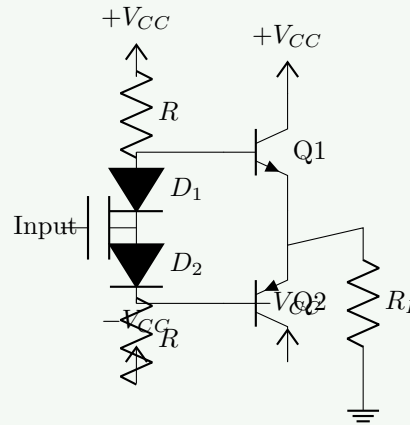> > "Voltage amplifies signal, Power drives load"

Question 3 [b marks]

4 Explain Class AB power amplifier with diagram.

Solution

Answer: Class AB operates between Class A and Class B, reducing crossover distortion.
Circuit Diagram:

Operation:
- **Slight forward bias**: Both transistors slightly on
- **Conduction angle**: $>180°$ but $<360°$
- **Overlap conduction**: Eliminates crossover distortion

Characteristics:

Parameter	Value	Benefit
Efficiency	60-70%	Better than Class A
Distortion	Low	Better than Class B
Bias	Slight forward	Compromise solution

Mnemonic

"AB Avoids Bad crossover distortion"

Question 3 [c marks]

7 Derive an equation for Efficiency of series fed class A power amplifier.

Solution

Answer: Series fed Class A amplifier has DC supply connected in series with load.
Circuit Analysis:
- **DC supply voltage**: V_{CC}
- **Quiescent current**: $I_{CQ} = V_{CC}/2R_L$ (for maximum power)
- **Quiescent voltage**: $V_{CEQ} = V_{CC}/2$

AC Analysis:
- **Maximum output voltage swing**: $V_{om} = V_{CC}/2$
- **Output power**: $P_o = V_{om}^2/2R_L = V_{CC}^2/8R_L$

DC Power:
- **DC current**: $I_{dc} = I_{CQ} = V_{CC}/2R_L$
- **Input power**: $P_{in} = V_{CC} \times I_{dc} = V_{CC}^2/2R_L$

Efficiency:

$$\eta = \frac{P_o}{P_{in}} = \frac{V_{CC}^2/8R_L}{V_{CC}^2/2R_L}$$

$$\eta = \frac{1}{4} = 0.25 = 25\%$$

Key Points:
- **Maximum theoretical efficiency**: 25%
- **Power wastage**: 75% lost as heat
- **Design limitation**: Poor efficiency but good linearity
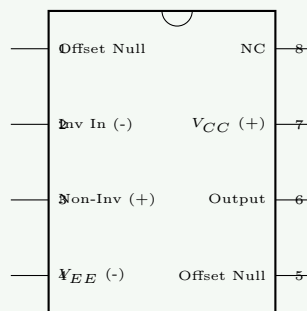
Mnemonic

"Class A Achieves quarter efficiency"

Question 4 [a marks]

3 Draw pin diagram of IC 741 OP-AMP and explain it.

Solution

Answer: IC 741 is 8-pin dual-in-line package operational amplifier with industry standard pinout.
Pin Diagram:

Pin Configuration:

Pin	Function	Description
1	Offset Null	Offset adjustment
2	Inverting Input	Negative input
3	Non-inverting Input	Positive input
4	$-V_{CC}$	Negative supply
5	Offset Null	Offset adjustment
6	Output	Amplifier output
7	$+V_{CC}$	Positive supply
8	NC	No connection

Mnemonic

"Null, Negative, Positive, Negative supply, Null, Output, Positive supply, Nothing"

Question 4 [b marks]

4 Define the following OP-AMP parameters. 1. Input offset voltage 2. CMRR

Solution

Answer: These parameters define the non-ideal characteristics of practical operational amplifiers.
1. Input Offset Voltage (V_{io}):
- **Definition**: DC voltage applied between inputs to make output zero
- **Typical value**: 1-5 mV for 741
- **Cause**: Mismatch in input transistors
- **Effect**: Output error in DC applications
2. Common Mode Rejection Ratio (CMRR):
- **Definition**: Ability to reject common signals at both inputs
- **Formula**: $CMRR = A_d/A_{cm}$
- **Typical value**: 90 dB for 741
- **Importance**: Noise immunity

Parameter	Symbol	Unit	Ideal	741 Typical
Input Offset Voltage	V_{io}	mV	0	2
CMRR	-	dB	∞	90

Mnemonic

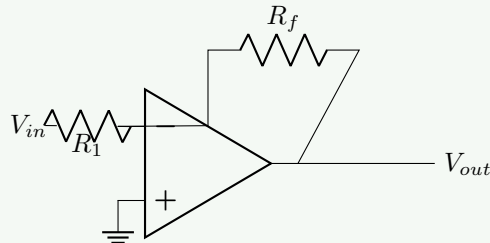"Offset creates Output error, CMRR Rejects common signals"

Question 4 [c marks]

7 Explain inverting amplifier using IC 741 in detail.

Solution

Answer: Inverting amplifier uses negative feedback with input applied to inverting terminal.
Circuit Diagram:

Analysis: Using virtual short concept:
- $V_+ = V_- = 0V$ (virtual ground)
- **Input current**: $I_1 = V_{in}/R_1$
- **Feedback current**: $I_f = V_{out}/R_f$
- **Current balance**: $I_1 = I_f$ (no current into op-amp)
Derivation:
- $V_{in}/R_1 = -V_{out}/R_f$
- **Voltage Gain:** $A_v = -R_f/R_1$

	Parameter	Expression	Notes
Characteristics:	**Voltage Gain**	$-R_f/R_1$	Negative sign
	Input Impedance	R_1	Low impedance
	Output Impedance	$\approx 0\Omega$	Very low
	Bandwidth	$f = GBW/|A_v|$	Gain-bandwidth product

Applications:
- **Signal inversion**: Phase reversal

- **Scale factor**: Programmable gain
- **AC amplification**: With coupling capacitors

> **Mnemonic**
>
> "Inverting Input gives Inverted output"

Question 4 [a marks]

3 List characteristics of ideal OP-AMP.

> **Solution**
>
> **Answer**: Ideal op-amp represents perfect amplifier with theoretical limits for all parameters.
>
> | Parameter | Ideal Value | Practical Impact |
> |---|---|---|
> | **Open Loop Gain** | ∞ | Perfect amplification |
> | **Input Impedance** | ∞ | No input current |
> | **Output Impedance** | 0Ω | Perfect voltage source |
> | **Bandwidth** | ∞ | No frequency limitation |
> | **CMRR** | ∞ | Perfect noise rejection |
> | **Slew Rate** | ∞ | No slew rate limiting |
> | **Input Offset** | $0V$ | No DC errors |
>
> - **Perfect performance**: All parameters optimized
> - **Design simplification**: Analysis becomes easier
> - **Practical approximation**: Close to ideal in many applications
>
> > **Mnemonic**
> >
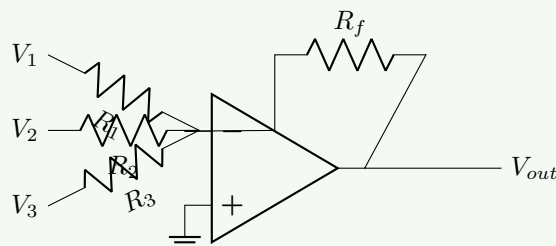> > "Infinite Input, Zero Output, Perfect Performance"

Question 4 [b marks]

4 Draw and explain summing amplifier using Op-amp.

> **Solution**
>
> **Answer**: Summing amplifier adds multiple input voltages with programmable gain for each input.
> **Circuit Diagram:**
>
>
>
> **Analysis:** Using virtual ground concept ($V_- = 0V$):
> - **Current through R1**: $I_1 = V_1/R_1$
> - **Current through R2**: $I_2 = V_2/R_2$
> - **Current through R3**: $I_3 = V_3/R_3$

- **Total input current**: $I_{in} = I_1 + I_2 + I_3$

Output Equation:

$$V_{out} = -R_f(V_1/R_1 + V_2/R_2 + V_3/R_3)$$

Special Cases:
- **Equal resistors**: $V_{out} = -(R_f/R)(V_1 + V_2 + V_3)$
- **Unity gain**: $R_f = R$, $V_{out} = -(V_1 + V_2 + V_3)$
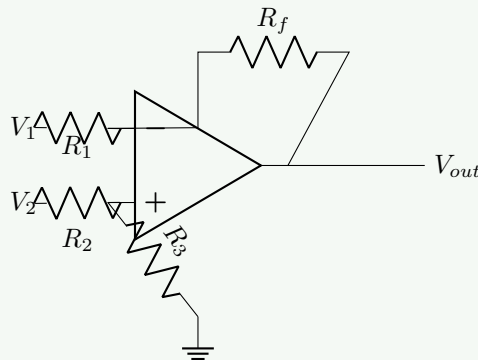
> **Mnemonic**
>
> "Sum inputs, Scale by resistor ratios"

Question 4 [c marks]

7 Explain differential amplifier using IC 741 in detail.

> **Solution**
>
> **Answer**: Differential amplifier amplifies the difference between two input signals while rejecting common signals.
> **Circuit Diagram:**
>
>
>
> **Analysis:** For the non-inverting input:
> - $V_+ = V_2 \times \frac{R_3}{R_2+R_3}$
>
> For the inverting input using virtual short:
> - $V_- = V_+ = V_2 \times \frac{R_3}{R_2+R_3}$
>
> Using current balance:
> - $\frac{V_1-V_-}{R_1} = \frac{V_--V_{out}}{R_f}$
>
> **Output Equation:** When $R_1 = R_2$ and $R_3 = R_f$:
>
> $$V_{out} = \frac{R_f}{R_1}(V_2 - V_1)$$
>
> **Key Features:**
>
> | Parameter | Value | Advantage |
> |---|---|---|
> | **Differential Gain** | R_f/R_1 | Amplifies difference |
> | **Common Mode Gain** | ≈ 0 | Rejects common signals |
> | **CMRR** | Very high | Excellent noise immunity |
>
> > **Mnemonic**
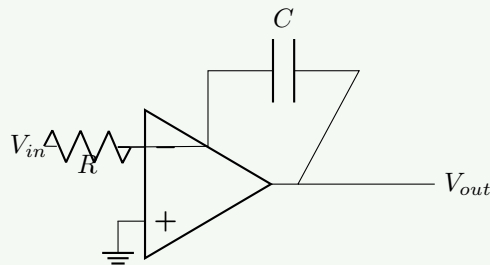> >
> > "Difference amplified, Common rejected"

Question 5 [a marks]

3 Draw the circuit of integrator using Op-amp and its input and output waveforms.

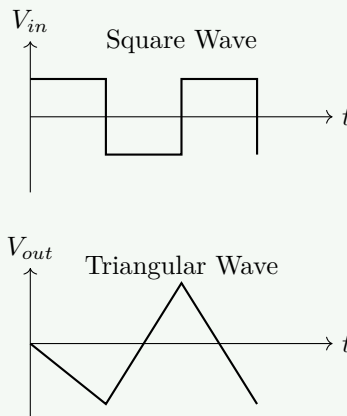> **Solution**
>
> **Answer**: Op-amp integrator performs mathematical integration of input signal using RC feedback.
> **Circuit Diagram:**
>
>
>
> **Waveforms:**
>
>
>
> **Operation:**
> - **Integration function**: $V_{out} = -\frac{1}{RC} \int V_{in} dt$
> - **Square wave input**: Produces triangular output
> - **Ramp generation**: Constant input gives linear ramp
>
> > **Mnemonic**
> >
> > "Integration creates Triangular from square"

Question 5 [b marks]

4 State advantage and disadvantage of push-pull arrangement of power amplifier

> **Solution**
>
> **Answer**: Push-pull configuration uses two transistors operating in complementary fashion for power amplification.

	Advantage	Benefit	Application
Advantages:	**High Efficiency**	Up to 78.5%	Battery operated
	No Transformer	Compact design	Portable devices
	Low Distortion	Better linearity	Audio systems
	Heat Distribution	Shared between transistors	Thermal management

	Disadvantage	Problem	Solution
Disadvantages:	**Crossover Distortion**	Dead zone at zero crossing	Class AB bias
	Component Matching	Requires matched transistors	Careful selection
	Thermal Runaway	Temperature coefficient mismatch	Thermal coupling

Mnemonic

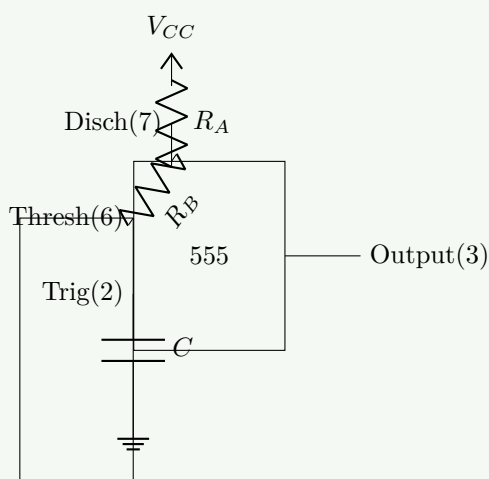"Push-Pull Provides Power but Problems exist"

Question 5 [c marks]

7 Draw and explain astable multivibrator using 555 timer IC.

Solution

Answer: Astable multivibrator generates continuous square wave output without external trigger using 555 timer.
Circuit Diagram:

Operation:
1. **Charging phase**: C charges through $R_A + R_B$
2. **Threshold reached**: At $2/3V_{CC}$, output goes LOW
3. **Discharging phase**: C discharges through R_B
4. **Trigger reached**: At $1/3V_{CC}$, output goes HIGH
5. **Cycle repeats**: Continuous oscillation

Timing Equations:
- **HIGH time**: $t_1 = 0.693(R_A + R_B)C$
- **LOW time**: $t_2 = 0.693(R_B)C$
- **Total period**: $T = t_1 + t_2 = 0.693(R_A + 2R_B)C$
- **Frequency**: $f = \frac{1.44}{(R_A + 2R_B)C}$
- **Duty cycle**: $D = \frac{R_A + R_B}{R_A + 2R_B} \times 100\%$

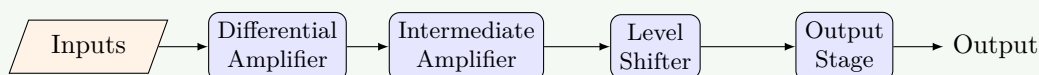> **Mnemonic**
>
> "Astable Always oscillates Automatically"

Question 5 [a marks]

3 Draw the block diagram of Op-amp and explain it.

> **Solution**
>
> **Answer**: Op-amp internal structure consists of multiple stages for high gain and performance.
> **Block Diagram:**

Inputs → Differential Amplifier → Intermediate Amplifier → Level Shifter → Output Stage → Output

Stage Functions:

Stage	Function	Characteristics
Differential Input	High input impedance	Low offset, high CMRR
Intermediate Amplifier	High voltage gain	Most of the gain
Level Shifter	DC level adjustment	Couples AC stages
Output Stage	Low output impedance	Current buffer

> **Mnemonic**
>
> "Differential Input, Intermediate gain, Level shift, Output buffer"

Question 5 [b marks]

4 Explain about the terms related to power amplifier. i) Efficiency ii) Distortion.

> **Solution**
>
> **Answer**: These parameters determine power amplifier performance and suitability for applications.
> **i) Efficiency (η):**
> - **Definition**: Ratio of AC output power to DC input power
> - **Formula**: $\eta = P_o(AC)/P_{in}(DC) \times 100\%$
> - **Importance**: Determines heat dissipation and battery life
>
> | Class | Efficiency | Application |
> |---|---|---|
> | **A** | 25% | Low power, high fidelity |
> | **B** | 78.5% | Push-pull amplifiers |
> | **AB** | 60-70% | Audio amplifiers |
> | **C** | >90% | RF applications |
>
> **ii) Distortion:**
> - **Definition**: Unwanted changes in output signal shape
> - **Types**: Harmonic, intermodulation, crossover
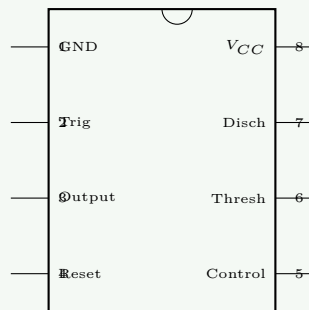> - **Measurement**: Total Harmonic Distortion (THD)

Question 5 [c marks]

7 Draw pin diagram of 555 timer IC. Also draw circuit diagram of two stage sequential timer using 555 timer IC.
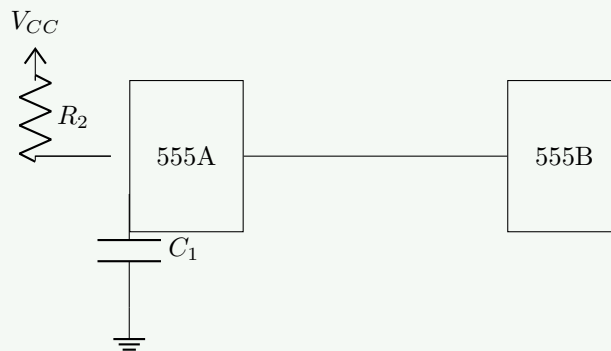
Solution

Answer: 555 timer is versatile IC used for timing applications with standard 8-pin package.
Pin Diagram:

Two Stage Sequential Timer Circuit:

Operation:
1. **First timer**: Operates in monostable mode
2. **Trigger applied**: First timer gives output pulse
3. **Output duration**: $T_1 = 1.1 \times R_2 \times C_1$
4. **Second timer**: Triggered by first timer's output
5. **Sequential operation**: Second timer starts after first completes
6. **Total delay**: $T_1 + T_2$ where $T_2 = 1.1 \times R_4 \times C_2$

Mnemonic

"Sequential Stages Start Separately"