

# Milav Jayeshkumar Dabgar

## Engineering Educator & R&D Professional

Innovation Leader • Full-Stack Developer • AI/Data Science Practitioner

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## Executive Summary

Engineering educator and R&D professional with **9+ years** of comprehensive experience spanning electronics hardware development, embedded systems, AI/ML, and full-stack software engineering. Currently pursuing **BS in Data Science and Applications from IIT Madras** (Diploma in Programming completed with distinction). Distinguished by exceptional student mentorship that enabled teams to secure **Rs. 45+ lakhs in innovation funding** (Rs. 25 lakhs from Shark Tank India, Rs. 20 lakhs from iHub Gujarat), resulting in **2 student patents**. Proven expertise in bridging academia-industry gap through innovative teaching methodologies, self-hosted infrastructure development, and real-world system implementation. Seeking AICTE Industry Fellowship to gain advanced industrial research experience and enhance diploma-level innovation ecosystem in Gujarat.

## Professional Experience

### Lecturer (Class - II) & Innovation Leader

*Government Polytechnic, Education Department - Government of Gujarat*

Nov 2016 – Present

*Palanpur, Gujarat*

- Lead comprehensive technical education across Programming in C, Microprocessor Programming, Embedded Systems, Circuit Design, Consumer Electronics, and Entrepreneurship development
- Hold key institutional positions: **IT Convener** (infrastructure strategy), **SSIP Co-Convener** (startup ecosystem), **Training & Placement Member**, and active contributor to MIS and UDAYAM initiatives
- Achieved remarkable student success: mentored teams securing **Rs. 25 lakhs from Shark Tank India** and **Rs. 20 lakhs through government innovation programs**, resulting in **2 student patents**
- Architected and deployed comprehensive **Next.js Academic Management System** at [gppalanpur.in](https://gppalanpur.in) with production-grade CI/CD pipeline
- Developed enterprise-grade infrastructure: self-hosted Linux servers, Dockerized microservices, CI/CD pipelines, and automated backup systems

### Electronics & Communication Engineer (R&D)

*TEXEG India Private Limited (Japan-based Technology Firm)*

Jul 2015 – Oct 2016

*Gandhinagar, Gujarat*

- Led end-to-end product development lifecycle for commercial embedded systems in international R&D environment
- Executed circuit simulation, PCB design, firmware development, and control systems implementation
- Designed control algorithms (PID, PI, Fuzzy Logic) using MATLAB Control System Toolbox for industrial applications

### Research & Development Intern

*eiTRA - eInfochips Training & Research Academy Ltd*

Aug 2014 – Jul 2015

*Ahmedabad, Gujarat*

- Gained comprehensive foundation in embedded systems research methodologies and industry best practices
- Developed proficiency in advanced debugging techniques and hardware-software integration strategies

## Education

### Bachelor of Science (BS) in Data Science and Applications

*Indian Institute of Technology Madras (IIT Madras)*

2021 – Present

Roll No: 21F1005510 | Overall CGPA: 7.07/10

- **Foundation Level:** Completed (32/32 credits, CGPA: 7.50/10) - Strong performance in Computational Thinking, Mathematics for Data Science, and Programming in Python
- **Diploma Programming Track:** Completed (27/27 credits, CGPA: 7.19/10) - Database Management, Data Structures & Algorithms, Modern Application Development I & II

- **Diploma Data Science Track:** In Progress (21/27 credits, CGPA: 6.24/10) - Machine Learning Techniques, Business Analytics, Business Data Management

**Master of Engineering(ME), Communication Systems**

2013 – 2015

*L.D College of Engineering, Gujarat Technological University*

*Ahmedabad, Gujarat | CGPA: 8.04/10*

- Specialized in Digital Signal Processing, Wireless Communications, and Advanced Communication Protocols

**Bachelor of Engineering(BE), Electronics & Communication**

2009 – 2013

*Sal Institute of Technology and Engineering Research, Gujarat Technological University*

*Gujarat | CGPA: 7.28/10*

- Foundation in Electronics Design, Embedded Systems, and Communication Technologies

## Technical Expertise

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**Programming Languages:**

- Java, Python, JavaScript (ES6+), C/C++
- R, SQL, Assembly Language
- HTML5, CSS3, Markdown

**Web & Full-Stack Development:**

- Next.js, React.js, Vue.js 3
- Node.js, Express.js, Flask, FastAPI
- RESTful APIs, JWT Authentication
- SQLite, MongoDB, PostgreSQL

**Data Science & Machine Learning:**

- TensorFlow, PyTorch, Scikit-learn
- Pandas, NumPy, Matplotlib, Seaborn
- Computer Vision, NLP, Deep Learning
- Recommender Systems, Clustering

**Infrastructure & DevOps:**

- Linux Server Administration
- Docker, CI/CD Pipelines
- Git Version Control, GitHub Actions
- Self-hosted & Cloud Solutions

**Embedded Systems & Hardware:**

- 8051, PIC, AVR, STM32 Microcontrollers
- Arduino, Raspberry Pi, ESP8266
- EagleCAD, Altium, OrCAD, KiCAD
- Multisim, Proteus, LTspice

**Engineering Tools & Platforms:**

- MATLAB, Simulink, Control Systems
- FPGA Design, Verilog, Digital Design
- Signal & Image Processing
- Modbus Protocol, Industrial Automation

# Key Projects & Innovations

<b>Smart Academic Portal</b>   <i>Next.js, React, Docker, PostgreSQL</i>	2025 – Present
<ul style="list-style-type: none"><li>– Production-ready academic management system deployed at <a href="http://gppalanpur.in">gppalanpur.in</a> serving Government Polytechnic Palanpur with complete institutional infrastructure management</li><li>– Comprehensive admin dashboard with user/role management, CSV import/export, advanced filtering/sorting, automated feedback analysis, and multi-format report generation</li><li>– Student portfolio system with LinkedIn-style public profiles, downloadable resume/CV generation, interactive newsletters, and extensive paper solutions repository</li><li>– Enterprise-grade deployment with self-hosted infrastructure, CI/CD pipelines, zero-downtime deployments, and collaborative development with student contributors</li></ul>	
<b>Personal Blog &amp; Portfolio</b>   <i>Hugo, Blowfish Theme, Markdown</i>	2024 – Present
<ul style="list-style-type: none"><li>– Self-hosted personal website at <a href="http://milav.in">milav.in</a> featuring comprehensive blog, portfolio showcase, and educational resource repository</li><li>– Developed extensive study material collection with paper solutions for ECE/ICT/IT branches available in both English and Gujarati languages</li><li>– Implemented modern static site architecture using Hugo framework with responsive Blowfish theme for optimal performance and user experience</li></ul>	
<b>System Threat Forecaster</b>   <i>Python, Machine Learning, TensorFlow</i>	2024 – 2025
<ul style="list-style-type: none"><li>– Developed comprehensive machine learning pipeline for system threat prediction and cybersecurity risk assessment</li><li>– Implemented advanced data analysis and predictive modeling techniques using Python, TensorFlow, and Scikit-learn</li><li>– Applied pattern recognition and anomaly detection algorithms for real-time threat identification</li></ul>	
<b>Scarlett Web Application (MAD2)</b>   <i>Vue.js, Flask, PostgreSQL</i>	2023 – 2024
<ul style="list-style-type: none"><li>– Enhanced platform with modern Vue.js 3 frontend and advanced Flask backend processing</li><li>– Implemented microservices-style API structure with dedicated modules for comprehensive functionality</li><li>– Integrated Celery with Redis for asynchronous task handling and background processing capabilities</li></ul>	
<b>FPGA Image Steganography</b>   <i>FPGA, Verilog, Digital Signal Processing</i>	2014 – 2015
<ul style="list-style-type: none"><li>– Developed hardware implementation of YASS (Yet Another Steganographic Scheme) for secure communication</li><li>– Implemented resistance to blind steganalysis attacks using advanced algorithms</li><li>– Published in International Journal of Computer Applications - <a href="#">View Research Paper</a></li></ul>	

# Professional Certifications & Achievements

<b>Student Innovation Mentorship Excellence</b>	2018 – Present
<ul style="list-style-type: none"><li>– Established comprehensive innovation pipeline from ideation to commercialization, mentoring 50+ student projects</li><li>– Achieved exceptional funding success: <b>Rs. 25 lakhs from Shark Tank India</b> and <b>Rs. 20 lakhs from government innovation programs</b></li><li>– Resulted in <b>2 filed patents</b> and multiple technology transfer opportunities with industry partners</li></ul>	
<b>NPTEL Excellence &amp; Recognition</b>	2018 – Present
<ul style="list-style-type: none"><li>– <b>NPTEL EVANGELIST</b> (Dec 2020) - Top recognition for exceptional contribution to online learning ecosystem</li><li>– <b>NPTEL DISCIPLINE STAR - Computer Science</b> (Dec 2019, Dec 2020) - Top 1% performer nationally</li><li>– <b>24 Course Completions with 15 Elite Performances</b> - Demonstrating mastery across AI, Data Science, and Programming domains</li></ul>	
<b>Coursera Specializations</b>	2016 – Present
<ul style="list-style-type: none"><li>– <b>Advanced Machine Learning:</b> Deep Learning, Computer Vision, NLP, Reinforcement Learning, Bayesian Methods</li><li>– <b>Core Specializations:</b> Machine Learning, Deep Learning, Recommender Systems, Data Structures &amp; Algorithms</li><li>– <b>Big Data &amp; Analytics:</b> Big Data, Data Mining, Cloud Computing, Business Analytics</li></ul>	

# Professional Objectives

<b>Immediate Goal:</b> Secure AICTE Industry Fellowship to gain advanced industrial research experience in AI, embedded computing, or system design, bridging academic excellence with industry innovation
<b>Long-term Vision:</b> Pursue PhD in AI/Embedded Computing/System Design from premier institution while establishing Gujarat as a leading hub for diploma-level innovation and research-driven education
<b>Impact Mission:</b> Transform technical education landscape by integrating industrial rigor with academic excellence, fostering sustainable innovation ecosystems, and creating scalable models for interdisciplinary collaboration