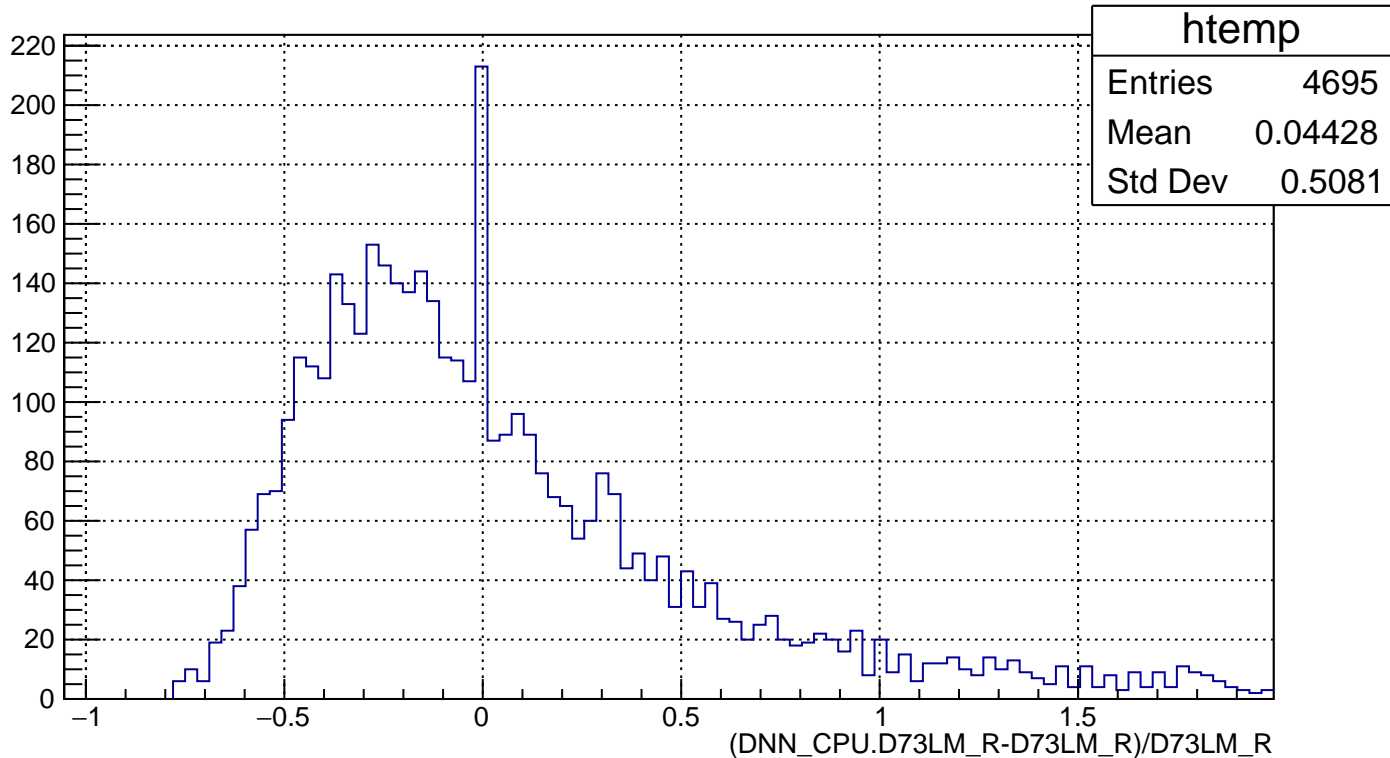
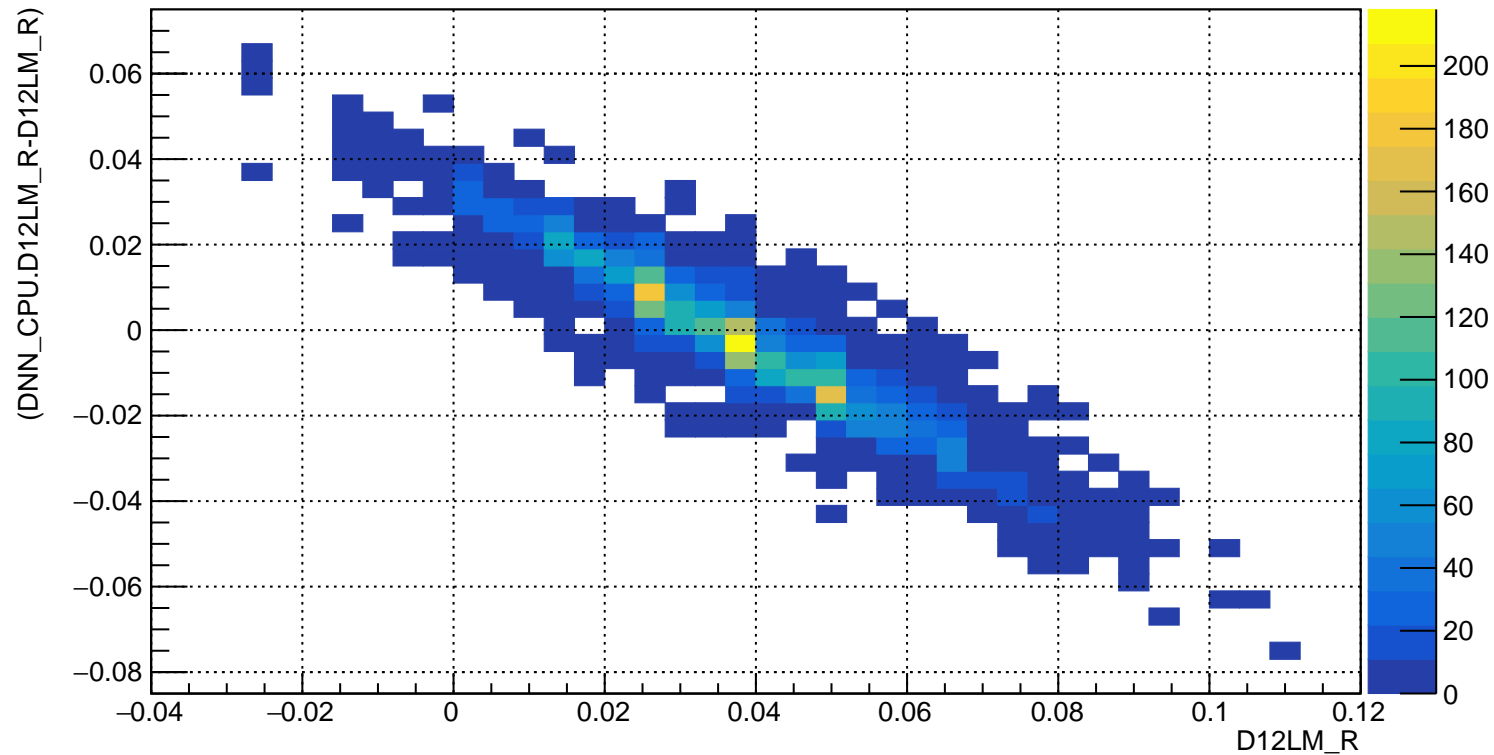


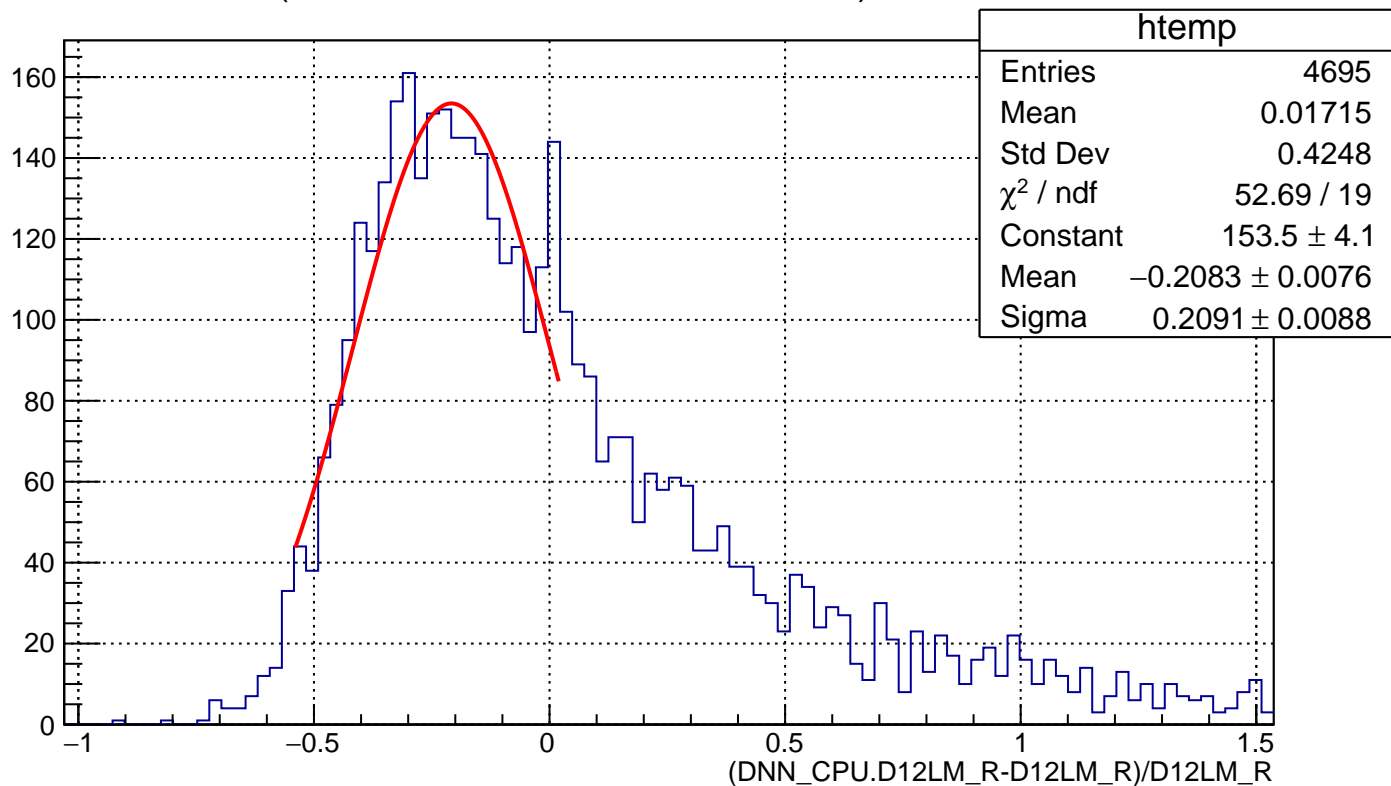
(DNN_CPU.D73LM_R-D73LM_R)/D73LM_R



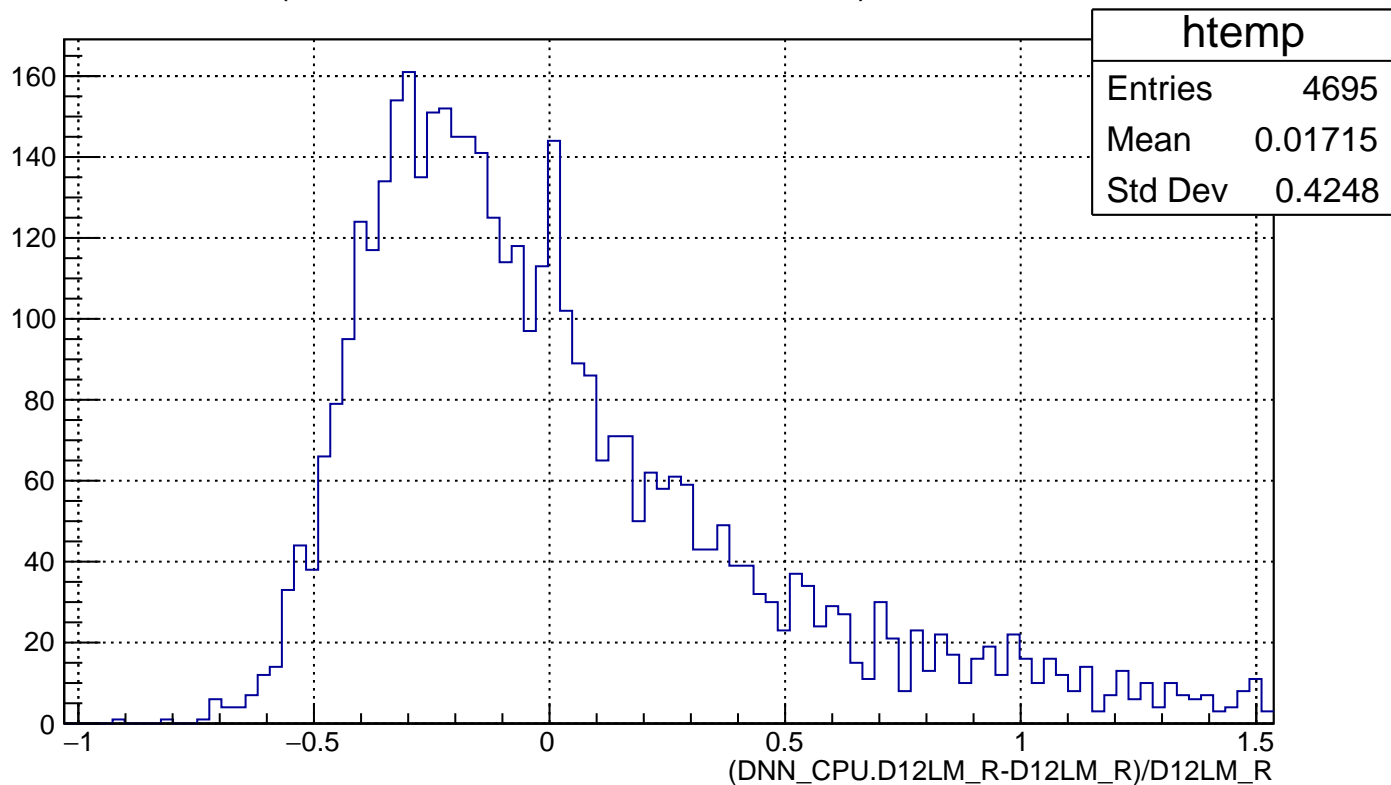
(DNN_CPU.D12LM_R-D12LM_R):D12LM_R



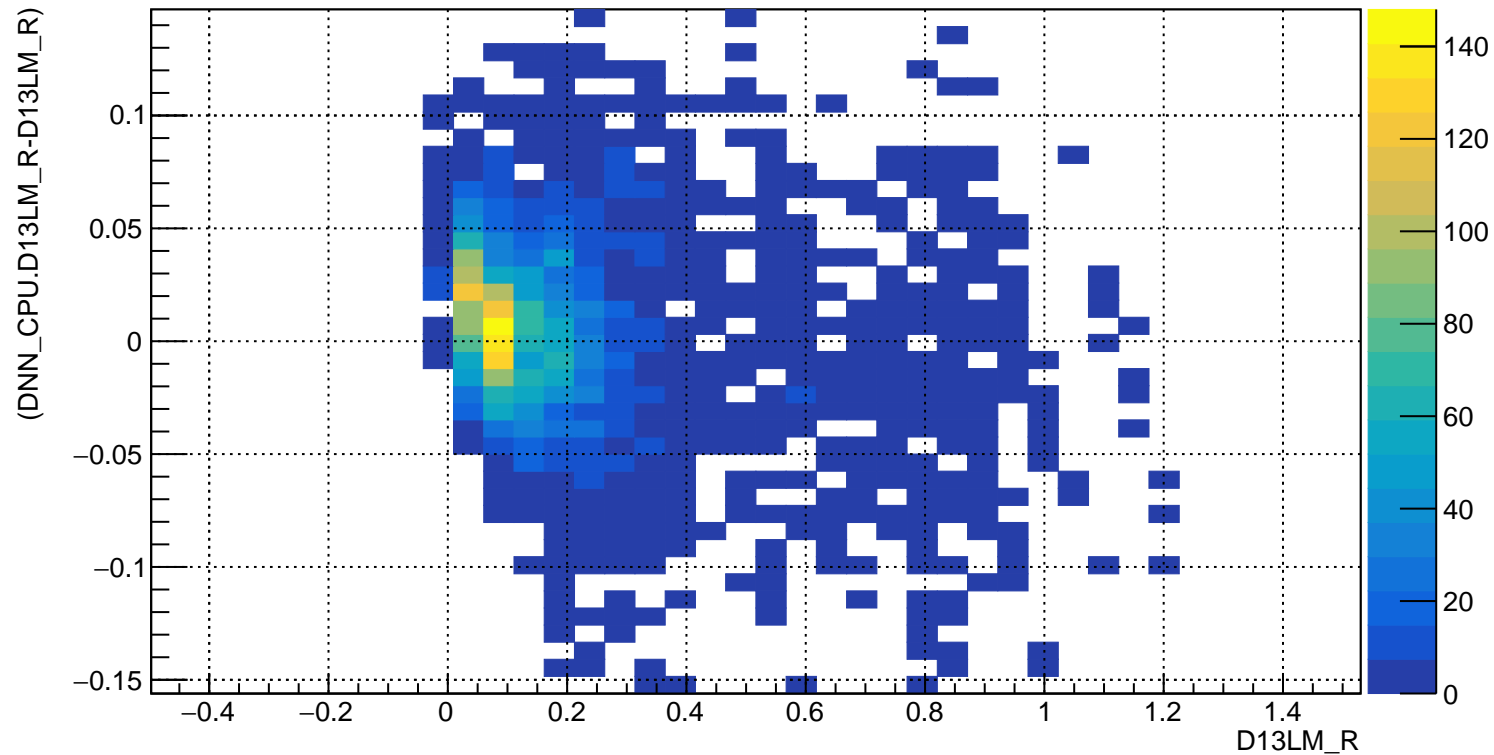
(DNN_CPU.D12LM_R-D12LM_R)/D12LM_R



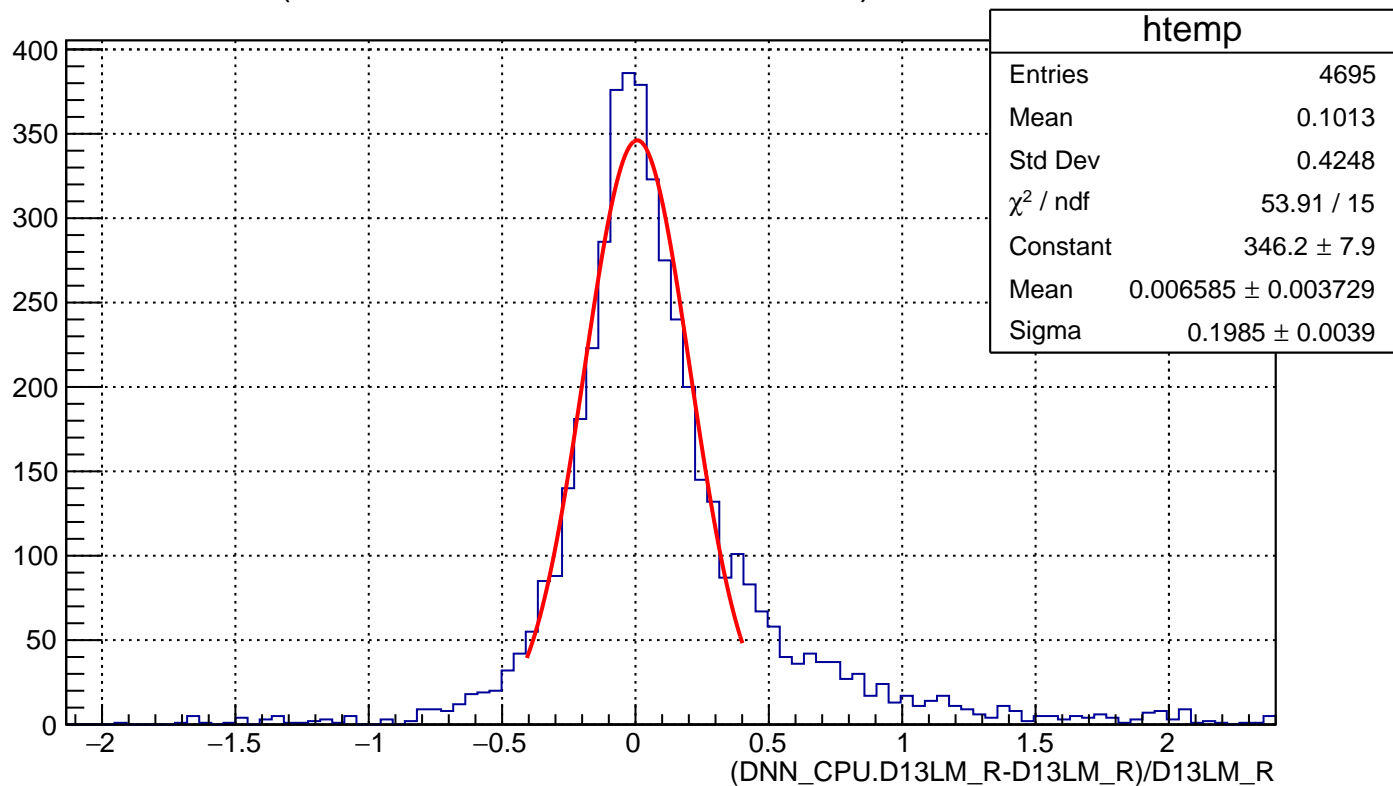
(DNN_CPU.D12LM_R-D12LM_R)/D12LM_R



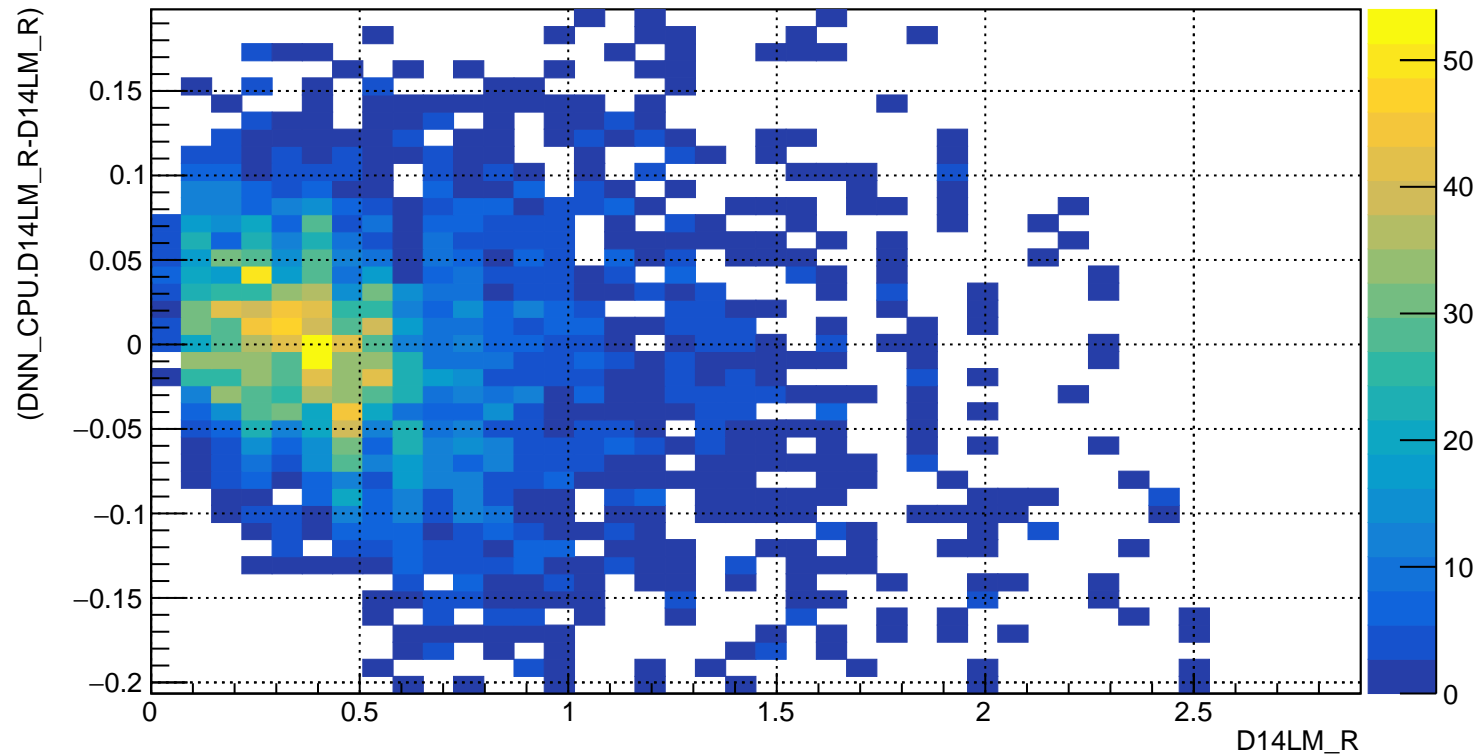
(DNN_CPU.D13LM_R-D13LM_R):D13LM_R



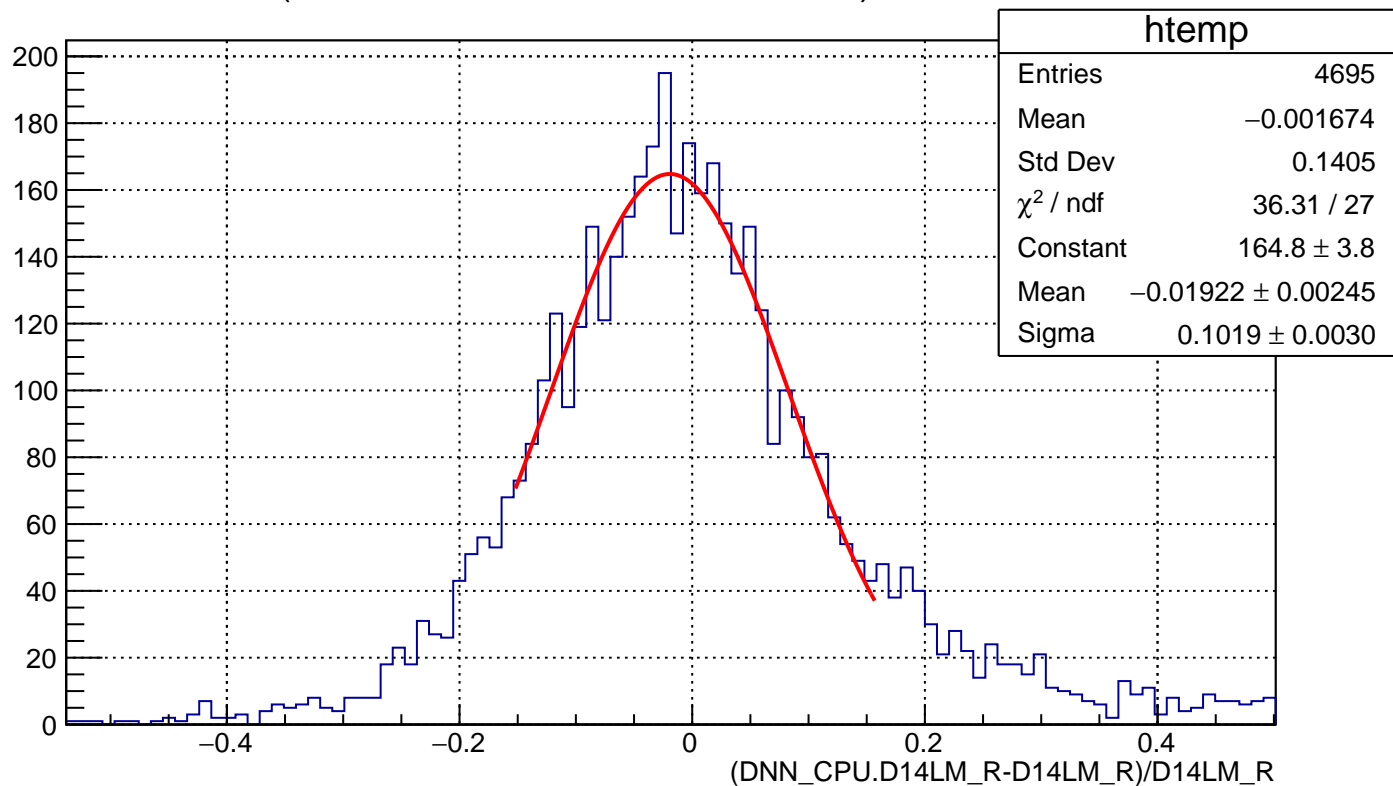
(DNN_CPU.D13LM_R-D13LM_R)/D13LM_R



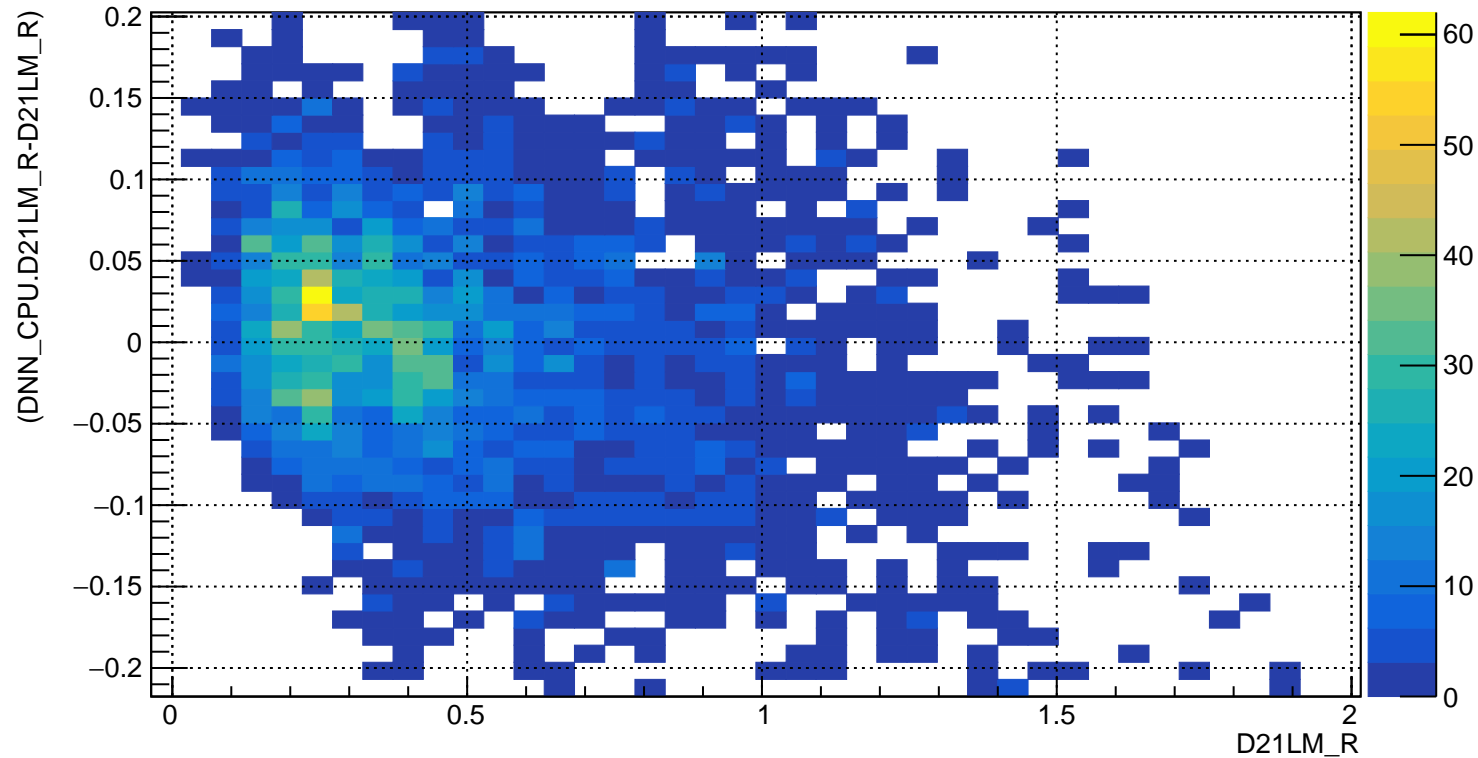
(DNN_CPU.D14LM_R-D14LM_R):D14LM_R



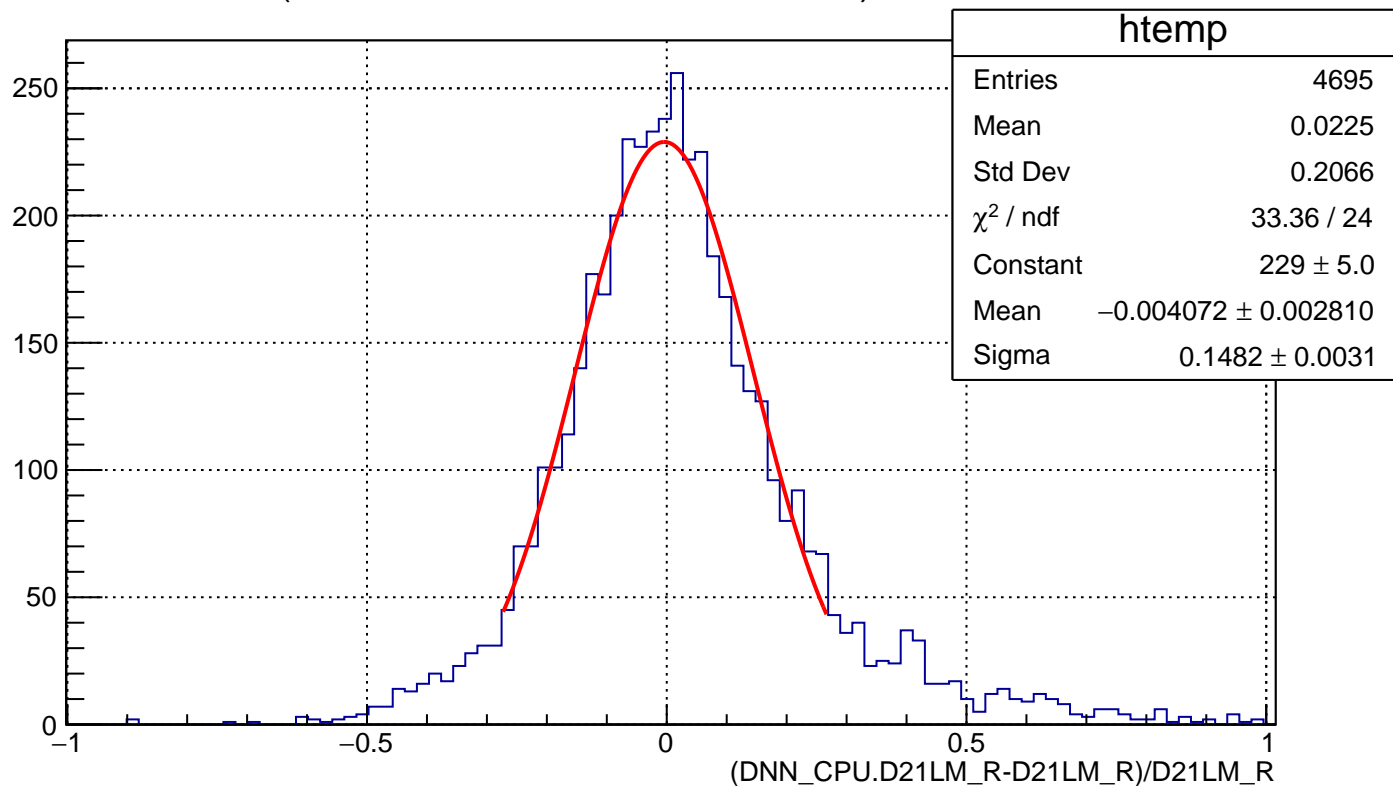
(DNN_CPU.D14LM_R-D14LM_R)/D14LM_R



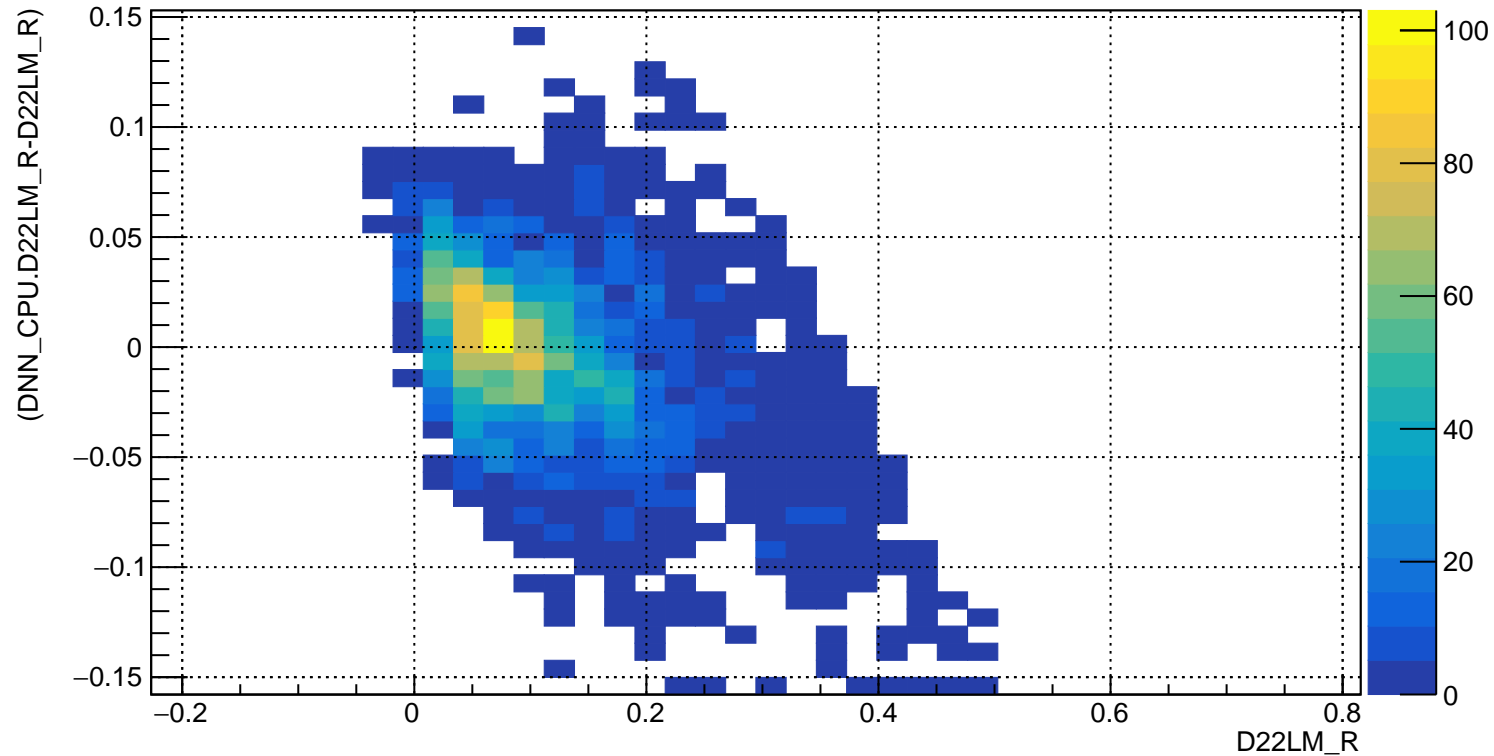
(DNN_CPU.D21LM_R-D21LM_R):D21LM_R



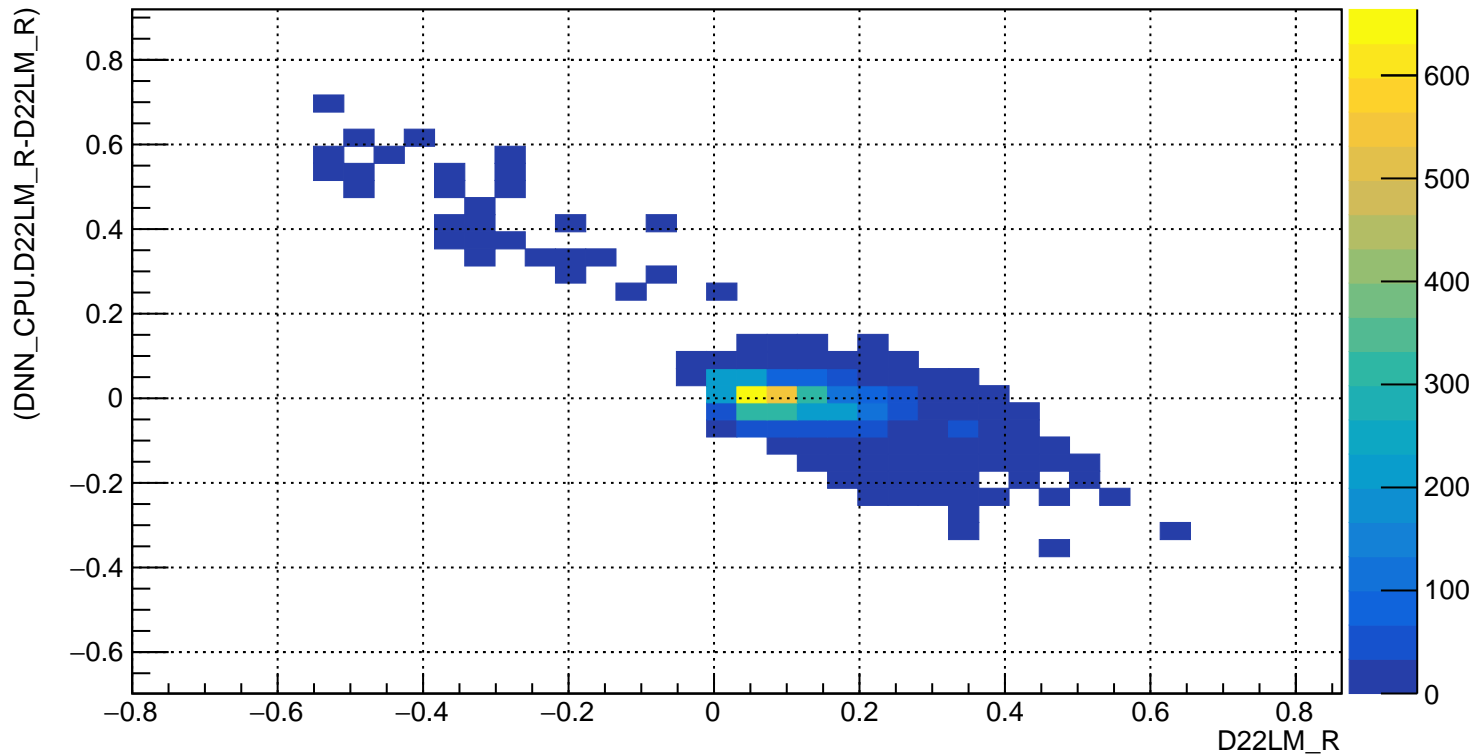
(DNN_CPU.D21LM_R-D21LM_R)/D21LM_R



(DNN_CPU.D22LM_R-D22LM_R):D22LM_R



(DNN_CPU.D22LM_R-D22LM_R):D22LM_R



(DNN_CPU.D22LM_R-D22LM_R)/D22LM_R

