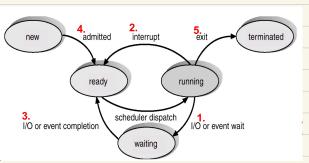
do questions from text pg 326

Basic Concepts CPU-1/0 Bunts: process execution alternates b/w CPU and 110 ops. CPU burst > duration of 1 CPU ere. Cylle 110 bure + ) duration of 1 110 operation (wait time) CPU scheduling objective: keep the CPU busy as much as pose. focussing on concepts of short-term (ready queue) scheduler for uni and multiprocessor Crus load store CPU burst add store read from file one cycle I/O burst wait for I/O store increment CPU burst write to file I/O burst wait for I/O load store CPU burst add store read from file wait for I/O I/O burst

#### (PU Scheduler (Short Term)

goal scleet process (1/t) in the ready queue and allocate it to the CPU (1 process/core)

the scheduler runs whenever the process changes chate



· ready -> running is called scheduler dispatch always made by the CPU-scheduler

always made by the CPU-schedule

the CPU scheduler has to execute when a process is in the exit or event / 110 wait, else the CPU will idle

1. Nonpreemptive once a process has been assigned to the cru, it keeps it until it voluntaring the cru either by a) terminating b) 1/0 or event wait

scheduling is nonpreemptive if it happens only for exit or 110/event wait land occasionally admission when core is idle)

2. Preemptive: CPV can be taken away from a running process at any time can be for 1,5 and 4 but ALSO for 2,3,4 or at

Scheduling Objectives

# 1. Max CPU utilization → measured by total exe time on each core total time x no of cores

any other time instant

2. Max throughput -> number of processes completing exeptime

no. of exit transitions per unit time

	1.	Min. turnaround time: amount of time taken to execute a
		(state) (named time) process
		running (QU burst) - from "admitted" to "exit"
S	+	waiting (110 burst) average over au processes is
S recipio	+	ready (waiting time) denoted as average turn around
ر د		turnaround time time
) 		
427	2.	Min waiting time: amount of time a process has been
ייאס כל		waiting in ready state
7		NOT THE TIME IN THE WAIT STATE
	3.	Min response time: time from a request submission
		(admitted) to response produced
		(assumed to coincide with start of
		1000000 100000 100000 100000 100000 1000000

execution)

assume a single CPU burst, single CPU core per process. focus on average waiting time 2. SJF 3. priority 4. Round 5. Multilevel

T Scheduling 7 Robin Scheduling Scheduling Algorithms: Uniprocessor System (1. First come First Serve Scheduling) as the name suggests. can be implemented using a FIFO queue  $P_3$   $\rightarrow$  all arrive at 0, in the order P1, P2, P3waiting time:  $P_1 \rightarrow 0$  $\rho_2 \rightarrow 24$ avg = (0+24+27)/3 = 17 $P_3 \rightarrow 27$ since the process keeps the CPU until termination / wait this is non-preemptive. if they artive in the order Pz, P3, P1: waiting time PI > 6 P2 -> 0 avg = (6+3+0)/3 = 3P3 → 3 clearly, average waiting time is affected by arrival order nonpreemptive: processes have to voluntarily vuesse the CPU once allocated · convoy effect: short processes suffer long waiting times if they arrive after a long process

## 2. Shortest Job First (STF) Scheduling

prioritizing processes based on their (PU burst lengths

- shorter burst - higher priority.

-handles convoy effect of FCFS 1. Nonpreemptive: once the core is given to a process, it Stays until the CPV burst is completed

2. Preemptive | Snortest Remaining time first: if a newly created process has CPV burst (ength less than the remaining CPV burst of a currently running process, then preemption

# SJF | SRTF is optimal for optimizing any waiting time

**Burst Time** 

(7-S-2)

Arrival Time

will occur and if will get switched out.

non prem.

	$P_1$	0.0	7		•	ווטו	Ρ,	-//·			
	1 1	0.0	,				•				
	$P_2$	2.0	4								
	-			l Pi		Pi   P		Pλ	Pγ	P4	P4
	$P_3$	4.0	1					ر ،	1 2		
	$P_4$	5.0	1	0	:	į	: 7	9	}	12	
	' 4	0.0	7		00	; P	3 P4				
					P2		-	٠.	00/0 10/0	iting tiv	N 0
					(6)	( :	3) (7	<b>-</b> )	wig wa	ara ig ra	, 40
									= (	0+6+3+	7),
0.0	(- 0.100 F	_							- 4		
ργ	e-emp	ν,								•	

pre-emp.			- 4
		,	
, P1 P,2 P3	P2 P4	PI	
0 2 4	5 7 1	1 16	
5 huy 2 huy	). P4	wa	waiting = (0+9+1+2)
(11-2=9) (5-1=1)	9 Left	J	= 3

## 3. Priority-based Scheduling

a priority number (interger) is associated with each process

4 smallest number ⇒ greatest priority

FCFS & SJF are forms of this

Lev burst (engther)

arrival time

Problem of starvation: lower priority may never execute

Solution: Agina

solution: Aging

as time progresses, system gradually increases priority

#### 4. Round Robin (RR) Scheduling

a "fair scheduling algorithm"

timer
inturnuptall processes get the CPU for q time units; pre-empted
after and placed in the ready queue

· q = quantum time (normally 10-100 milliseconds)
· for n processes in the ready queue → waiting time
is not more than
(n-1) q

higher ang waiting/turnaround time than SJF, but better response time.

corge q (greater than CPV burst) → becomes FCFS small q (many context switches, high overhead)

no well defertlation blw any turnaround time & quantum size although of c: any turnaround time is fixed once quantum size is greater than the max cpu burst length

### 5. Multilevel Queure Scheduling

diff processes have different requirements and hence need different scheduling

so the ready queue is partitioned into separate queues and each queue has its own scheduling algorithms.

foreground processes: keystrokes, 1/0 interaction need to be interactive - fast response time so RR is pref.

background processes: batch processing, virus scanning need not be interactive → FCFS.

each queue has its own scheduling and the queues

themselves need to be scheduled to.

fixed priority

time slice based

Serve all foreground queues - each queue gets a fixed time first, then after serving quantum on the CPU au processes, consider (80 ms fore, 20 ms back)

the background queene

the background queene

the background for lower priority

queue

1. Partitioned scheduling

2. Global Scheduling

1

Scheduling Algorithms: Multiprocessor System

ASSUME multi-core CPU, each process can execute on only one CPU-core at any time instant.

(Asymmetric Multiprocessing AMP)

Processes are partitioned apriori (based on theoretical deduction) among the CPU cores (at process creation time)

reach process is mapped to one core separate uniprocessor CPV scheduling for each core

several queues, specific to cores.

advantages

core-specific scheduling strategy is feasible (FCFS,STF, RR, multilevel)

easy and simple extension of uniprocessor CPU scheduling to multiprocessor CPU scheduling.

disadvantages

mapping is tough, the load must ideally be balanced else works could be idle while others are overloaded kind of like knapsack problem hurristic such as best-fit can be used to assign the process to the CPU

#### 2. Global Scheduling (Symmetric Multiprocessing)

one or more ready queues for the whole system (no more matching)

when any core becomes Idle, CPV scheduler runs and allocates the next process to execute

process solution based on strategy applied globally or symmetrically across all cores (FCFS, SJF, RR)

the same process could execute on different cores at different time

pre-empted.

disciduantages

implementation complexity is night compared to partitioned scheduling

Les the cores need to be synced, schedular must use 2 clock.

I clock.

I selection of next process may involve going through services processes in the ready queue

cache, so data needs to migrate which is time consuming overhead is high

