

·	communication tech wire winess USB, HDM1 Wifi, *
	UI devices L keyboard, capacitive touch, camera, mrc, display, speaks
	Togo void, applicative to the state of the s
	USB
	universal serial bus
	UART protocol
-	Stardard commution to most 1/1+
•	4 pins: power, and, data+,~ (an be self powered)
•	USB1.0 \rightarrow 12 Mbps USB3.2 \rightarrow 20 Gbps
	USB3.2 -> 20 Geps
•	tiered star topo logy
	USB MOST
	initiates transfer USB MOST — USB Hub
	· all transfer is relative toit
	·
	for connection:
	(typ1: ennumeration (device into host)
	stip 2: host checke if it can drive the device
	classes of USB derives
_	NID: human interface
	communication device class ((DC) implement virtual com
	- Arduino et.
_	Mass Storage Class
_	Mass Storage Class Aerdio Class

	HDMI High-definition multimedia interface transfers audio-visual data, allows control info to pals
,	enabling remote control: CEC NDMI V1: 3-966bps: 1080p60fpe NDMI V2: 12.6 Gbps: 8k rs.
	Industrial scientific & Medical (ISM) Radio freg(RF)Band 2: 4 GHz, 5.8 GHz are popular there are voyality free, also used for wifi + buttooth
	wifi 2 topologies - Intrastructure Adnoc 1 ** to renter of network peer to peer
-	is access pt) router $\frac{1/\infty}{\text{transmission range is affected by freq, power, interference}} \sim 10 \text{Gb ps} , 20 - 150 \text{m} . $
-	Bluetooth Low rate, low power, 10-m20 m, rate: Kbpe 2.4 GHz range star topology 2 protocols — Bt classic — Blf (low energy)
	2 protocols — BT classic (Blf (low energy) but most hosts are dual mode, can convert to both now trequency hopping

Capacitive toruch capacitance I on touch Li ∝ dielectric constant processor used to figure out pt of contact. Camera sen16rs (amos, + Analog-to-Digita)) I mage signal Brocessor (ISP) Interface 1/0 microphone charge nuded is found on electrical mems & ELM = 2 kind both use capacitos, transduced signal is analog charge by pump MFMs > ECM + smaller, better signal to noise ratio speaker electric - mechanical - strund energy

Puplline

autim

dont use common
work best when full
pupiletine conflict - halt to piplein, flush instruct.

O insufficient resource
same resource, wrice mem, & read mem
have suft resource, processing units to allow simultaneous

Q data dependency conflict
using old value
source operand of our = dast op of prior

→ colve by detecting dependency and stalling our op
before ex stage Cie Decode Stage)

→ insert nops

© branch Unst 2 cycles wasted as decision to branch is in Ex stage © decision made earlier in devode stage (add a adde)

delay= 1 cycle D delayer brachling

(2) instructions after loop is always executed.

Ly must be independent -- affecting flags !!!

D dynamic branch prediction

Now to predict outcome of branch inst: T/F

according lines up Next inst.

can be cornect or wrong?

Arm contex Processor: 3 stage pipeline (F, D, Ex)
has branch speculation
fetches inust of both T&F situation during D, Decodes the correct one when in Ex

