


- Communication tech
 - wire: USB, HDMI
 - wireless: Wifi, *

- UI devices
 - ↳ Keyboard, capacitive touch, camera, mic, display, speaker

USB

- universal serial bus
- UART protocol
- standard connection to most UI
- 4 pins: power, gnd, data+, - (can be self powered)
- USB 1.0 → 12 Mbps
- USB 3.2 → 20 Gbps
- tiered star topology
 - ↳ USB Host
 - initiates transfer
 - all transfer is relative to it

USB Host — USB Hub

for connection:

step 1: enumeration (device ^{info} → host)

step 2: host checks if it can drive the device

classes of USB devices

- HID: human interface
- communication device class (CDC) implement virtual com
 - ↳ Arduino etc.
- Mass Storage class
- Audio class

Capacitive touch

- capacitance \uparrow on touch
- $\hookrightarrow \propto$ dielectric constant
- processor used to figure out pt of contact.

Camera

- lens
- sensors (CMOS, + Analog-to-Digital)
- Image Signal Processor (ISP)
- Interface I/O

microphone

charge needed is found on electret

- MEMS & ECM \leftarrow 2 kind

both use capacitor, transduced signal is analog

charge by pump

MEMS > ECM \leftarrow smaller, better signal to noise ratio

Speaker

electric \rightarrow mechanical \rightarrow sound energy

Pipeline

- don't use common
- work best when full
- pipeline conflict \rightarrow halt to pipeline, flush instruction

① insufficient resource

same resource, write mem. & read mem

have soft resources, processing units to allow simultaneous action

② data dependency conflict

using old value

source operand of cur = dest op of prior

\rightarrow solve by detecting dependency and stalling cur op before ex stage (ie Decode stage)

\rightarrow insert nops

③ branch inst

· 2 cycles wasted as decision to branch is in Ex stage

① decision made earlier in decode stage (adda, addi)
delay = 1 cycle

② delayed branching

(2) instructions after loop is always executed.

\hookrightarrow must be independent \rightarrow affecting flags !!!
 \hookrightarrow or nop

④ dynamic branch prediction

how to predict outcome of branch inst: T/F
according lines up next inst.

can be correct or wrong \uparrow



Arm cortex processor : 3 stage pipeline (F, D, Ex)

↓

has branch speculation

↓

fetches inst of both T & F situation during D,
Decodes the correct one when in Ex

