


PROJECT COVER

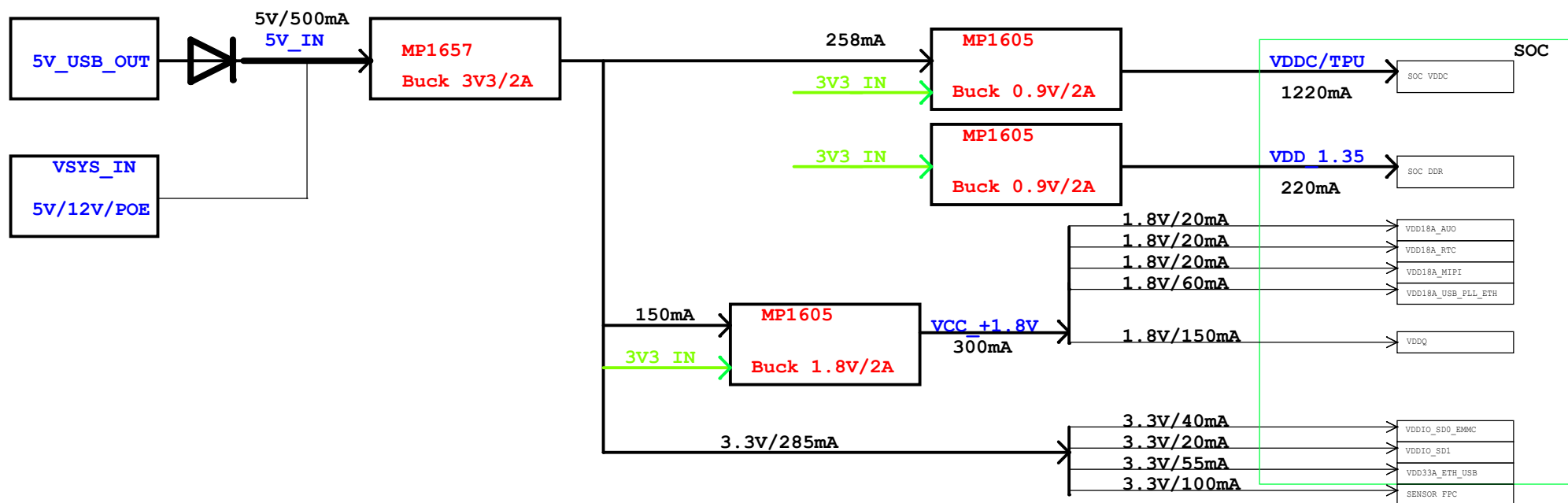
Board Name	DUO_256M
Board Version	V1.0
Designer	
Reviewed by	
CIS Library	YES

			
Project:	SG2002		
File:	01 Project Cover		
Date:	Tuesday, January 02, 2024	Rev:	1.0
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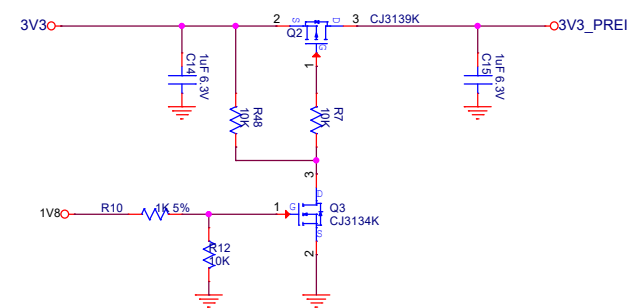
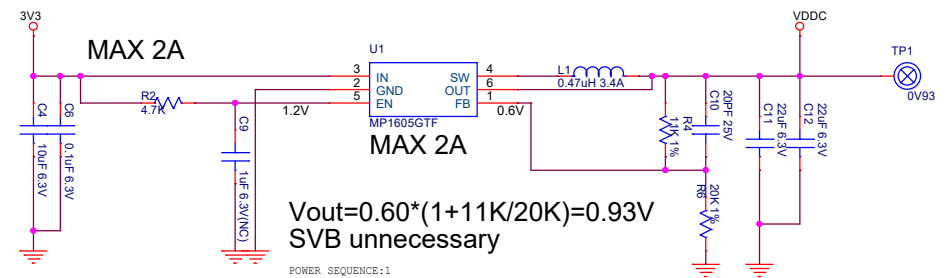
Power Tree




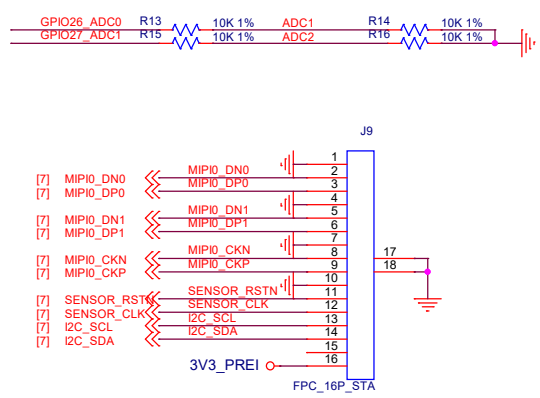
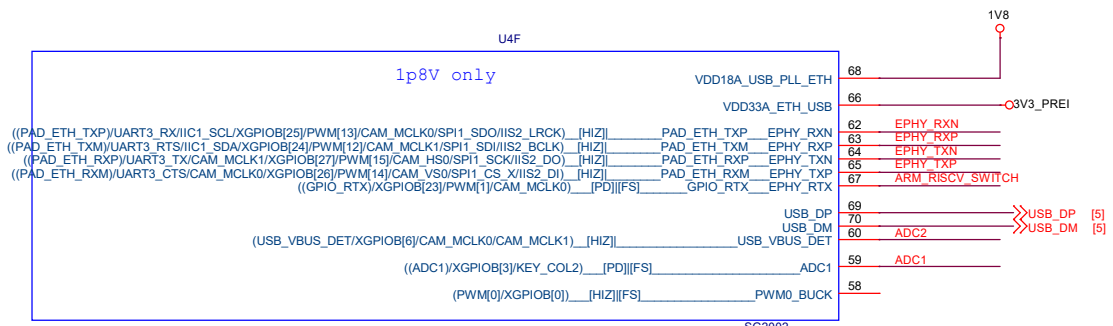
milky

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File:	04 Power Tree		
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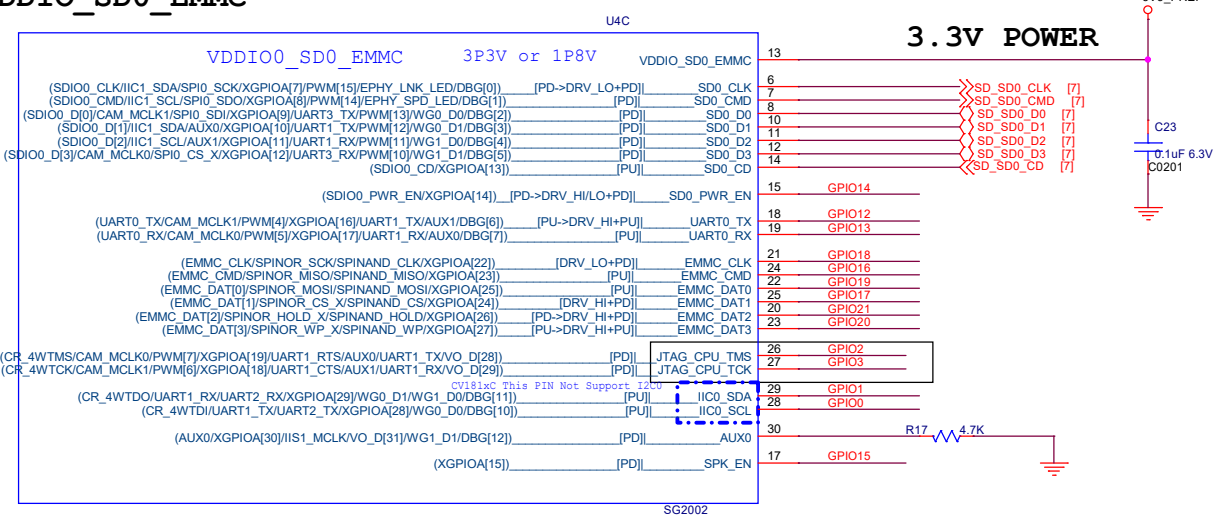
ilkv



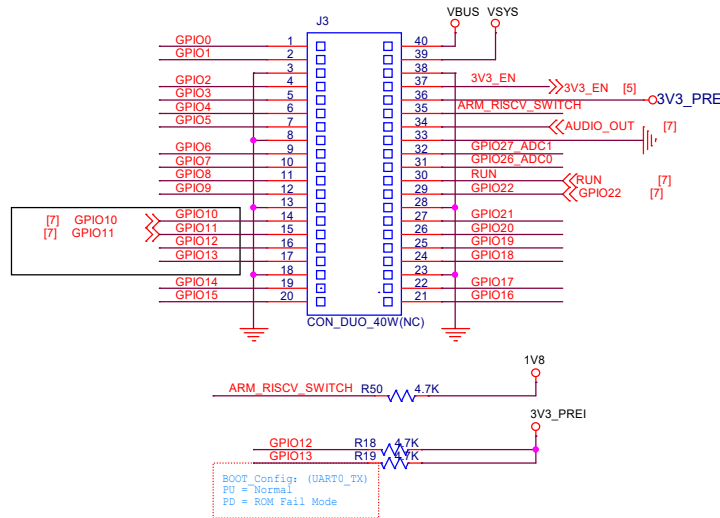
			
Project:	SG2002		
File:	05 Power		
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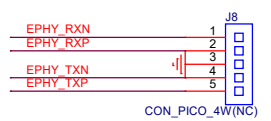
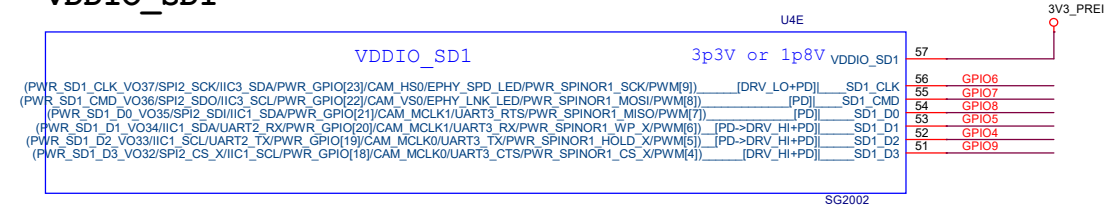
VDDIO_SD0_EMMC



3.3V POWER



VDDIO_SD1



milkV

Project: SG2002

File: 06 SG2002 PART1

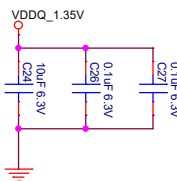
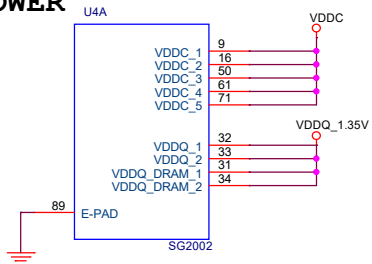
Date: Tuesday, January 02, 2024

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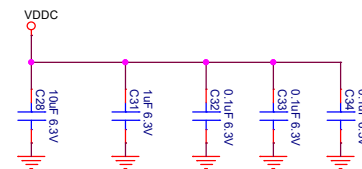
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DDR2 POWER

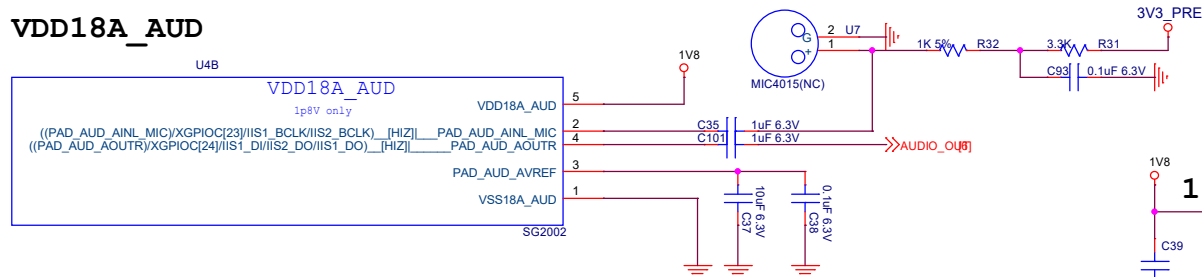


VDDC&TPU POWER

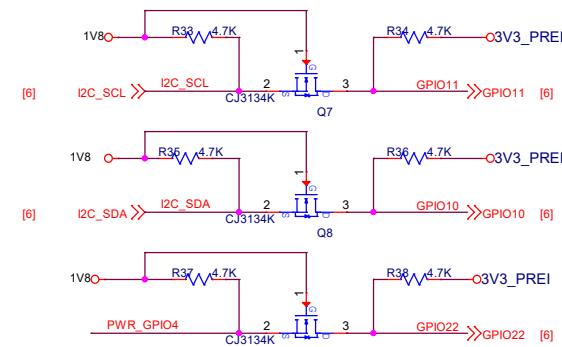
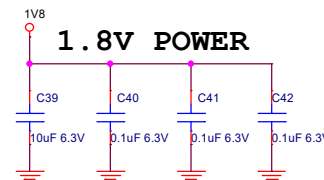


SG2002 II

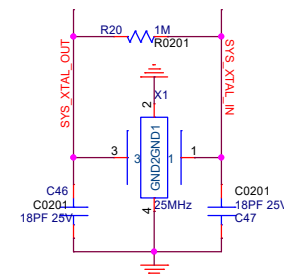
VDD18A AUD



1.8V POWER

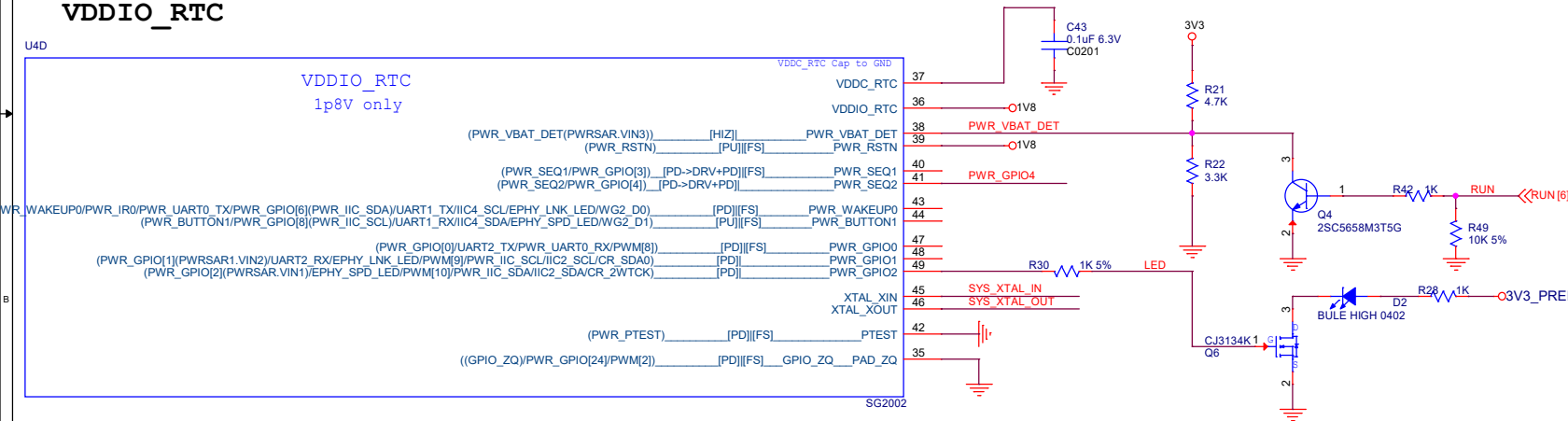


LEVEL SHIFT

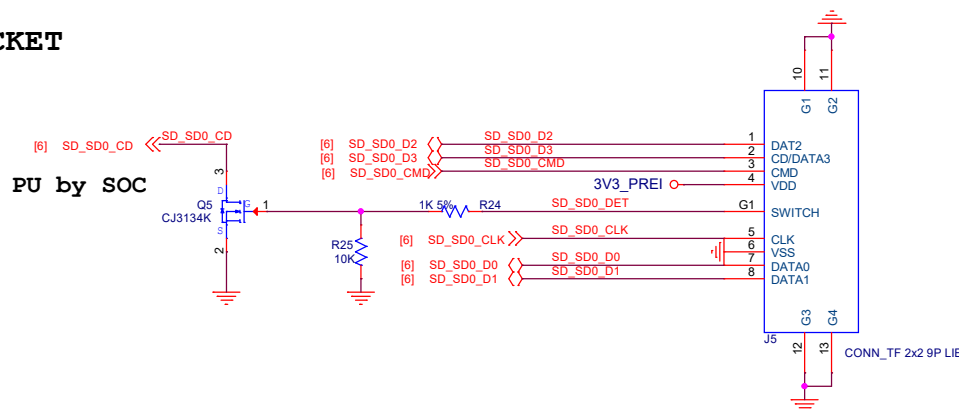


SYS XTAL

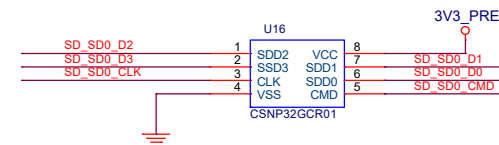
VDDIO_RTC



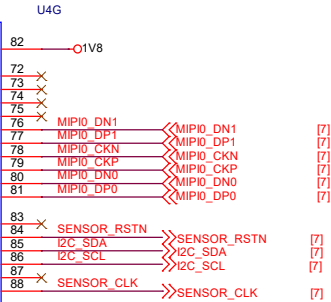
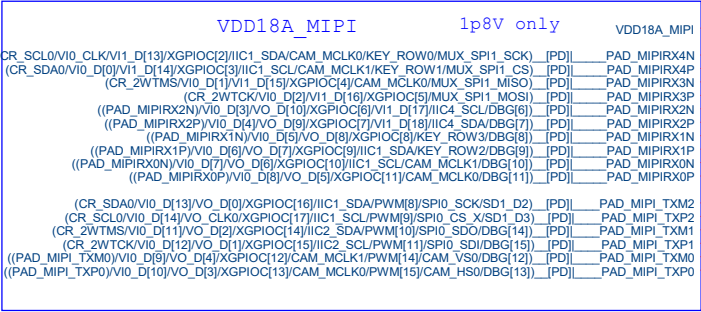
TF_SOCKET



SD NAND



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REV.	CHANGE LIST	EDITOR	DATE
V 1.0			2023/12/18
V 1.1			
V 1.2			