
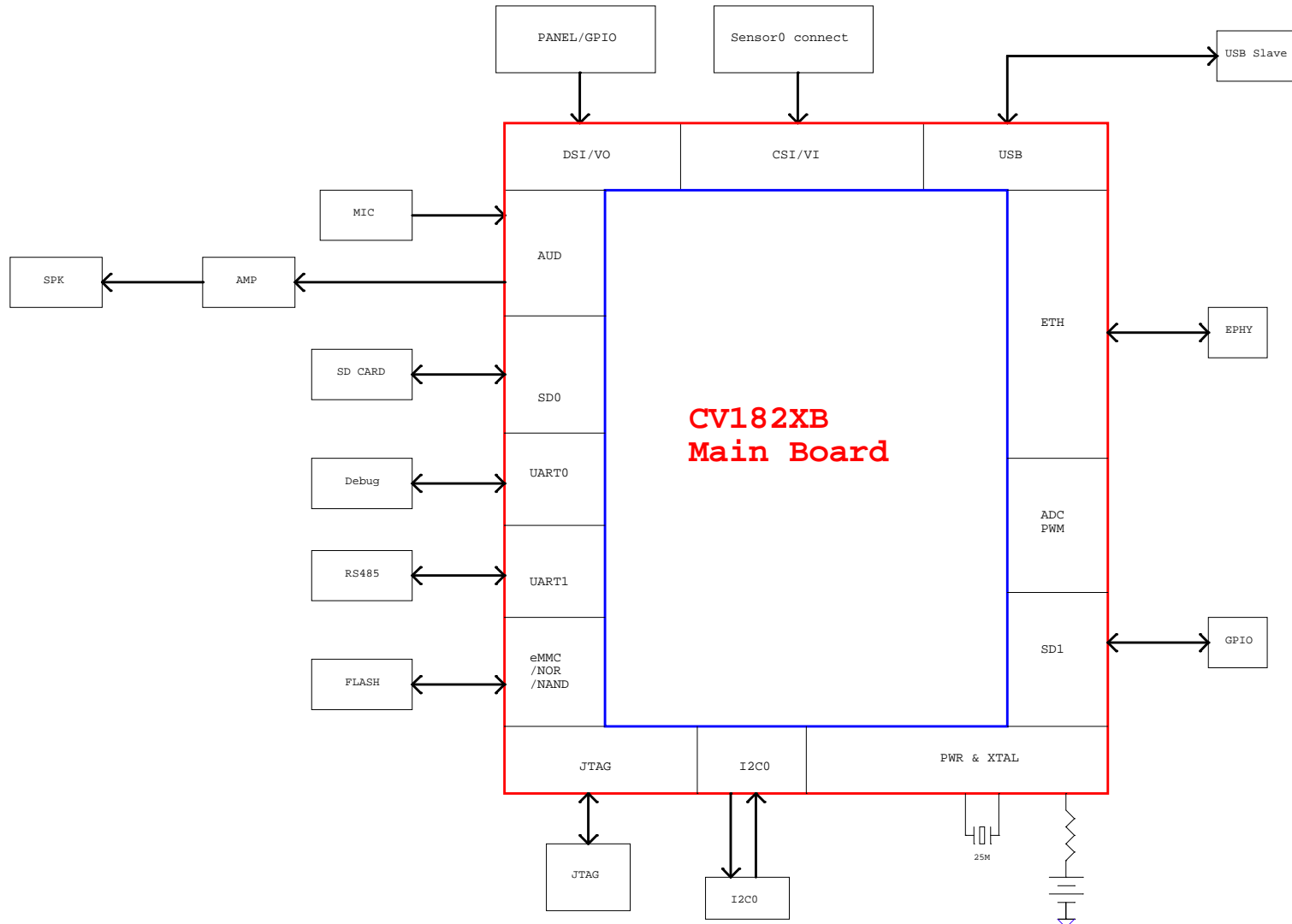
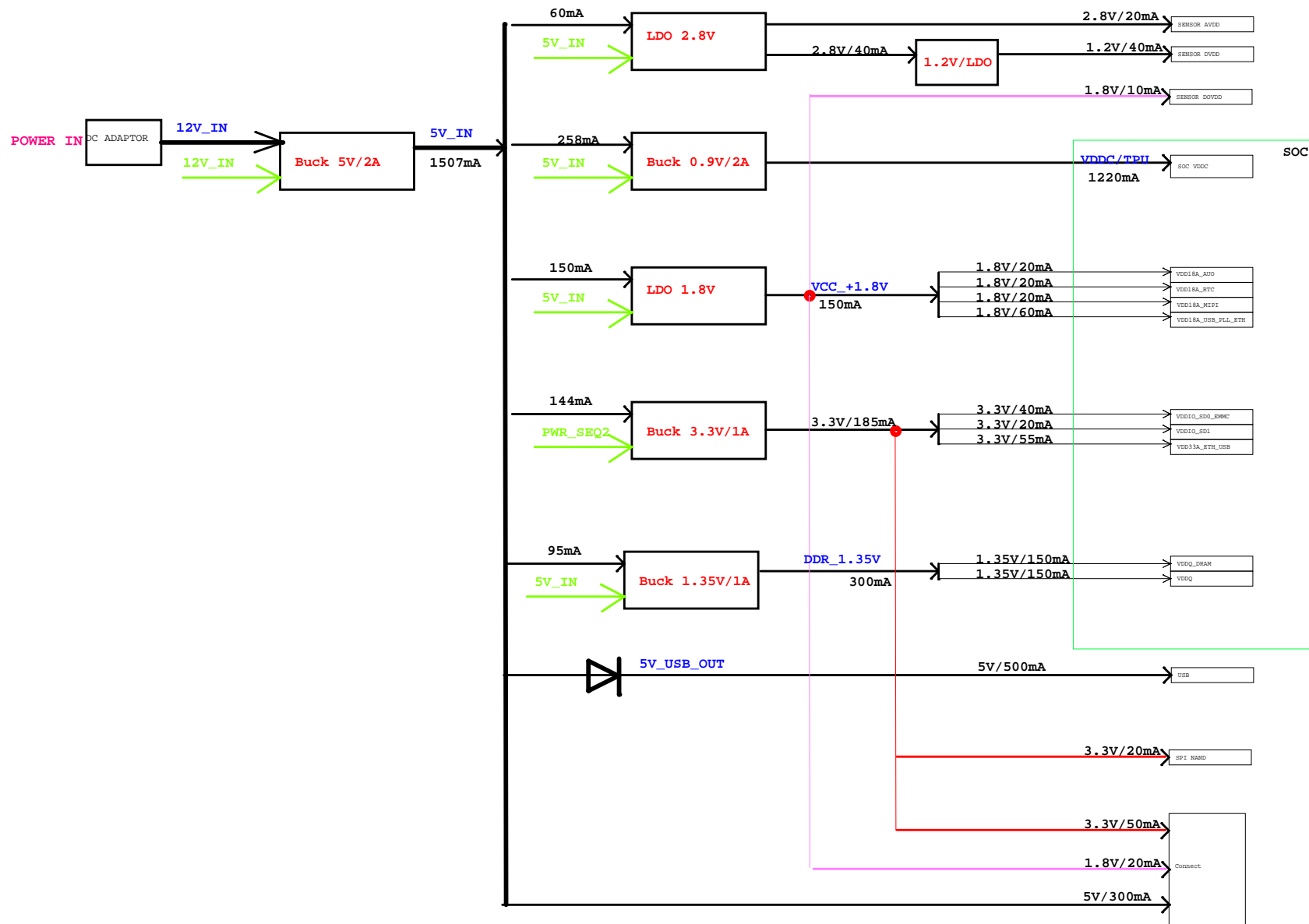


Version List			
Version	Change List	Date	Author
1.0.0	First Release	2021-11-11	Miller.Zhang
1.1.0	1、C77,C78 100nF电容NC 2、增加R16	2022-6-10	Miller.Zhang
1.3.0	1、增加CV1812C-P VDDC电压0.92V说明 2、C39由10uF改为22uF	2022-12-28	Miller.Zhang

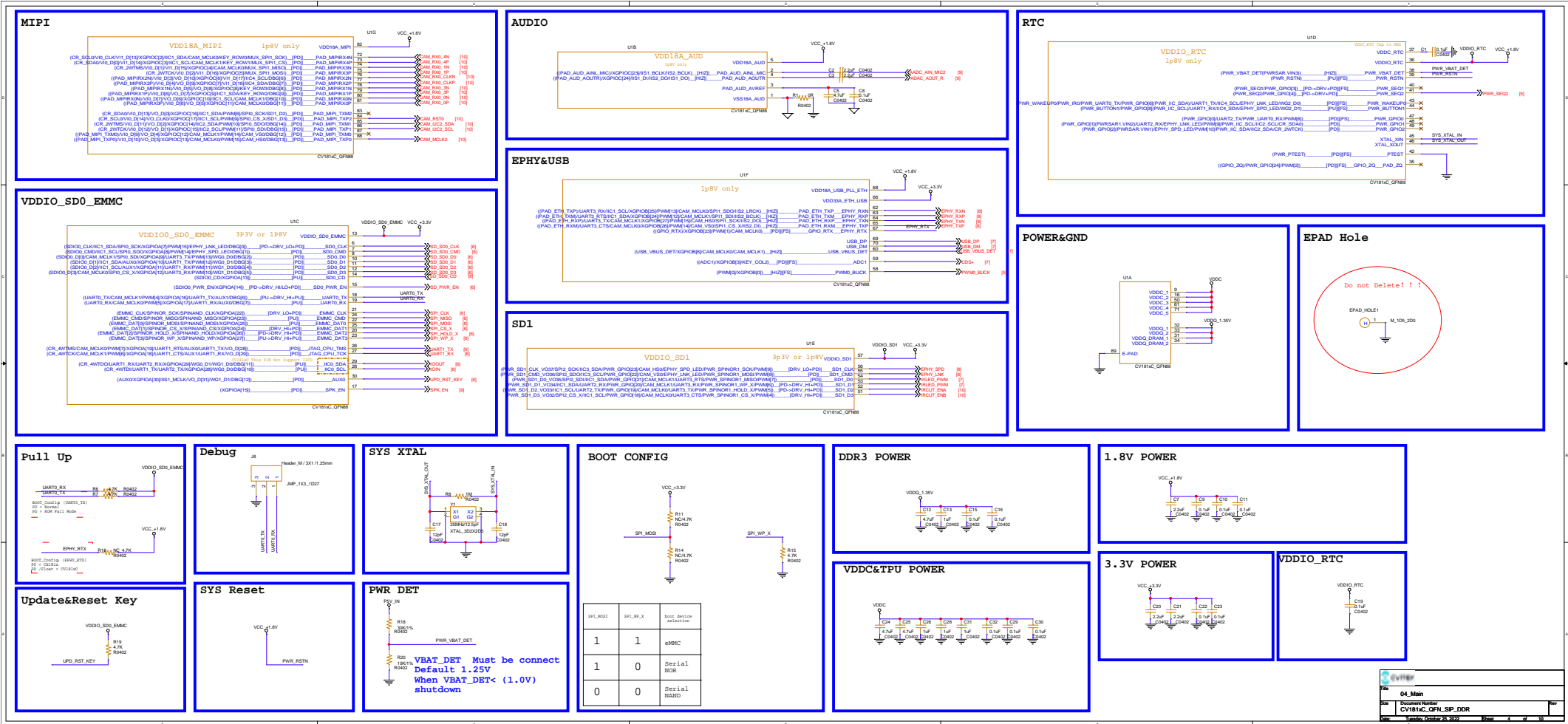
		
Title 01_Cover		
Size	Document Number CV181xC_QFN_SIP_DDR	Rev 1.0.0
Date: Wednesday, March 01, 2023		Sheet 1 of 10

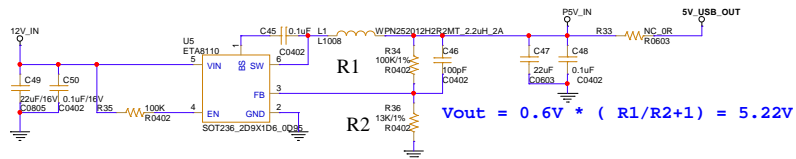
Block Diagram for CV182XB



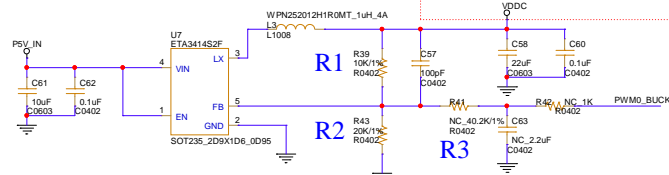
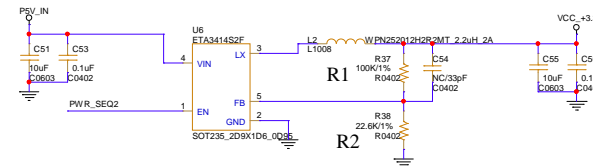


CVITEK		
03_Power tree		
Size	Document Number	Rev
	CV181xC_QFN_SIP_DDR	1.0.0
Date	Monday, September 19, 2022	Sheet 3 of 10



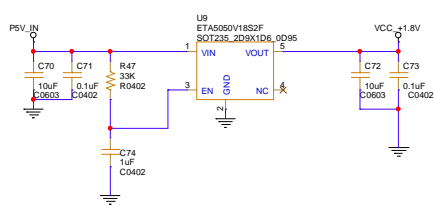
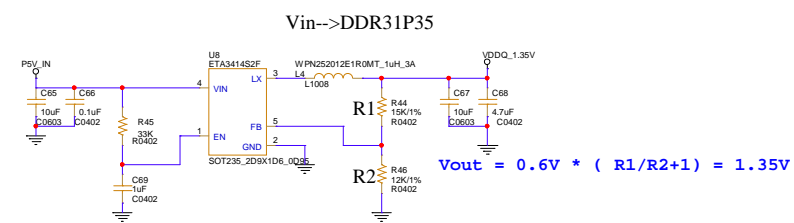


PWR_SEQ02

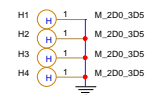


Note:
CV1812C-P: Need 0.92V!!!

Note:
CV1811C/CV1812C: DDR3L VDDQ=1.35V
CV1810C: DDR2L VDDQ=1.5V

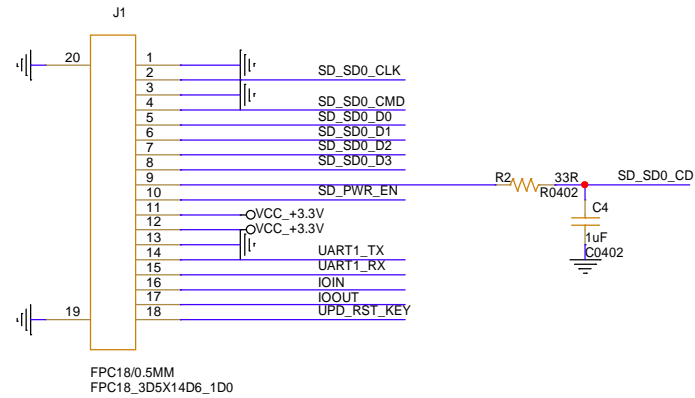
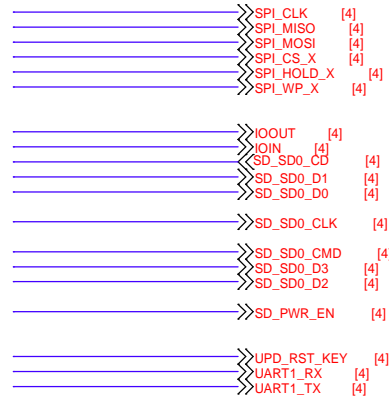


→PWR_SEQ02 [4]
 →PWR_SEQ02 [4]

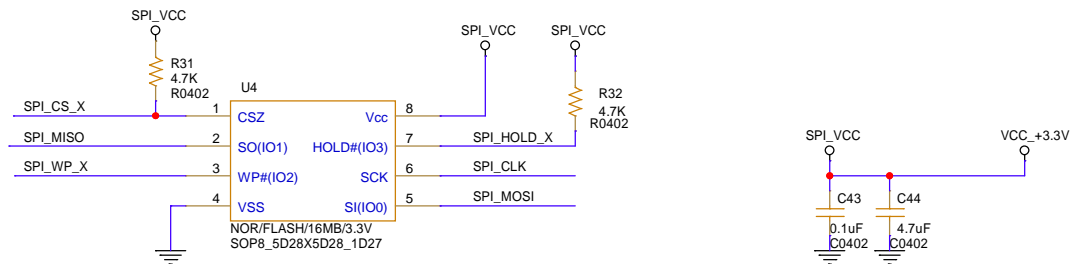


CVITEK		
Title	04_SYS_Power	
Size	Document Number	Rev
	CV181xC_QFN_SIP_DDR	1.0.0
Date	Wednesday, December 28, 2022	Sheet 5 of 10

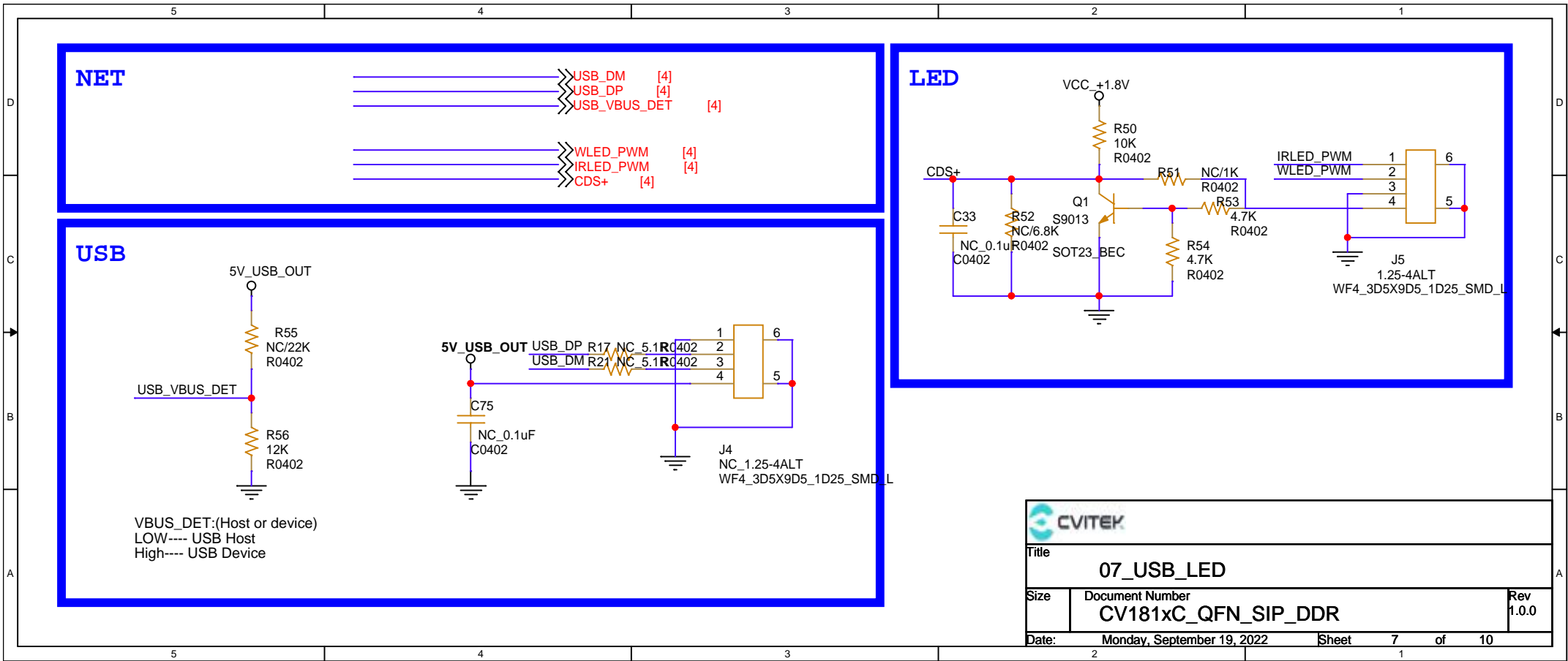
NET

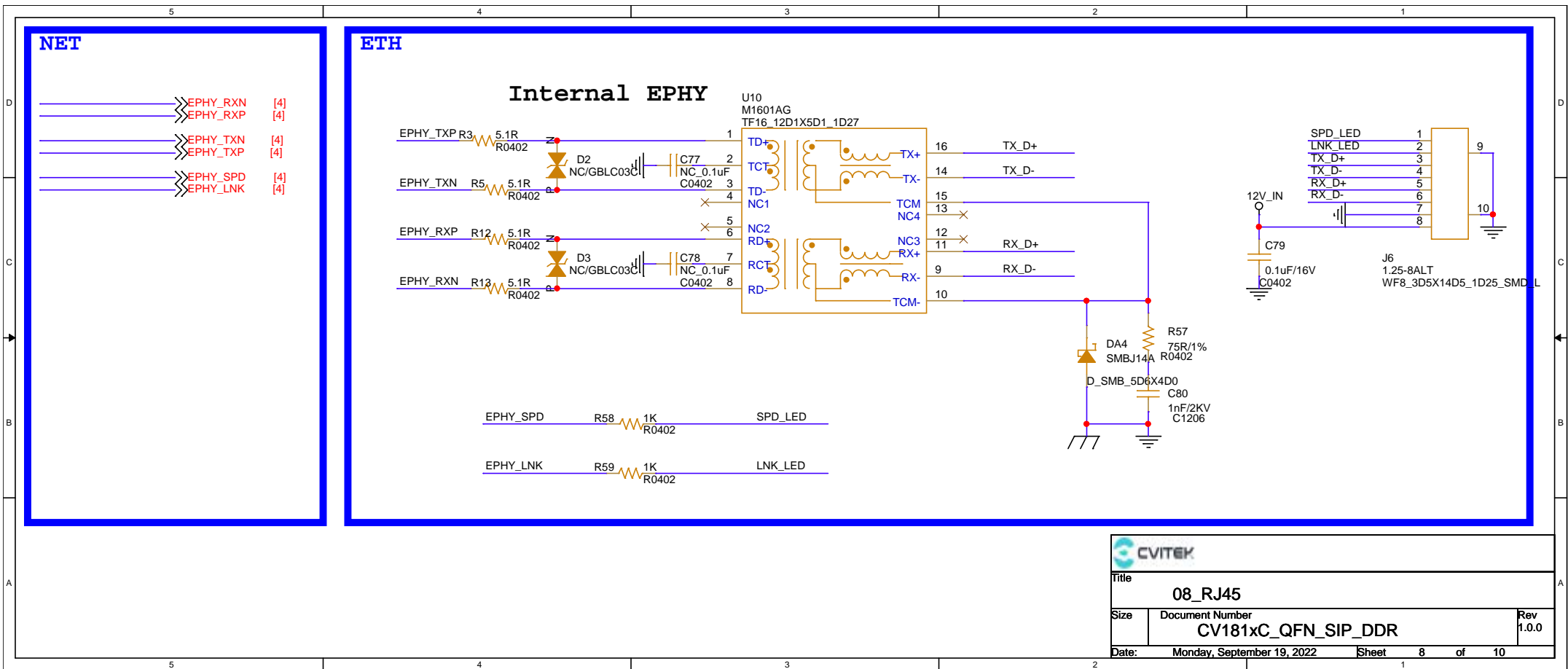


SPINOR



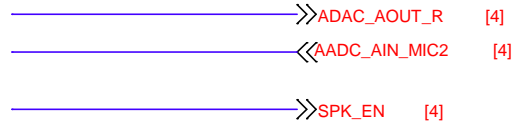
CVITEK	
Title	06_SPIFLASH_SD
Size	Document Number
Date	CV181xC_QFN_SIP_DDR
Rev	1.0.0
Date	Monday, September 19, 2022
Sheet	6 of 10



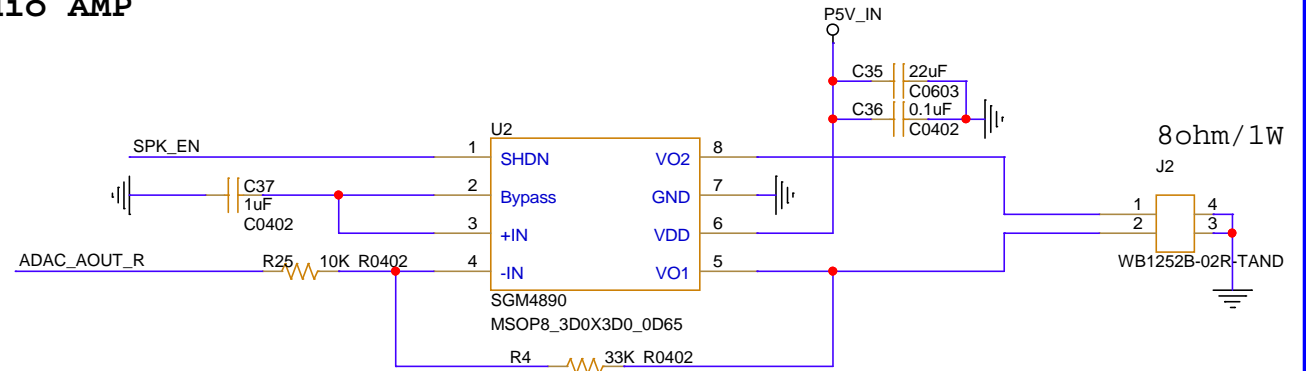


CVITEK			
Title		08_RJ45	
Size	Document Number		Rev
	CV181xC_QFN_SIP_DDR		1.0.0
Date:	Monday, September 19, 2022	Sheet	8 of 10

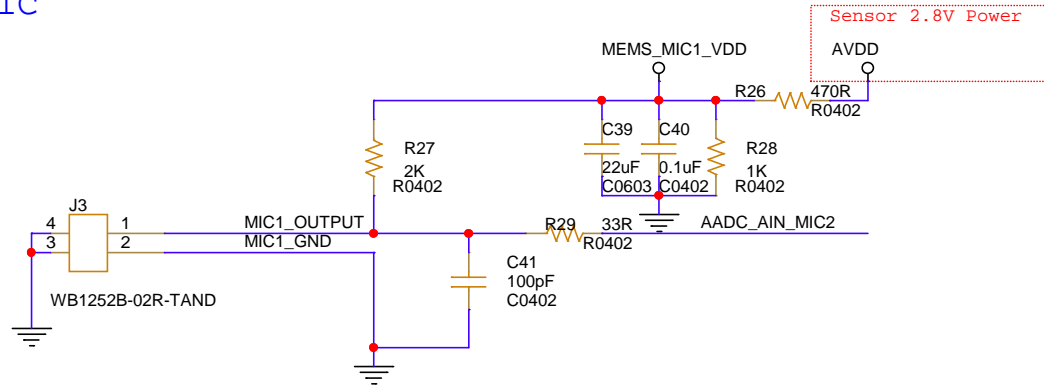
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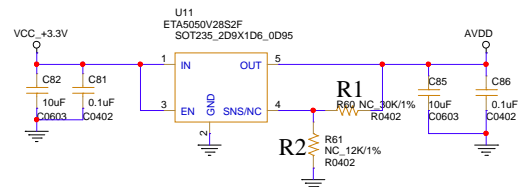
Audio AMP



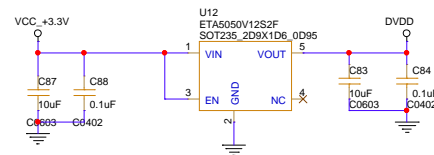
MIC



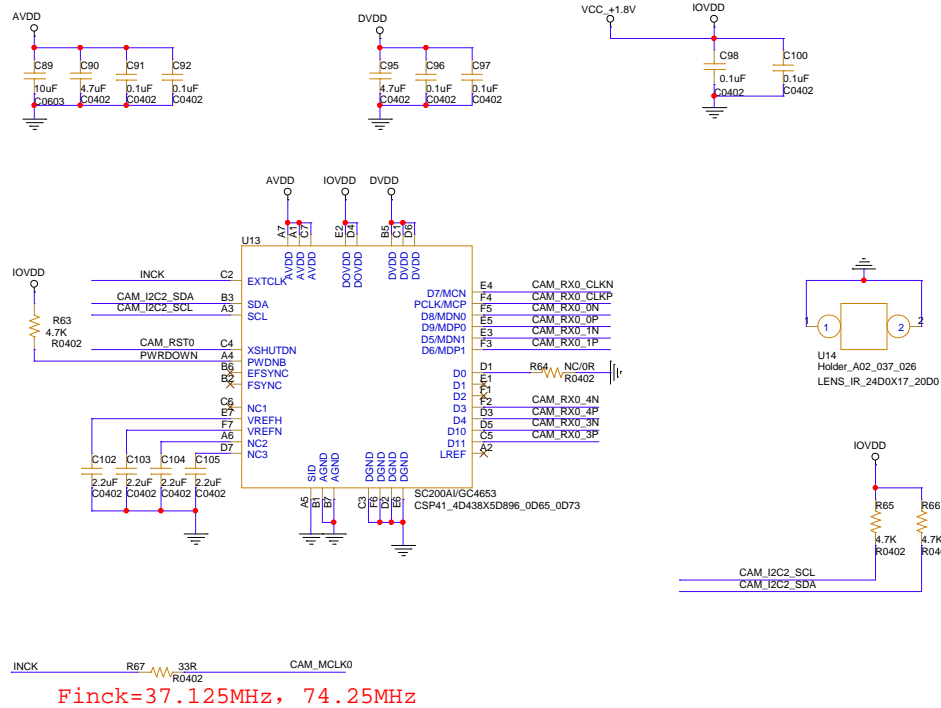
Title			09_AUDIO		
Size	Document Number				Rev
	CV181xC_QFN_SIP_DDR				1.0.0
Date:	Wednesday, March 01, 2023		Sheet	9	of 10



$$V_{out} = 0.8V * (R1/R2 + 1)$$



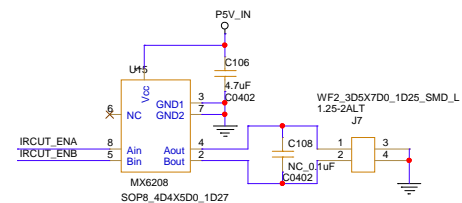
SENSOR



NET

CAM_RX0_4N [4]
 CAM_RX0_4P [4]
 CAM_RX0_3N [4]
 CAM_RX0_3P [4]
 CAM_RX0_CLKN [4]
 CAM_RX0_CLKP [4]
 CAM_RX0_1N [4]
 CAM_RX0_1P [4]
 CAM_RX0_0N [4]
 CAM_RX0_0P [4]
 CAM_MCLK0 [4]
 CAM_RST0 [4]
 CAM_I2C2_SCL [4]
 CAM_I2C2_SDA [4]
 IRCUT_ENA [4]
 IRCUT_ENB [4]

IR CUT



PAD	SC200AI	SC3335	SC500AI	GC2063	GC4653
A6	C104	NC	NC	2.2uF	NC
D1	R64	NC	NC	0R	NC
D7	C105	NC	NC	2.2uF	2.2uF
MIP1	2Lan	2Lan	4Lan	2Lan	2Lan



File			10_Sensor		
Size	Document Number	CV181xC_QFN_SIP_DDR			Rev
Date	Monday, September 19, 2022	Sheet	10	of	10